



# PJU4NA50A / PJD4NA50A / PJP4NA50A / PJF4NA50A

## 500V N-Channel MOSFET

**Voltage**

**500 V**

**Current**

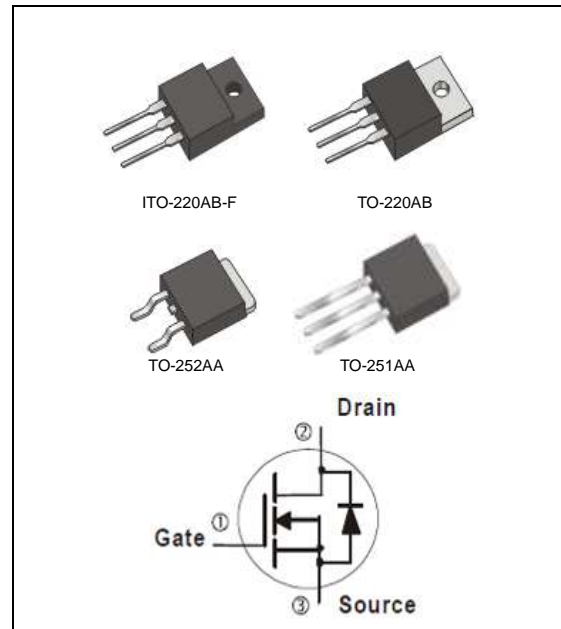
**4 A**

### Features

- $R_{DS(ON)}, V_{GS}@10V, I_D@2A < 2.3\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS2.0 (2011/65/EU & 2015/865/EU directive)
- Green molding compound as per IEC61249 Std. (Halogen Free)

### Mechanical Data

- Case : TO-251AA, TO-252AA, TO-220AB, ITO-220AB-F
- Terminals : Solderable per MIL-STD-750, Method 2026



## Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	TO-251AA	TO-220AB	ITO-220AB-F	TO-252AA	UNITS
Drain-Source Voltage		$V_{DS}$	500				V
Gate-Source Voltage		$V_{GS}$	±30				V
Continuous Drain Current		$I_D$	4				A
Pulsed Drain Current		$I_{DM}$	16				A
Single Pulse Avalanche Energy <sup>(Note 1)</sup>		$E_{AS}$	125				mJ
Power Dissipation	$T_C=25^\circ\text{C}$	$P_D$	78	90	42	78	W
	Derate above $25^\circ\text{C}$		0.62	0.72	0.34	0.62	W/°C
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150				°C
Typical Thermal Resistance							
- Junction to Case		$R_{\theta JC}$	1.6	1.39	3.0	1.6	°C/W
- Junction to Ambient		$R_{\theta JA}$	110	62.5	120	110	

- Limited only By Maximum Junction Temperature



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## Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	500	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	3.0	4	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=2A$	-	2.0	2.3	$\Omega$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=500V, V_{GS}=0V$	-	-	1.0	$\mu A$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 30V, V_{DS}=0V$	-	-	$\pm 100$	nA
Diode Forward Voltage	$V_{SD}$	$I_S=4A, V_{GS}=0V$	-	0.8	1.4	V
<b>Dynamic</b> (Note 4)						
Total Gate Charge	$Q_g$	$V_{DS}=400V, I_D=4A,$ $V_{GS}=10V$ (Note 2,3)	-	9.8	-	nC
Gate-Source Charge	$Q_{gs}$		-	2.2	-	
Gate-Drain Charge	$Q_{gd}$		-	3.9	-	
Input Capacitance	$C_{iss}$	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0\text{MHz}$	-	449	-	pF
Output Capacitance	$C_{oss}$		-	45	-	
Reverse Transfer Capacitance	$C_{rss}$		-	13	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=250V, I_D=4A,$ $R_G=10\Omega$ (Note 2,3)	-	12	-	ns
Turn-On Rise Time	$t_r$		-	11	-	
Turn-Off Delay Time	$t_{d(off)}$		-	31	-	
Turn-Off Fall Time	$t_f$		-	7.6	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_S$	---	-	-	4	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{SM}$	---	-	-	16	A
Reverse Recovery Time	$t_{rr}$	$V_{GS}=0V, I_S=4A$	-	402	-	ns
Reverse Recovery Charge	$Q_{rr}$	$di_F/dt=100A/\mu s$ (Note 2)	-	1.3	-	$\mu C$

**NOTES :**

1.  $L=10\text{mH}, I_{AS}=5A, V_{DD}=50V, R_G=25\text{ohm}$ , Starting  $T_J=25^\circ\text{C}$
2. Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$
3. Essentially independent of operating temperature typical characteristics.
4. Guaranteed by design, not subject to production testing



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## TYPICAL CHARACTERISTIC CURVES

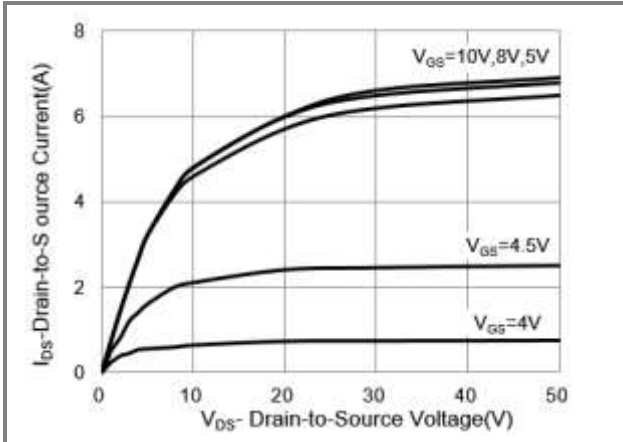


Fig.1 Output Characteristics

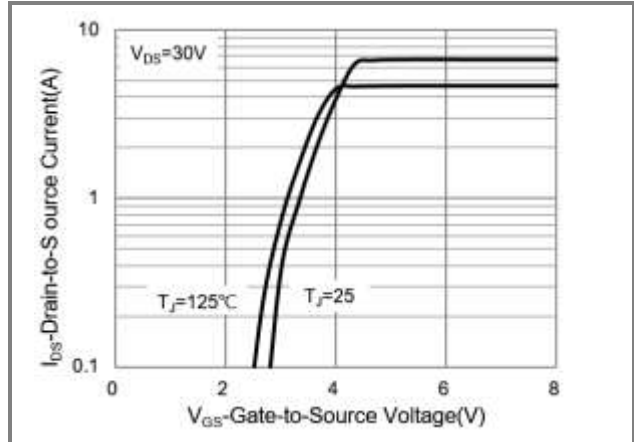


Fig.2 Transfer Characteristics

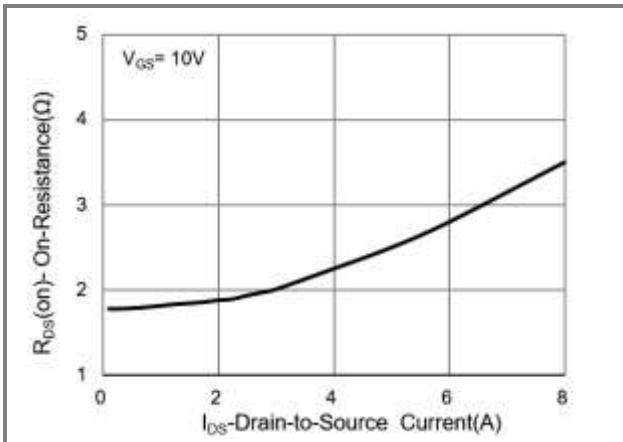


Fig.3 On-Resistance vs. Drain Current

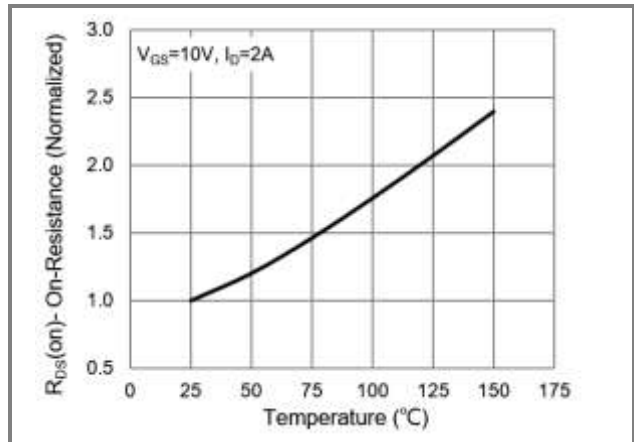


Fig.4 On-Resistance vs. Junction Temperature

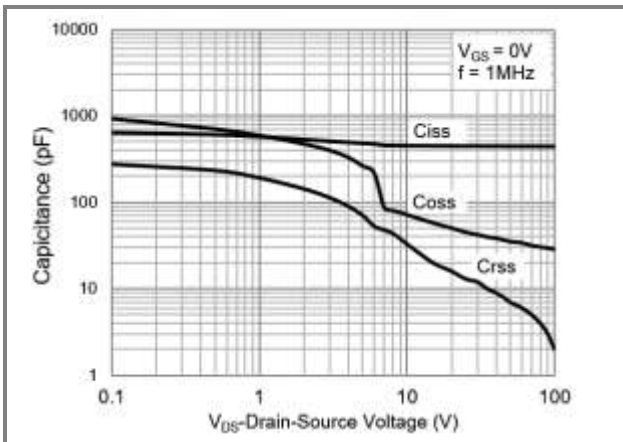


Fig.5 Capacitance vs. Drain-Source Voltage

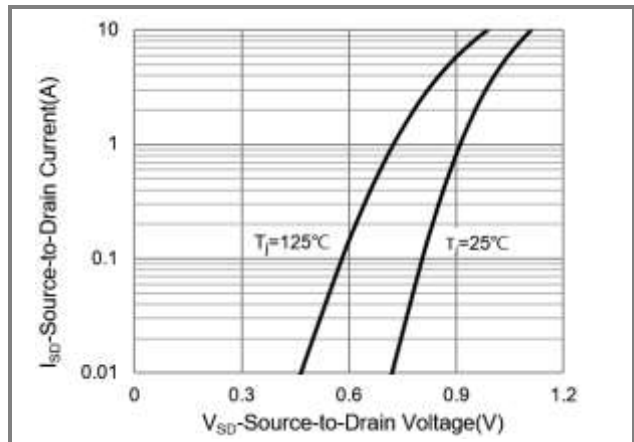


Fig.6 Source-Drain Diode Forward Voltage



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## TYPICAL CHARACTERISTIC CURVES

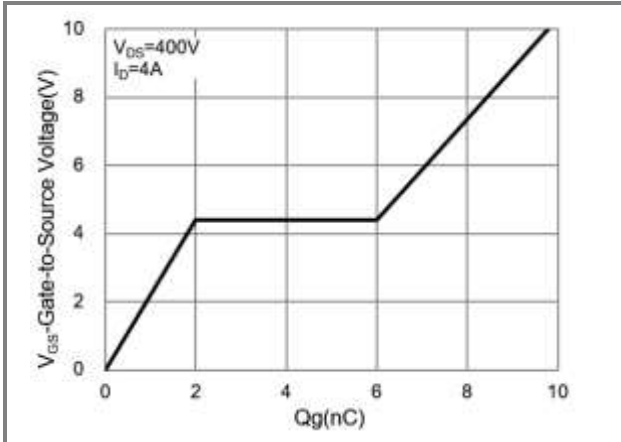


Fig.7 Gate Charge

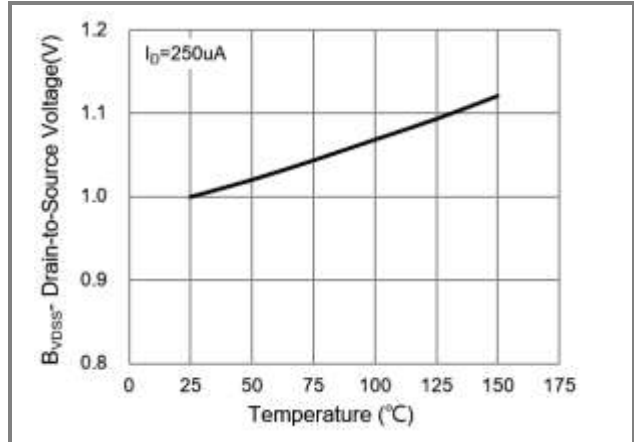


Fig.8  $B_{V_{DS}}$  vs. Junction Temperature

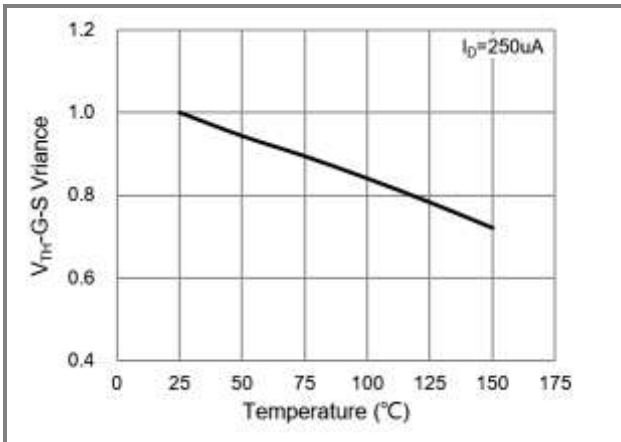


Fig.9 Threshold Voltage Variation with Temperature

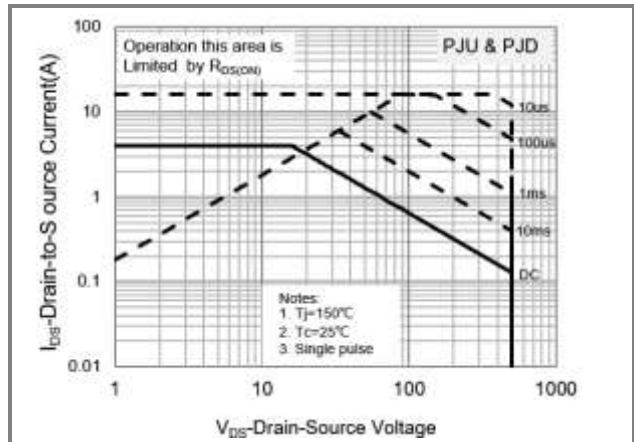


Fig.10 Maximum Safe Operating Area

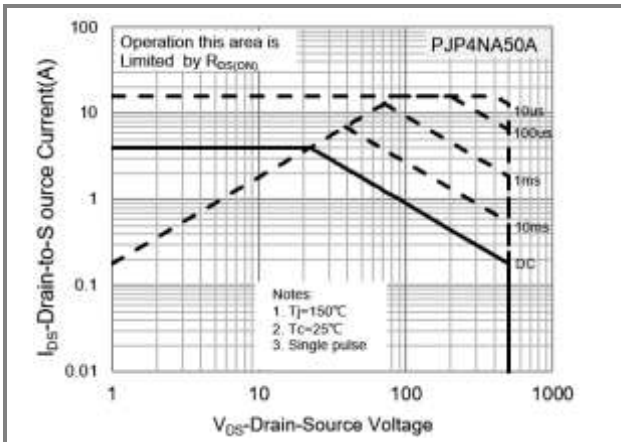


Fig.11 Maximum Safe Operating Area

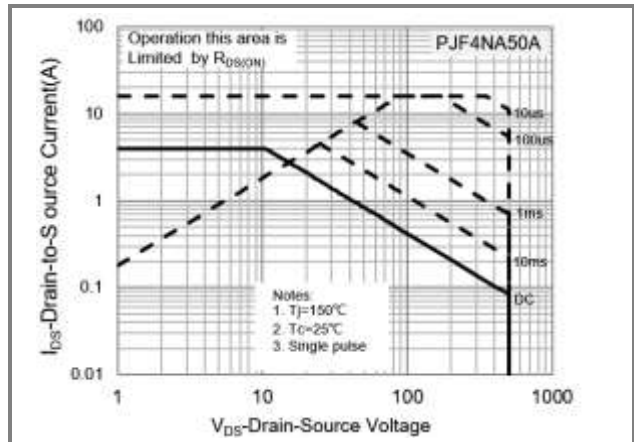


Fig.12 Maximum Safe Operating Area



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## TYPICAL CHARACTERISTIC CURVES

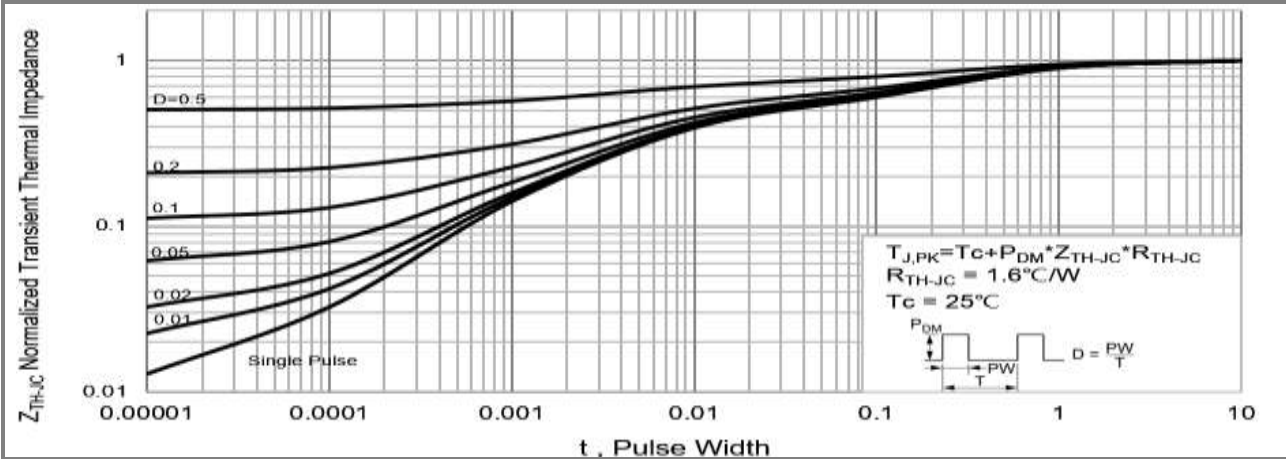


Fig.13 PJU/PJD Normalized Transient Thermal Impedance vs. Pulse Width

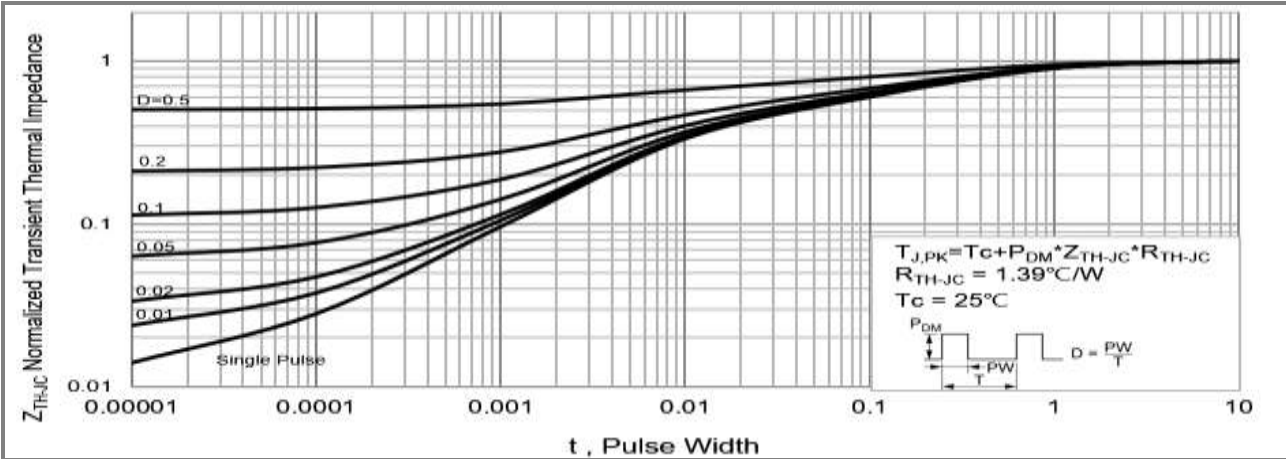


Fig.14 PJP Normalized Transient Thermal Impedance vs. Pulse Width

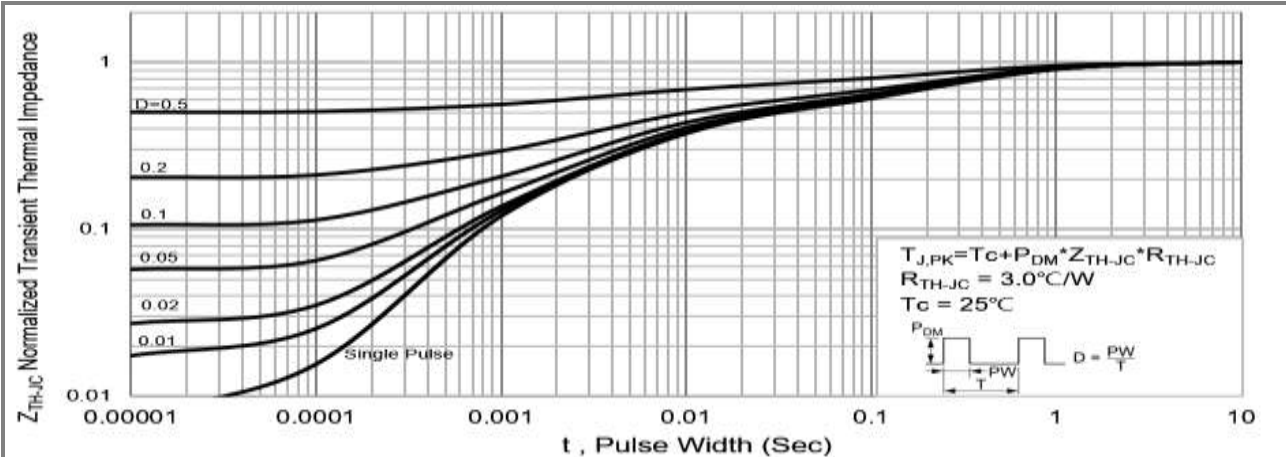
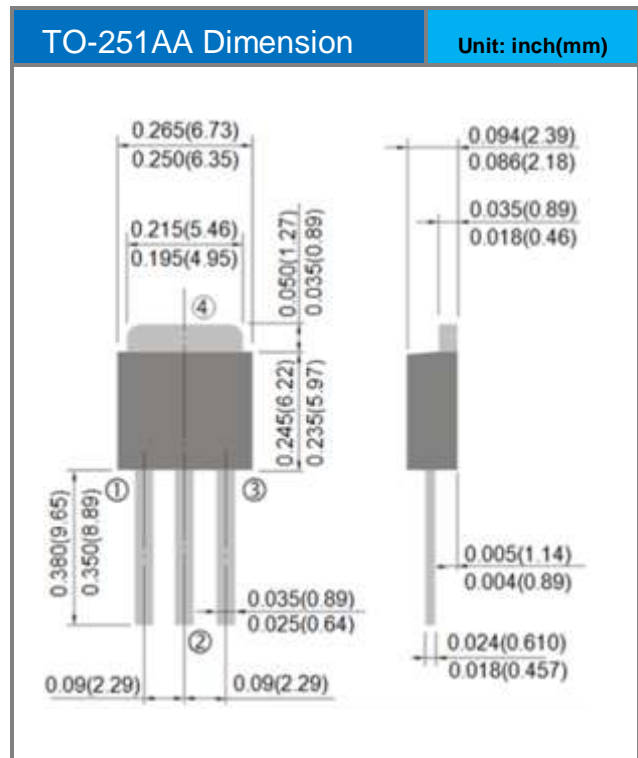
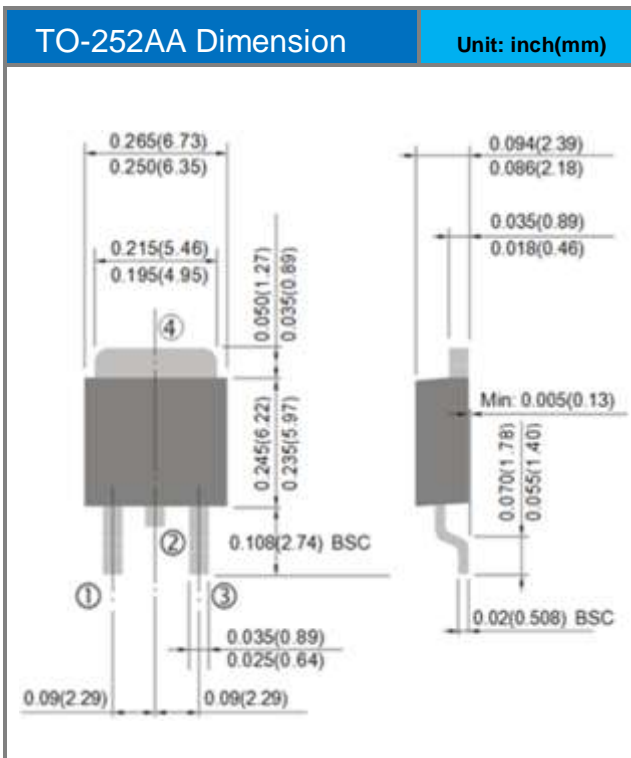
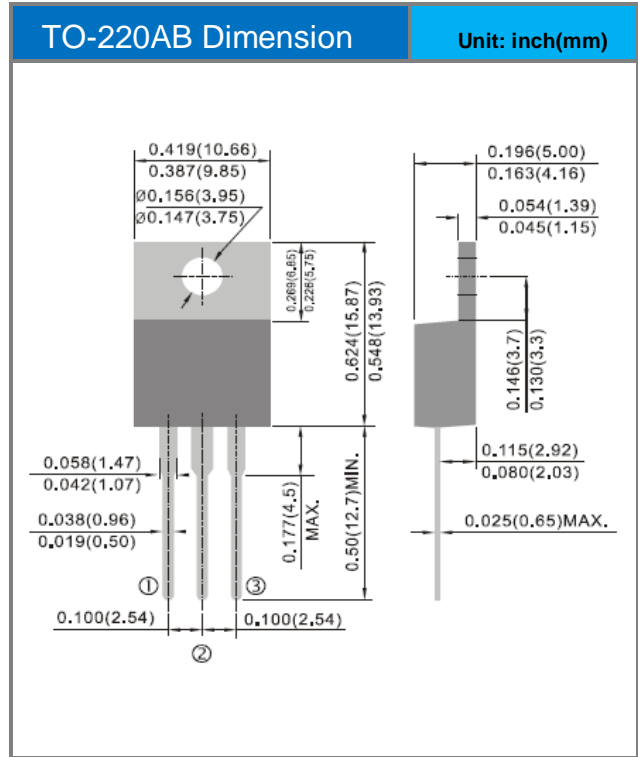
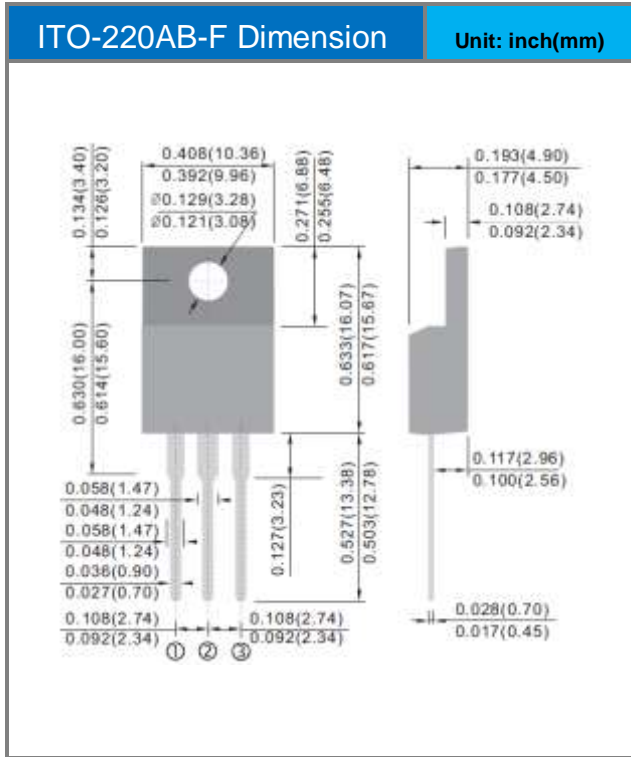


Fig.15 PJF Normalized Transient Thermal Impedance vs. Pulse Width



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## Packaging Information





## PJU4NA50A / PJD4NA50A / PJP4NA50A / PJF4NA50A

### PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJU4NA50A_T0_00001	TO-251AA	80pcs / Tube	U4NA50A	Halogen free
PJD4NA50A_L2_00001	TO-252AA	3,000pcs / 13" reel	D4NA50A	Halogen free
PJP4NA50A_T0_00001	TO-220AB	50pcs / Tube	P4NA50A	Halogen free
PJF4NA50A_T0_00001	ITO-220AB-F	50pcs / Tube	F4NA50A	Halogen free



## **PJU4NA50A / PJD4NA50A / PJP4NA50A / PJF4NA50A**

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