



Current

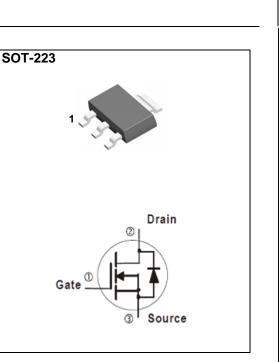
### Features

Voltage

- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@5A<42m\Omega$
- R<sub>DS(ON)</sub>, V<sub>GS</sub>@4.5V, I<sub>D</sub>@4A<51mΩ</li>
- Advanced Trench Process Technology •
- High density cell design for ultra low on-resistance •
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case : SOT-223 Package
- Terminals : Solderable per MIL-STD-750, Method 2026 •
- Approx. Weight : 0.043 ounces, 0.123 grams



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	40		
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20	V	
Continuous Drain Current (Note 4)	T <sub>A</sub> =25°C	- I <sub>D</sub>	6.5		
	T <sub>A</sub> =25°C T <sub>A</sub> =70°C		5	А	
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	26		
Power Dissipation	T <sub>A</sub> =25°C	PD	3.1	W	
	T <sub>A</sub> =70°C		2		
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C	
Typical Thermal Resistance		$R_{ extsf{ heta}JA}$			
- Junction to Ambient <sup>(Note 4,5)</sup>			40.3	°C/W	

Limited only By Maximum Junction Temperature



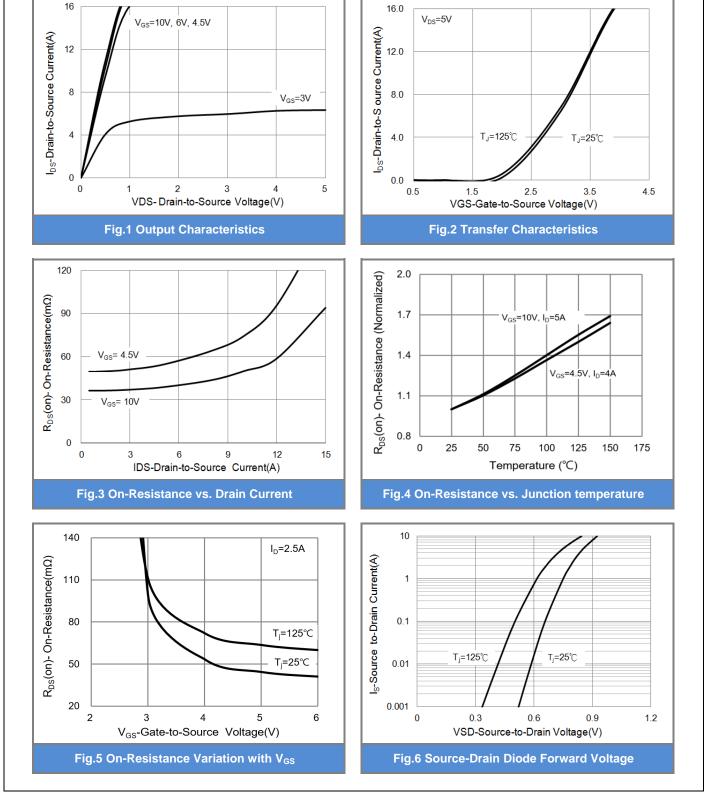
# **Electrical Characteristics** ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	40	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250$ uA	1	1.5	2.5	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	$V_{GS}$ =10V, I <sub>D</sub> =5A	-	35	42	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	-	44	51	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	-	-	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 20V, V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 6)						
Total Gate Charge	Qg	V <sub>DS</sub> =20V, I <sub>D</sub> =4.3A, V <sub>GS</sub> =4.5V <sup>(Note 1,2)</sup>	-	4.8	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.4	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	1.8	-	
Input Capacitance	Ciss	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1MHZ	-	410	-	pF
Output Capacitance	Coss		-	50	-	
Reverse Transfer Capacitance	Crss		-	30	-	
Turn-On Delay Time	td <sub>(on)</sub>		-	4	-	ns
Turn-On Rise Time	tr	$V_{DD}=20V, I_{D}=3.5A,$ $V_{GS}=10V,$	-	30	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	15	-	
Turn-Off Fall Time	t <sub>f</sub>	$R_{G}=1\Omega^{(Note 1,2)}$	-	8	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	I <sub>S</sub>		-	-	6.5	А
Diode Forward Current	15					
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =1A, V <sub>GS</sub> =0V	-	0.78	1.2	V

NOTES :

- 1. Pulse width
- 2. Essentially independent of operating temperature typical characteristics.
- Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
- 4. The maximum current rating is package limited.
- 5.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.

April 1,2019-REV.00

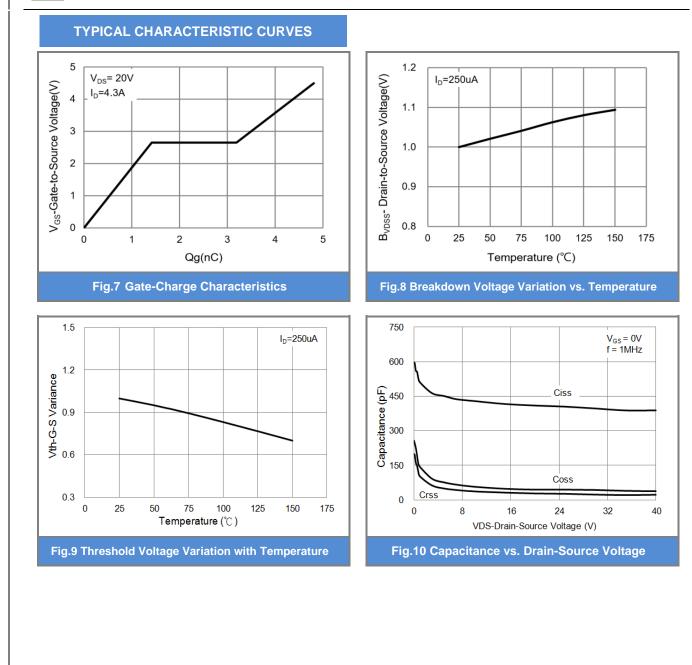


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**PJW7N04** 

**TYPICAL CHARACTERISTIC CURVES** 







# PJW7N04

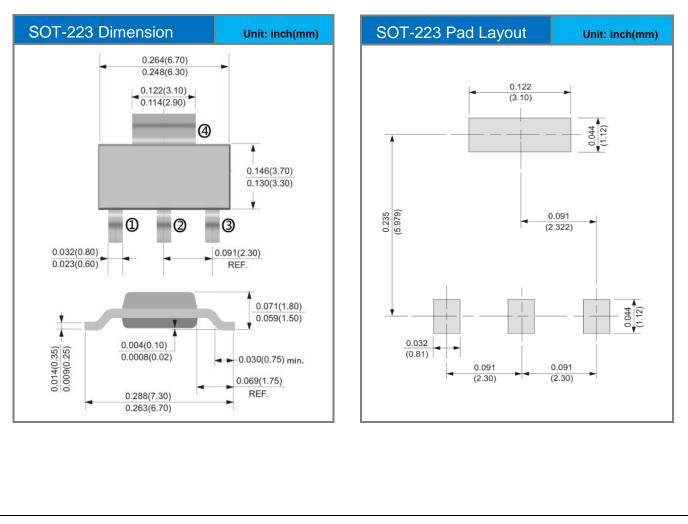


# PJW7N04

#### Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version	
PJW7N04_R2_00001	SOT-223	2,500pcs / 13" reel	W7N04	Halogen free	

# Packaging Information & Mounting Pad Layout





# PJW7N04

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