

30V P-Channel Enhancement Mode MOSFET

Current

-2.6A

Features

Voltage

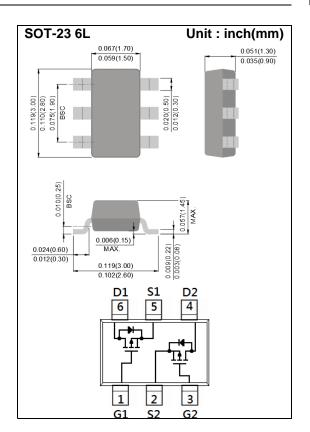
• RDS(ON), VGS@-10V, ID@-2.6A<115mΩ

-30 V

- RDS(ON) , VGS@-4.5V, ID@-1.7A<150mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: SOT-23 6L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.014 grams
- Marking: ST9



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMET	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-30	V
Gate-Source Voltage		V _{GS}	<u>+</u> 20	V
Continuous Drain Current		lь	-2.6	А
Pulsed Drain Current		I _{DM}	-10.4	А
Power Dissipation	T _a =25°C	PD	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	٥C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		R _{θJA}	100	°C/W



Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-30	-	-	V
Gate Threshold Voltage	$V_{\text{GS(th)}}$	V _{DS} =V _{GS} , I _D =-250uA	-1	-1.31	-2.1	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-2.6A	-	93	115	mΩ
		V _{GS} =-4.5V, I _D =-1.7A	-	116	150	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-30V, V _{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	lgss	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Qg	V _{DS} =-15V, I _D =-2.6A, V _{GS} =-10V ^(Note 1,2)	-	9.8	-	nC
Gate-Source Charge	Q_{gs}		-	1.5	-	
Gate-Drain Charge	Q_{gd}		-	2.2	-	
Input Capacitance	Ciss		-	396	-	pF
Output Capacitance	Coss	V _{DS} =-15V, V _{GS} =0V,	-	47	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	36	-	
Switching						
Turn-On Delay Time	td _(on)		-	5	-	ns
Turn-On Rise Time	tr	V _{DD} =-15V, I _D =-2.6A, V _{GS} =-10V,	-	30	-	
Turn-Off Delay Time	td _(off)		-	25	-	
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	8	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	1-		-	-	-1.5	A
Diode Forward Current	ls					
Diode Forward Voltage	V _{SD}	Is=-1.0A, V _{GS} =0V	-	-0.77	-1.2	V

NOTES :

1. Pulse width <300us, Duty cycle <2%

2. Essentially independent of operating temperature typical characteristics.

- 3. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited



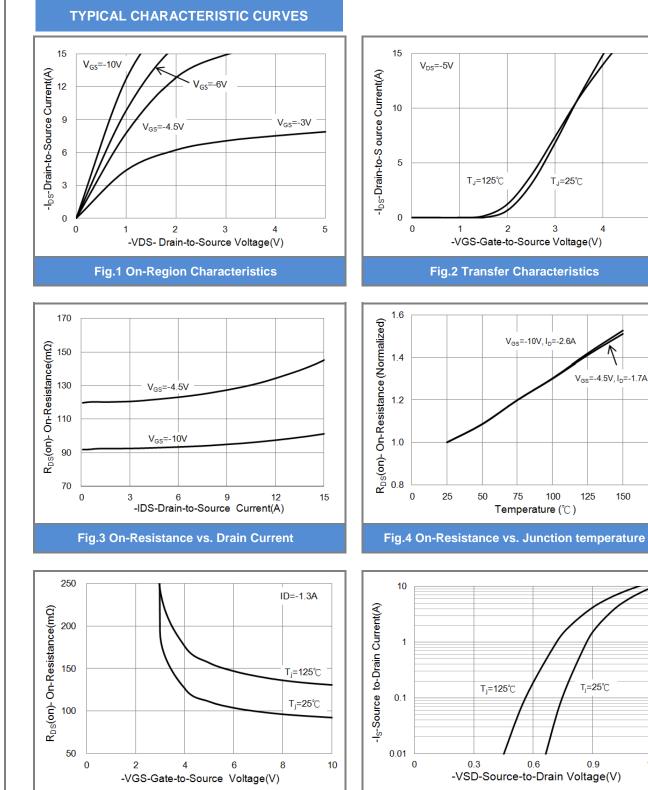


Fig.5 On-Resistance Variation with VGS.

1.2

Fig.6 Body Diode Characteristics

4

150

175

5



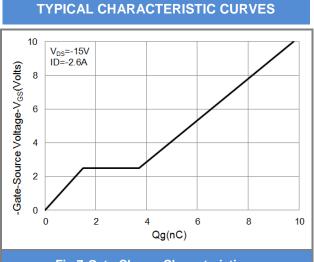


Fig.7 Gate-Charge Characteristics

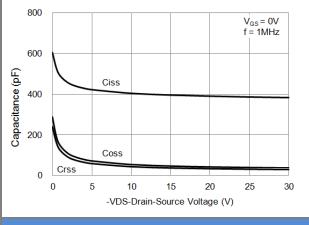
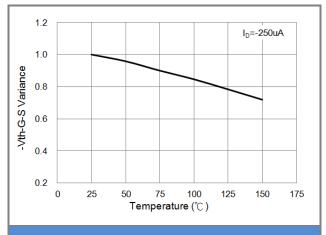


Fig.9 Capacitance vs. Drain-Source Voltage



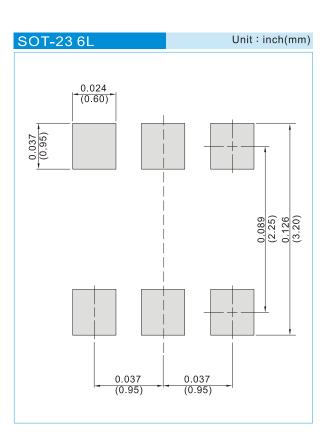




PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6809_S1_00001	SOT-23 6L	3K pcs / 7" reel	ST9	Halogen free RoHS compliant
PJS6809_S2_00001	SOT-23 6L	10K pcs / 13" reel	ST9	Halogen free RoHS compliant

MOUNTING PAD LAYOUT







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