

# PJS6806

## 30V N-Channel Enhancement Mode MOSFET

**Voltage**

**30 V**

**Current**

**4A**

### Features

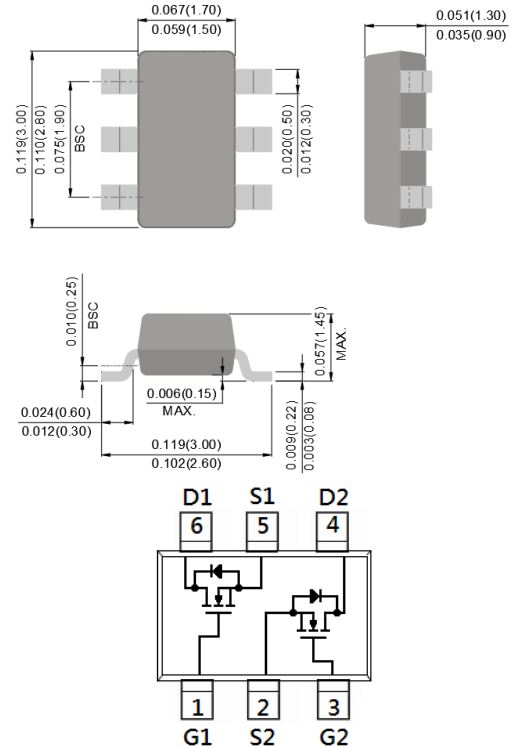
- $R_{DS(ON)}$  ,  $V_{GS}@10V$  ,  $I_D@4.0A < 48m\Omega$
- $R_{DS(ON)}$  ,  $V_{GS}@4.5V$  ,  $I_D@2.8A < 70m\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case: SOT-23 6L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.014 grams
- Marking: ST6

### SOT-23 6L

Unit : inch(mm)



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	30	V
Gate-Source Voltage		V <sub>GS</sub>	±20	V
Continuous Drain Current		I <sub>D</sub>	4	A
Pulsed Drain Current		I <sub>DM</sub>	16	A
Power Dissipation	T <sub>a</sub> =25°C	P <sub>D</sub>	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C
Typical Thermal Resistance		R <sub>θJA</sub>	100	°C/W
- Junction to Ambient <sup>(Note 3)</sup>				

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## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.0	1.37	2.1	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =4A	-	34	48	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =2.8A	-	50	70	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	-	0.01	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	±10	±100	nA
Dynamic						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =4A, V <sub>GS</sub> =10V(Note 1,2)	-	5.8	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	1	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1.0MHZ	-	235	-	pF
Output Capacitance	C <sub>oss</sub>		-	36	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	24	-	
Switching						
Turn-On Delay Time	td <sub>(on)</sub>	V <sub>DD</sub> =15V, I <sub>D</sub> =4A, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω(Note 1,2)	-	2.5	-	ns
Turn-On Rise Time	tr		-	39	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	23	-	
Turn-Off Fall Time	tf		-	28	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>s</sub>	---	-	-	1.5	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1.0A, V <sub>GS</sub> =0V	-	0.75	1.2	V

### NOTES :

1. Pulse width ≤ 300us, Duty cycle ≤ 2%
2. Essentially independent of operating temperature typical characteristics.
3. R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited

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## TYPICAL CHARACTERISTIC CURVES

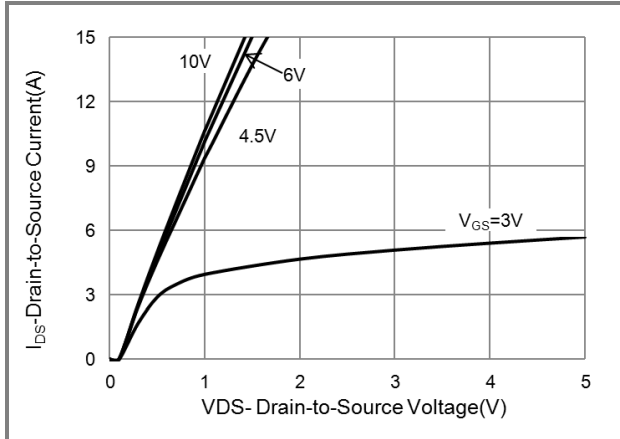


Fig.1 On-Region Characteristics

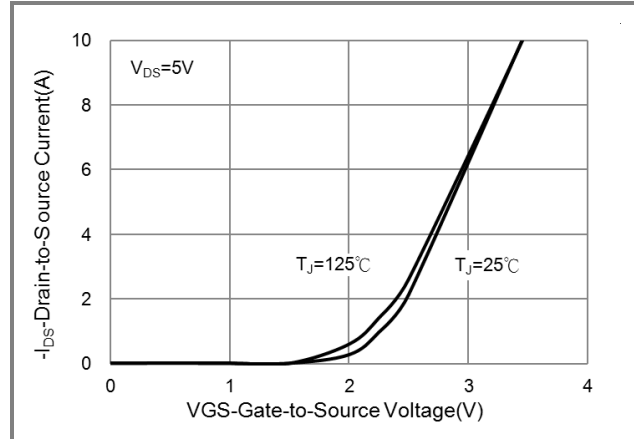


Fig.2 Transfer Characteristics

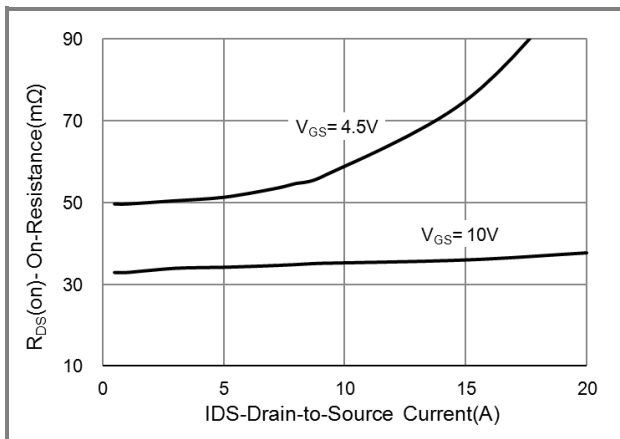


Fig.3 On-Resistance vs. Drain Current

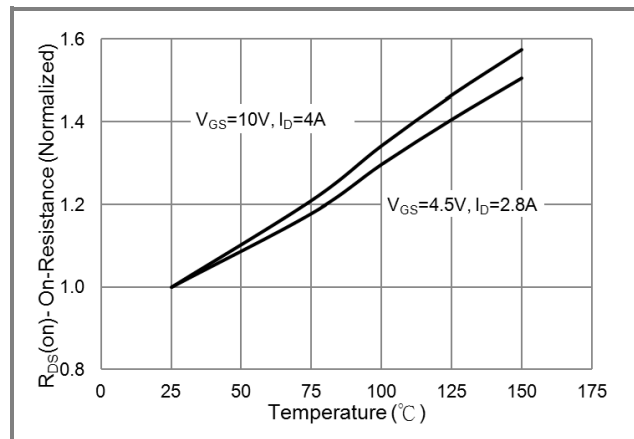


Fig.4 On-Resistance vs. Junction temperature

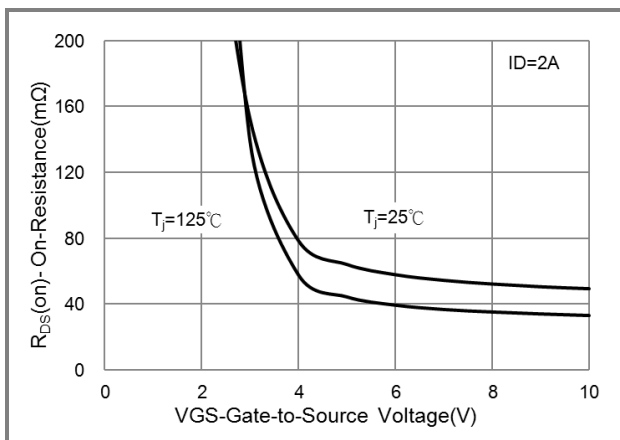


Fig.5 On-Resistance Variation with  $V_{GS}$ .

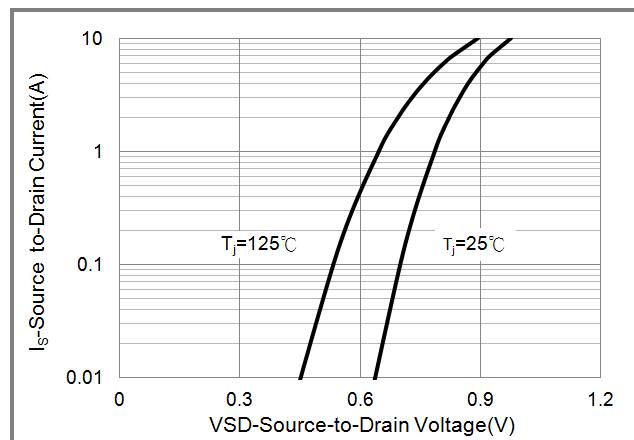


Fig.6 Body Diode Characteristics

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## TYPICAL CHARACTERISTIC CURVES

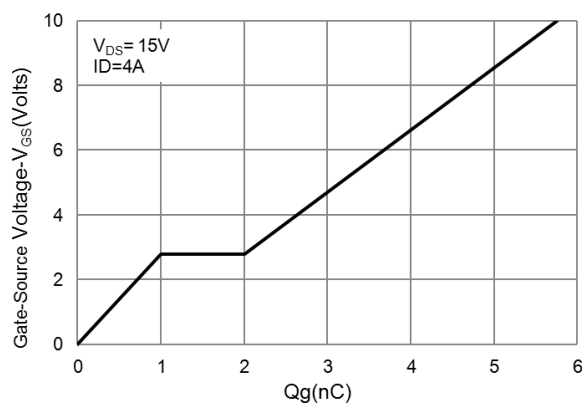


Fig.7 Gate-Charge Characteristics

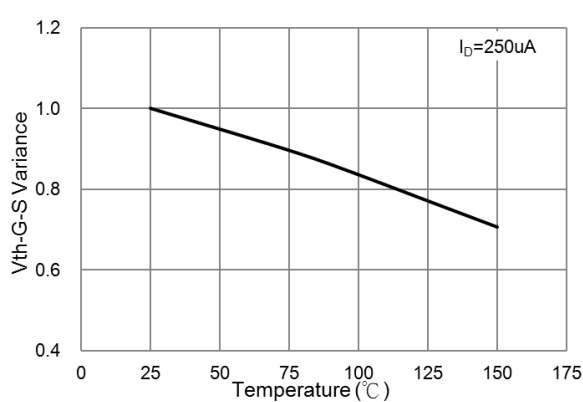


Fig.8 Threshold Voltage Variation with Temperature

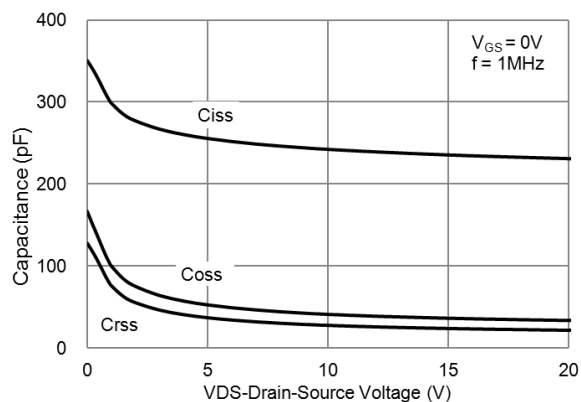


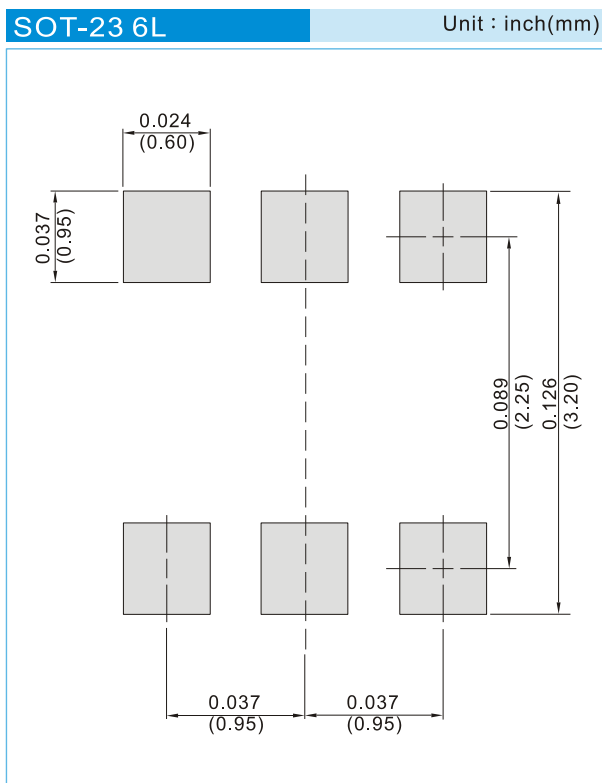
Fig.9 Capacitance vs. Drain-Source Voltage.

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## Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJS6806	SOT-23 6L	3K pcs / 7" reel	ST6
PJS6806	SOT-23 6L	10K pcs / 13" reel	ST6

## Mounting Pad Layout



## PJS6806

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