



### **60V N-Channel Enhancement Mode MOSFET**

Voltage

60 V

Current

40 A

#### **Features**

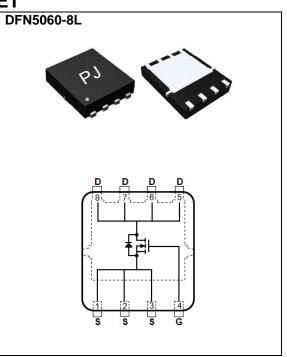
- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@15A<21m\Omega$
- R<sub>DS(ON)</sub>, V<sub>GS</sub>@4.5V, I<sub>D</sub>@8A<24mΩ
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

• Case: DFN5060-8L Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0028 ounces, 0.08 grams



## **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		$V_{DS}$	60		
Gate-Source Voltage		$V_{GS}$	<u>+</u> 20	V	
Continuous Drain Current (Note 4)	T <sub>C</sub> =25°C	,	40	А	
	T <sub>C</sub> =100°C	I <sub>D</sub>	25		
Pulsed Drain Current (Note 1)	T <sub>C</sub> =25°C	I <sub>DM</sub>	160		
Power Dissipation	T <sub>C</sub> =25°C	Po	100	W	
	T <sub>C</sub> =100°C		50		
Continuous Drain Current (Note 4)	T <sub>A</sub> =25°C	I <sub>D</sub>	6.5	А	
	T <sub>A</sub> =70°C		5		
Power Dissipation	T <sub>A</sub> =25°C	Po	2.4	147	
	T <sub>A</sub> =70°C		1.6	W	
Single Pulse Avalanche Energy (Note 6)		E <sub>AS</sub>	42	mJ	
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~175	°C	
Typical Thermal Resistance (Note 4,5)	Junction to Case	$R_{ heta JC}$	1.5	°C/W	
	Junction to Ambient	$R_{\theta JA}$	62.5		

Limited only By Maximum Junction Temperature





### **Electrical Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$		1	1.73	2.5	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	$V_{GS}$ =10V, $I_D$ =15A	-	18	21	mΩ
		$V_{GS}$ =4.5V, $I_D$ =8A	-	21	24	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	-	-	1	uA
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	<u>+</u> 100	nA
Dynamic (Note 7)						
Total Gate Charge	$Q_g$	V <sub>DS</sub> =30V, I <sub>D</sub> =15A, V <sub>GS</sub> =10V <sup>(Note 2,3)</sup>	-	28	-	nC
Gate-Source Charge	$Q_gs$		-	3.5	-	
Gate-Drain Charge	$Q_{gd}$		-	6.5	-	
Input Capacitance	Ciss	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1MHZ	-	1680	-	pF
Output Capacitance	Coss		-	115	-	
Reverse Transfer Capacitance	Crss		-	85	-	
Turn-On Delay Time	td <sub>(on)</sub>		-	7.2	-	
Turn-On Rise Time	t <sub>r</sub>	$\begin{array}{c} V_{DD}{=}30V,\ I_{D}{=}1A,\\ V_{GS}{=}10V,\ R_{G}{=}6\Omega\\ \text{(Note 2,3)} \end{array}$	-	38	-	ns
Turn-Off Delay Time	td <sub>(off)</sub>		-	34	-	
Turn-Off Fall Time	t <sub>f</sub>		-	8.2	-	
Drain-Source Diode						
Maximum Continuous Drain-Source			-	-	40	А
Diode Forward Current	I <sub>S</sub>					
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =1A, V <sub>GS</sub> =0V	-	0.7	1	V

#### NOTES:

- 1. Pulse width<300us, Duty cycle<2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}$ =150°C. Ratings are based on low frequency and duty cycles to keep initial  $T_J$  =25°C.
- 4. The maximum current rating is package limited.
- 5. Roja is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 6. The test condition is L=0.1mH,  $I_{AS}$ =29A,  $V_{DD}$ =25V,  $V_{GS}$ =10V.
- 7. Guaranteed by design, not subject to production testing.





#### TYPICAL CHARACTERISTIC CURVES

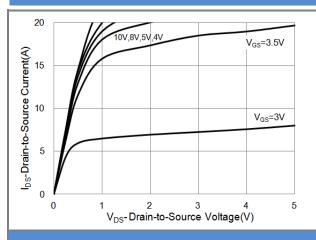
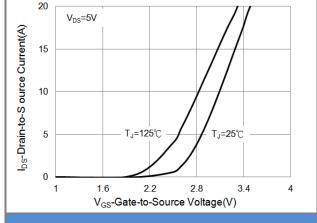


Fig.1 Output Characteristics



**Fig.2 Transfer Characteristics** 

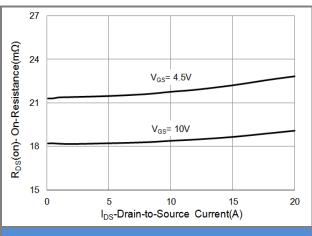


Fig.3 On-Resistance vs. Drain Current

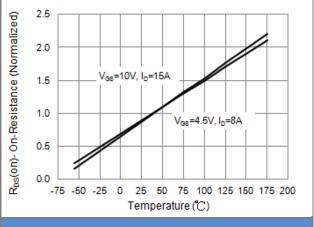
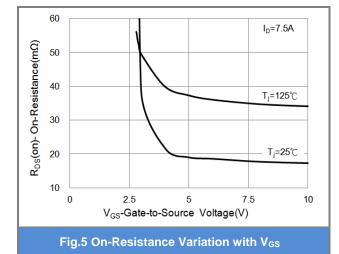


Fig.4 On-Resistance vs. Junction temperature



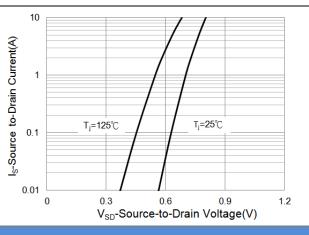


Fig.6 Source-Drain Diode Forward Voltage





#### TYPICAL CHARACTERISTIC CURVES

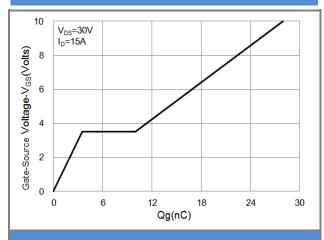


Fig.7 Gate-Charge Characteristics

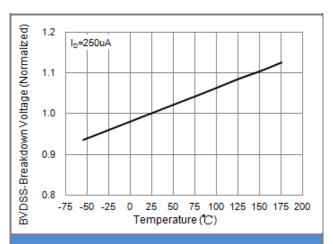


Fig.8 Breakdown Voltage Variation vs. Temperature

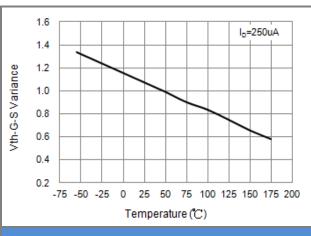


Fig.9 Threshold Voltage Variation with Temperature

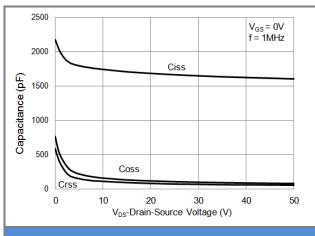


Fig.10 Capacitance vs. Drain-Source Voltage

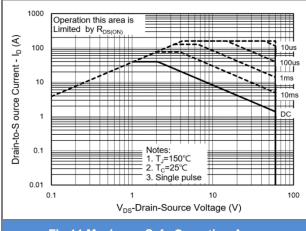


Fig.11 Maximum Safe Operating Area

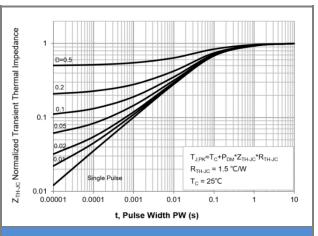


Fig.12 Normalized Transient Thermal Impedance





#### **TYPICAL CHARACTERISTIC CURVES**

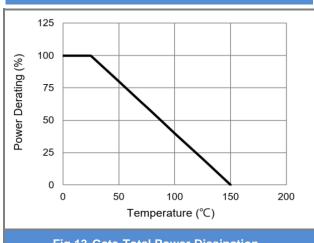


Fig.13 Gate-Total Power Dissipation

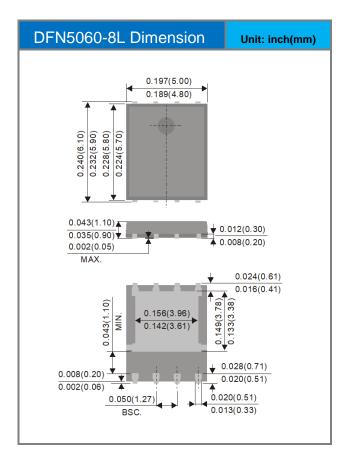


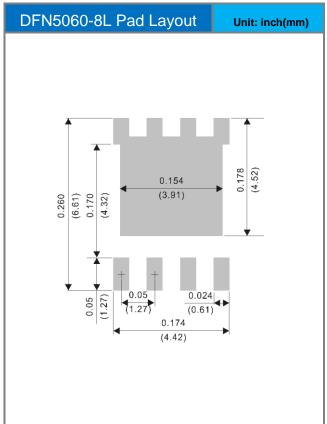


### **Part No Packing Code Version**

Part No Packing Code	Package Type	Packing Type	Marking	Version	
PJQ5466A-AU_R2_000A1	DFN5060-8L	3000pcs / 13" reel	Q5466A	Halogen free	

### **Packaging Information & Mounting Pad Layout**









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