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	SEMI CONDUCTOR

4 G2

G1

## PJL9836A

#### 60V Dual N-Channel Enhancement Mode MOSFET

Current

6 A

## Features

Voltage

•  $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@5.5A<21m\Omega$ 

60 V

- $R_{DS(ON)}$ ,  $V_{GS}@4.5V$ ,  $I_D@3A<24m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case : SOP-8 package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0029 ounces, 0.083 grams



PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	60	
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20	V
Continuous Drain Current	T <sub>A</sub> =25°C		6	
	T <sub>A</sub> =70°C	I <sub>D</sub>	5	A
Pulsed Drain Current <sup>(Note 1,3)</sup>		I <sub>DM</sub>	22	
Power Dissipation	T <sub>A</sub> =25°C	_	1.7	
	T <sub>A</sub> =70°C	P <sub>D</sub>	1.1	W
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	°C
Typical Thermal Resistance - Junction to Ambient <sup>(Note 5)</sup>		R <sub>θJA</sub>	73.5	°C/W

SOP-8



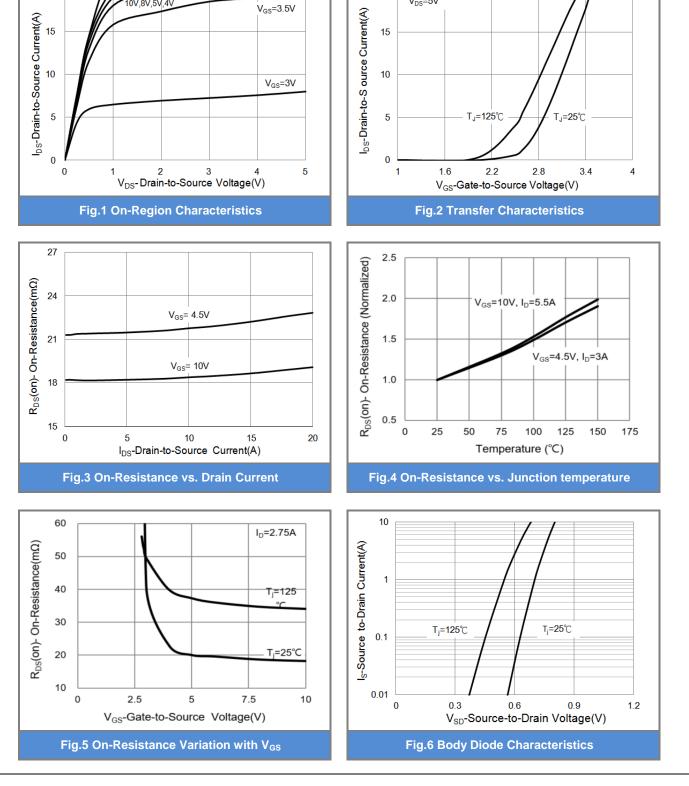
## **Electrical Characteristics** ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	$BV_{DSS}$	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60	-	-	- V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_{D}=250uA$	1.0	1.73	2.5	
Drain-Source On-State Resistance	$R_{DS(on)}$	V <sub>GS</sub> =10V, I <sub>D</sub> =5.5A	-	18	21	mΩ
Drain-Source On-State Resistance	$R_{DS(on)}$	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3.0A	-	21	24	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	-	-	1.0	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 20V, V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 6)				•	-	-
Total Gate Charge	$Q_g$	$V_{DS}$ =30V, I <sub>D</sub> =5.5A, $V_{GS}$ =10V <sup>(Note 1,2)</sup>	-	28	-	_
Gate-Source Charge	$Q_{gs}$		-	3.5	-	nC
Gate-Drain Charge	$Q_gd$		-	6.5	-	
Input Capacitance	Ciss	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1.0MHZ	-	1680	-	_
Output Capacitance	Coss		-	115	-	pF
Reverse Transfer Capacitance	Crss		-	85	-	
Turn-On Delay Time	td <sub>(on)</sub>	$V_{DD}$ =30V, $I_{D}$ =1A, $V_{GS}$ =10V, $R_{G}$ =6 $\Omega$ (Note 1,2)	-	7.2	-	_
Turn-On Rise Time	tr		-	38	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	34	-	ns
Turn-Off Fall Time	tf		-	8.2	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	I <sub>S</sub>		-	-	6	А
Diode Forward Current Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V	-	0.7	1	v

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150$  °C. Ratings are based on low frequency and duty cycles to keep initial  $T_J = 25$  °C.
- 5. R<sub>®JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.

March 31,2017-REV.00



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V<sub>DS</sub>=5V

# **PJL9836A**

10V,8V,5V,4V

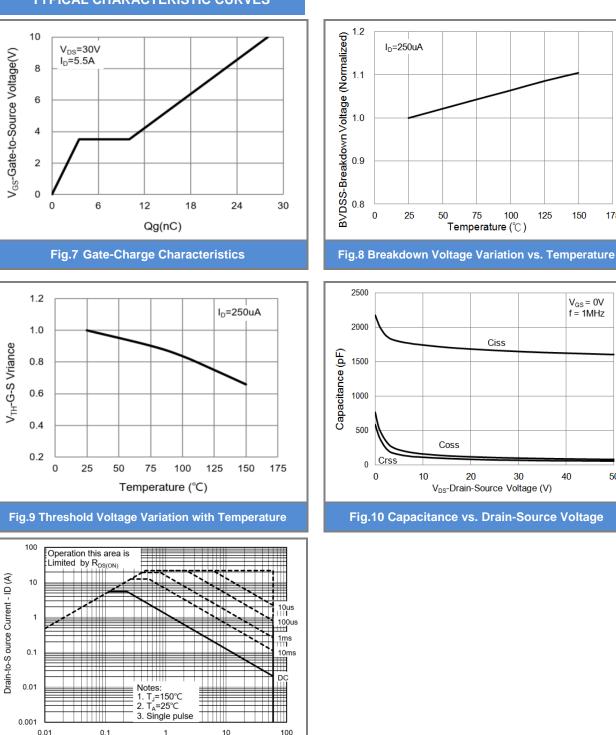
**TYPICAL CHARACTERISTIC CURVES** 

20



V<sub>DS</sub>-Drain-Source Voltage (V)

Fig.11 Maximum Safe Operating Area







**PJL9836A** 



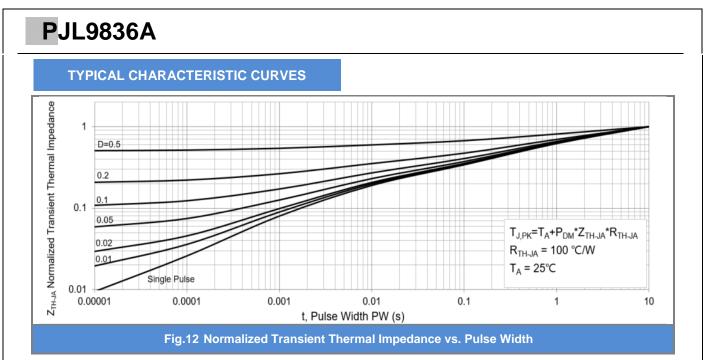
150

V<sub>GS</sub> = 0V f = 1MHz

40

50

175



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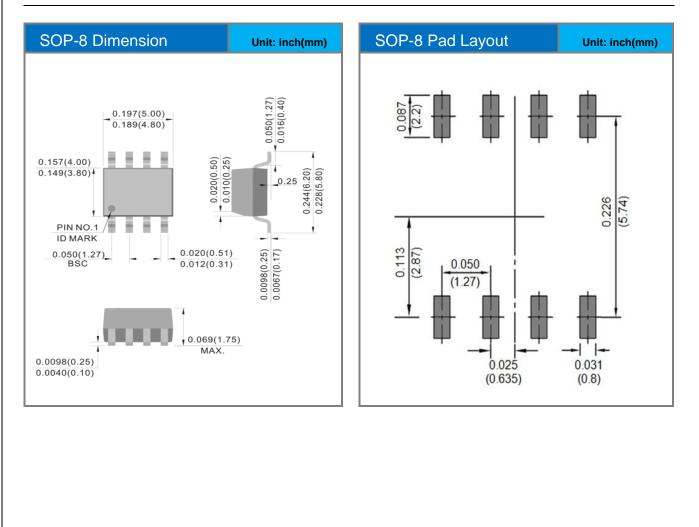


## PJL9836A

#### Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJL9836A_R2_00001	SOP-8	2.5K pcs / 13" reel	L9836A	Halogen free

#### Packaging Information & Mounting Pad Layout





## **PJL9836A**

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