



30V N-Channel Enhancement Mode MOSFET

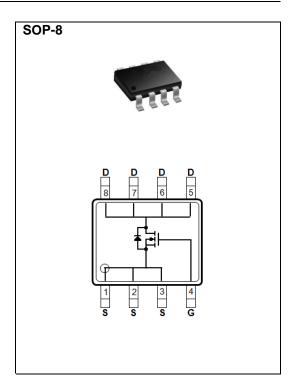
Voltage 30 V Current 16 A

Features

- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@10A<4.2m\Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_{D}@5A<6m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: SOP-8 package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0029 ounces, 0.083 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

| PARAMET | SYMBOL | LIMIT | UNITS | | |
|--|----------------------|-----------------|-------------|------|--|
| Drain-Source Voltage | | V_{DS} | 30 | .,, | |
| Gate-Source Voltage | | V_{GS} | <u>+</u> 20 | V | |
| Continuous Drain Current | T _A =25°C | | 16 | | |
| | T _A =70°C | l _D | 13 | А | |
| Pulsed Drain Current (Note 1) | | I _{DM} | 64 | | |
| Power Dissipation | T _A =25°C | | 2.1 | W | |
| | T _A =70°C | P_{D} | 1.3 | | |
| Operating Junction and Storage Temperature Range | | T_J, T_{STG} | -55~150 | °C | |
| Typical Thermal Resistance | | | | | |
| - Junction to Ambient (Note 5) | | $R_{\theta JA}$ | 59.5 | °C/W | |





Electrical Characteristics (T_A=25 °C unless otherwise noted)

| PARAMETER | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNITS |
|----------------------------------|---------------------|---|------|------|--------------|-------|
| Static | | | _ | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V, I _D =250uA | 30 | - | - | - V |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}=V_{GS}$, $I_{D}=250uA$ | 1 | 1.6 | 2.5 | |
| Drain-Source On-State Resistance | R _{DS(on)} | V _{GS} =10V, I _D =10A - | 3.5 | 4.2 | | |
| Drain-Source On-State Resistance | R _{DS(on)} | V _{GS} =4.5V, I _D =5A | - | 5.2 | 6 | mΩ |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =30V, V _{GS} =0V | - | - | 1 | uA |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} = <u>+</u> 20V, V _{DS} =0V | - | - | <u>+</u> 100 | nA |
| Dynamic (Note 6) | | | _ | | | |
| Total Gate Charge | Q_g | V _{DS} =15V, I _D =24A, V _{GS} =4.5V ^(Note 2,3) | - | 23 | - | nC |
| Gate-Source Charge | Q_gs | | - | 8 | - | |
| Gate-Drain Charge | Q_{gd} | | - | 9 | - | |
| Input Capacitance | Ciss | V _{DS} =25V, V _{GS} =0V, f=1.0MHZ | - | 2436 | - | pF |
| Output Capacitance | Coss | | - | 306 | - | |
| Reverse Transfer Capacitance | Crss | | - | 196 | - | |
| Turn-On Delay Time | td _(on) | \/ 45\/ 45\ | - | 32 | - | ns |
| Turn-On Rise Time | tr | V_{DS} =15V, I_{D} =15A, V_{GS} =10V, R_{G} =1 Ω (Note 2,3) | - | 169 | - | |
| Turn-Off Delay Time | td _(off) | | - | 232 | - | |
| Turn-Off Fall Time | tf | | - | 170 | - | |
| Drain-Source Diode | | , | | | | |
| Maximum Continuous Drain-Source | | | - | - | 16 | А |
| Diode Forward Current | I _S | | | | | |
| Diode Forward Voltage | V_{SD} | I _S =1A, V _{GS} =0V | - | 0.66 | 1 | V |

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 5. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

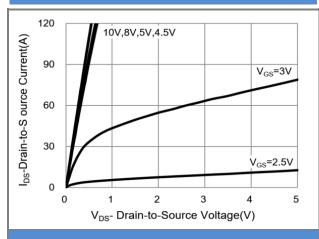


Fig.1 On-Region Characteristics

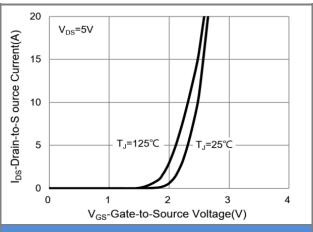


Fig.2 Transfer Characteristics

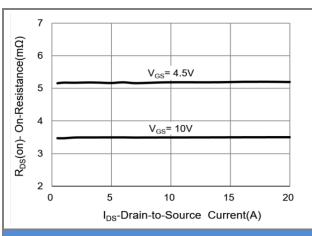


Fig.3 On-Resistance vs. Drain Current

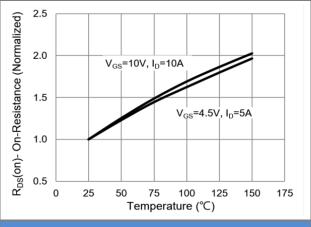


Fig.4 On-Resistance vs. Junction temperature

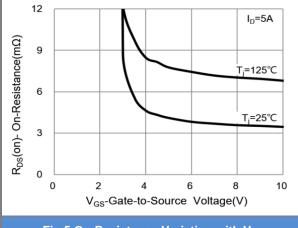


Fig.5 On-Resistance Variation with V_{GS}

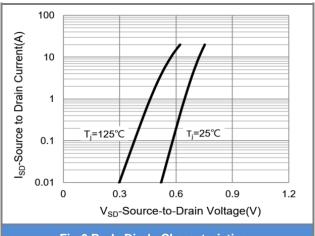


Fig.6 Body Diode Characteristics





TYPICAL CHARACTERISTIC CURVES

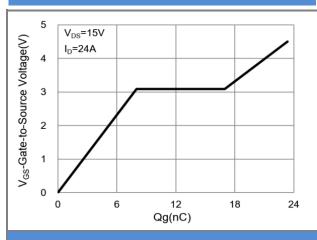


Fig.7 Gate-Charge Characteristics

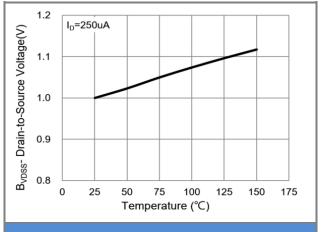


Fig.8 Breakdown Voltage Variation vs. Temperature

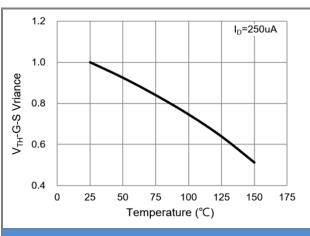


Fig.9 Threshold Voltage Variation with Temperature

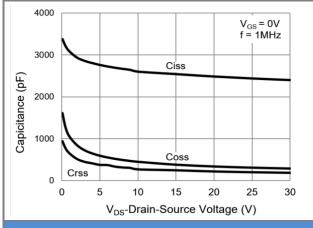


Fig.10 Capacitance vs. Drain-Source Voltage

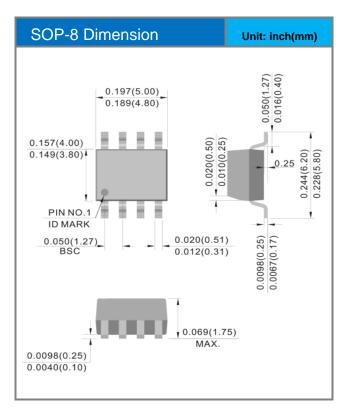


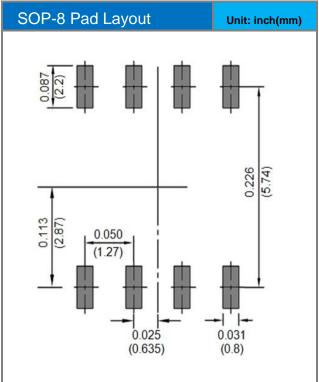


Part No Packing Code Version

| Part No Packing Code | Package Type | Packing Type | Marking | Version |
|----------------------|--------------|---------------------|---------|--------------|
| PJL9416_R2_00001 | SOP-8 | 2.5K pcs / 13" reel | L9416 | Halogen free |

Packaging Information & Mounting Pad Layout









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