

20V N-Channel Enhancement Mode MOSFET

Voltage 20 V Current 1.1A

Features

- RDS(ON) , VGS@4.5V, ID@1.1A<88mΩ
- RDS(ON) , VGS@2.5V, ID@0.7A<100mΩ
- RDS(ON) , VGS@1.8V, ID@0.5A<130m Ω
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

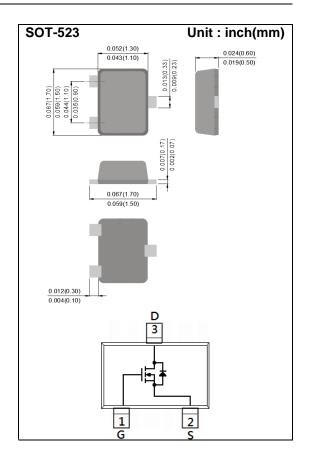
Mechanical Data

• Case: SOT-523 Package

Terminals: Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.002 grams

Marking : E00



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 12	V
Continuous Drain Current		ΙD	1.1	Α
Pulsed Drain Current		I _{DM}	4.4	А
Power Dissipation	T _a =25°C		300	mW
	Derate above 25°C	P _D	2.4	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		R _{θJA}	417	°C/W



Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =250uA	0.4	0.64	1.2	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =1.1A	-	76	88	mΩ	
		V _{GS} =2.5V, I _D =0.7A	-	85	100		
		V _{GS} =1.8V, I _D =0.5A	-	100	130		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V	-	0.01	1	uA	
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA	
Dynamic							
Total Gate Charge	Q_g	101/1	-	4.6	-	nC	
Gate-Source Charge	Qgs	V _{DS} =10V, I _D =1.1A,	-	0.8	-		
Gate-Drain Charge	Q_{gd}	V _{GS} =4.5V ^(Note 1,2)	-	1	-		
Input Capacitance	Ciss	101/11/101/	-	350	-	pF	
Output Capacitance	Coss	V _{DS} =10V, V _{GS} =0V,	-	40	-		
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	29	-		
Switching							
Turn-On Delay Time	td _(on)	101/1	-	3.4	-		
Turn-On Rise Time	tr	V_{DD} =10V, I_{D} =1.1A, V_{GS} =4.5V, R_{G} =6 $\Omega^{(Note 1,2)}$	-	47	-		
Turn-Off Delay Time	td _(off)		-	18	-	ns	
Turn-Off Fall Time	tf	RG=612(Note 1,2)	-	10	-		
Drain-Source Diode							
Maximum Continuous Drain-Source Diode Forward Current	Is		-	-	0.4	А	
Diode Forward Voltage	V _{SD}	Is=1A, V _{GS} =0V	-	0.74	1.2	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited



TYPICAL CHARACTERISTIC CURVES

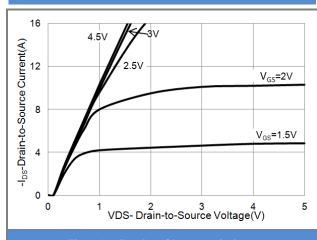


Fig.1 On-Region Characteristics

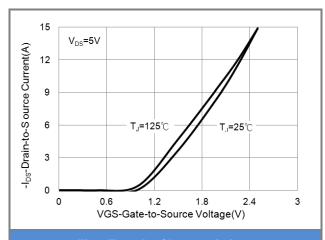


Fig.2 Transfer Characteristics

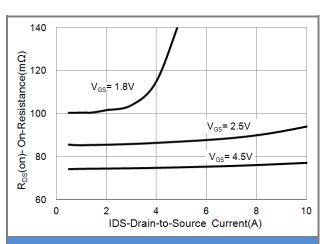


Fig.3 On-Resistance vs. Drain Current

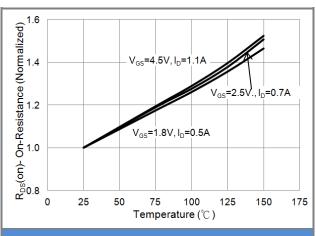
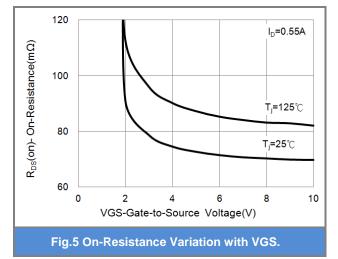


Fig.4 On-Resistance vs. Junction temperature



(Y) T_j=125°C T_j=25°C T_j=25°C O.01 O.03 O.6 O.9 1.2 VSD-Source-to-Drain Voltage(V)

Fig.6 Body Diode Characteristics

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TYPICAL CHARACTERISTIC CURVES

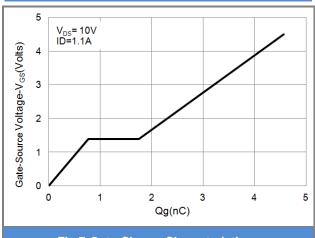


Fig.7 Gate-Charge Characteristics

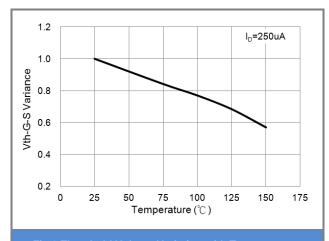


Fig.8 Threshold Voltage Variation with Temperature

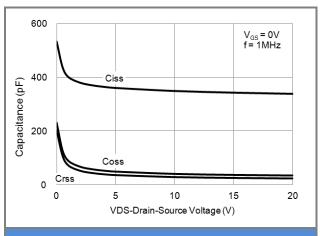


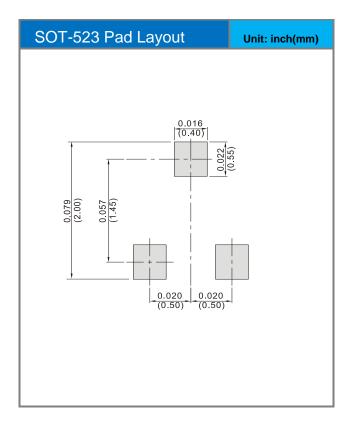
Fig.9 Capacitance vs. Drain-Source Voltage



Product and Packing Information

Part No.	Package Type	Packing Type	Marking	
PJE8400	SOT-523	4K pcs / 7" reel	E00	

Mounting Pad Layout





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