ΡΛΝ	JIT
	SEMI
	CONDUCTOR

PJA3416AE 20V N-Channel Enhancement Mode MOSFET – ESD Protected SOT-23 Unit: inch(mm) 20 V 6.5 A Current Voltage 0.006(0.15)MIN 0.120(3.04) Features 0.110(2.80) RDS(ON), VGS@4.5V, ID@6.5A<22mΩ 0.103(2.60) 0.086(2.20) RDS(ON), VGS@2.5V, ID@5.5A<26mΩ 0.056(1.40) RDS(ON), VGS@1.8V, ID@5.0A<34mΩ 0.047(1.20) Advanced Trench Process Technology 0.079(2.00) 0.008(0.20) • Specially Designed for Switch Load, PWM Application, etc. 0.070(1.80) 0.003(0.08) ESD Protected 2KV HBM Lead free in compliance with EU RoHS 2011/65/EU directive 0.004(0.10) 0.044(1.10) 0.000(0.00) 0.035(0.90) • Green molding compound as per IEC61249 Std. 0.020(0.50) (Halogen Free) 0.013(0.35) D Mechanical Data • Case : SOT-23 Package Terminals : Solderable per MIL-STD-750, Method 2026 • Approx. Weight : 0.0003 ounces, 0.0084 grams

Marking : A6E

Maximum Ratings and Thermal Characteristics (T_A=25^oC unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 8	V
Continuous Drain Current		Ι _D	6.5	А
Pulsed Drain Current ^(Note 4)		I _{DM}	32	А
Power Dissipation	T _a =25°C	P _D	1.25	W
	Derate above 25°C		10	mW/ºC
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	٥C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		R _{0JA}	100	°C/W

2

1 G



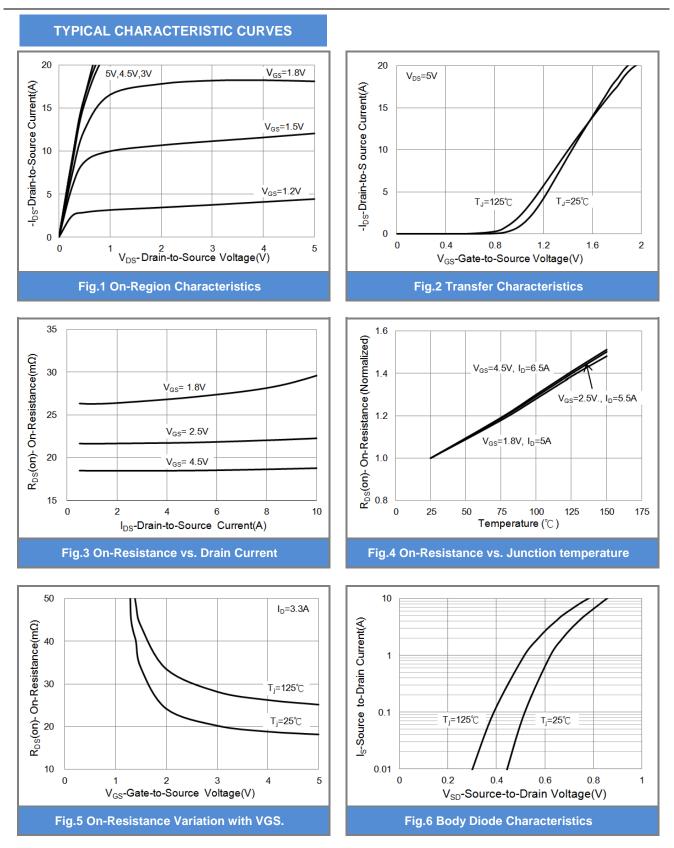
Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS} =0V, I _D =250uA	20	-	-	V	
Gate Threshold Voltage	$V_{\text{GS(th)}}$	V _{DS} =V _{GS} , I _D =250uA	0.4	0.58	1.0	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =6.5A	-	18.4	22	mΩ	
		V _{GS} =2.5V, I _D =5.5A	-	21.5	26		
		V _{GS} =1.8V, I _D =5.0A	-	26.4	34		
Zero Gate Voltage Drain Current	IDSS	V _{DS} =20V, V _{GS} =0V	-	-	1	uA	
Gate-Source Leakage Current	lgss	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	-	<u>+</u> 10	uA	
Dynamic							
Total Gate Charge	Q_{g}		-	8.6	-	nC	
Gate-Source Charge	Q_{gs}	V _{DS} =10V, I _D =6.5A, V _{GS} =4.5V ^(Note 1,2)	-	1.06	-		
Gate-Drain Charge	Q_gd	VGS=4.5V((100 ()=)	-	1.04	-		
Input Capacitance	Ciss		-	836	-		
Output Capacitance	Coss	V _{DS} =10V, V _{GS} =0V, f=1.0MHZ	-	96	-	pF	
Reverse Transfer Capacitance	Crss		-	80	-		
Switching							
Turn-On Delay Time	td _(on)		-	24	-		
Turn-On Rise Time	tr	$V_{DD}=10V, I_D=1A,$	-	46	-	ns	
Turn-Off Delay Time	td _(off)	$V_{GS}=4.5V$,	-	0.22	-		
Turn-Off Fall Time	tf	R _G =3Ω ^(Note 1,2)	-	0.30	-	us	
Drain-Source Diode							
Maximum Continuous Drain-Source					1.5	А	
Diode Forward Current	ls		-	-	C.1	A	
Diode Forward Voltage	V _{SD}	Is=1.0A, V _{GS} =0V	-	0.74	1.0	V	

NOTES :

- 1. Pulse width<300us, Duty cycle<2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.







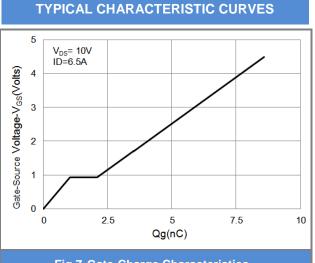


Fig.7 Gate-Charge Characteristics

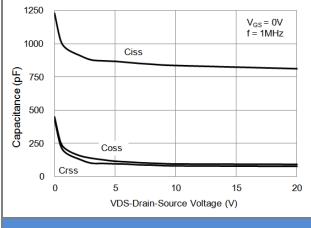
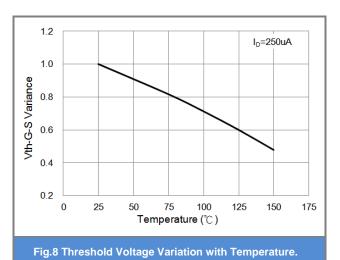


Fig.9 Capacitance vs. Drain-Source Voltage.

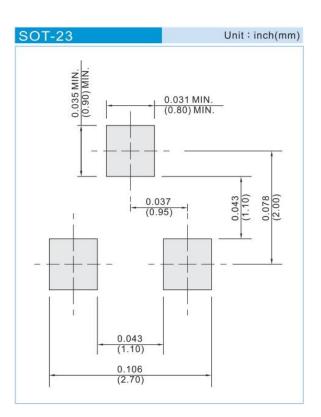




Product and Packing Information

Part No.	Package Type Packing Type		Marking
PJA3416AE	SOT-23	3K pcs / 7" reel	A6E

Mounting Pad Layout





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