

PJQ24817AW-AU

80V P-Channel Enhancement Mode MOSFET

Voltage

-80 V

Current

-5 A

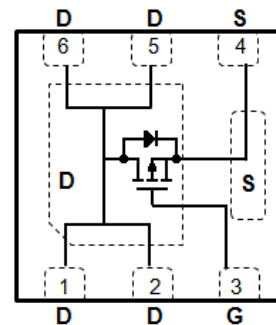
Features

- $R_{DS(ON)}$, $V_{GS}@-10V$, $I_D@-1.8A < 360m\Omega$
- $R_{DS(ON)}$, $V_{GS}@-6V$, $I_D@-1.5A < 400m\Omega$
- Excellent FOM
- Logic Level Drive
- Wettable flank terminals
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : DFN2020BW-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0096 grams

DFN2020BW-6L



Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	-80	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ^(Note 3)	$T_C=25^{\circ}C$	I_D	-5	A
	$T_C=100^{\circ}C$		-3.5	
Pulsed Drain Current ^(Note 1)	$T_C=25^{\circ}C$	I_{DM}	-10	
Power Dissipation	$T_C=25^{\circ}C$	P_D	20	W
	$T_C=100^{\circ}C$		10	
Continuous Drain Current ^(Note 4)	$T_A=25^{\circ}C$	I_D	-1.8	A
	$T_A=70^{\circ}C$		-1.5	
Power Dissipation	$T_A=25^{\circ}C$	P_D	2.3	W
	$T_A=70^{\circ}C$		1.6	
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~175	$^{\circ}C$
Thermal Resistance ^(Note 4)	Junction to Case	$R_{\theta JC}$	7.2	$^{\circ}C/W$
	Junction to Ambient	$R_{\theta JA}$	65	

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Electrical Characteristics (T_A=25°C unless otherwise noted)

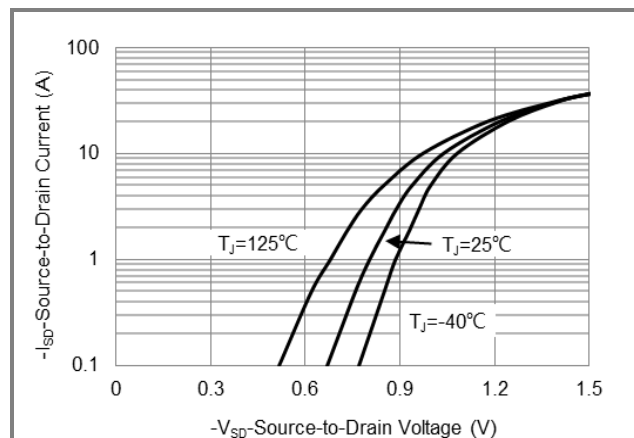
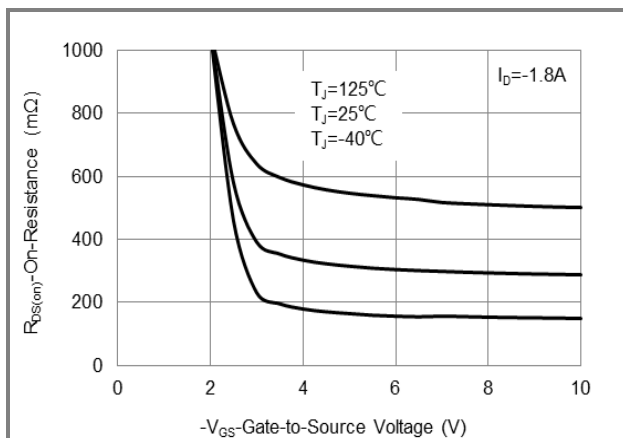
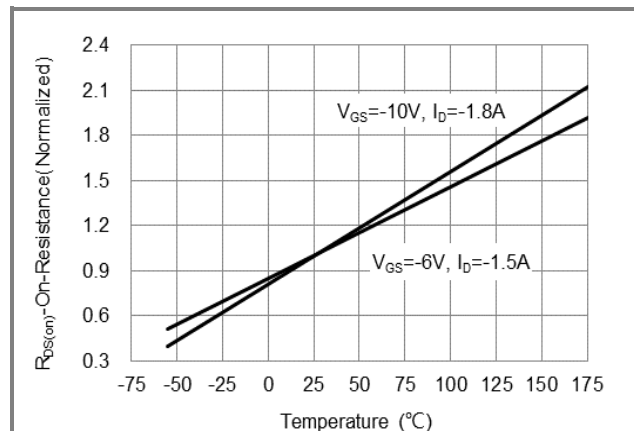
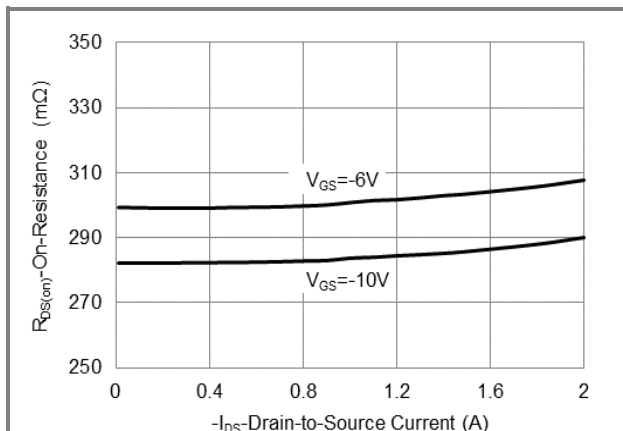
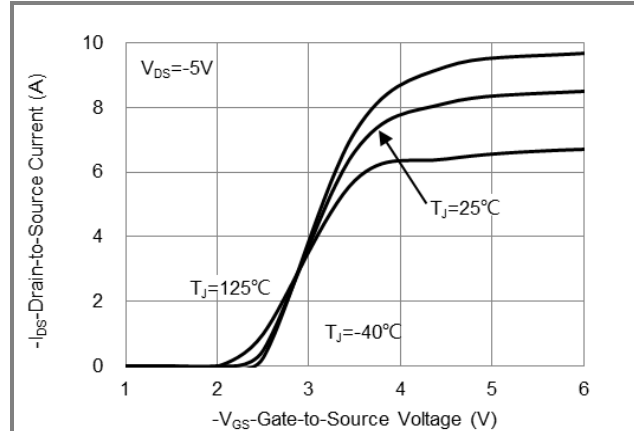
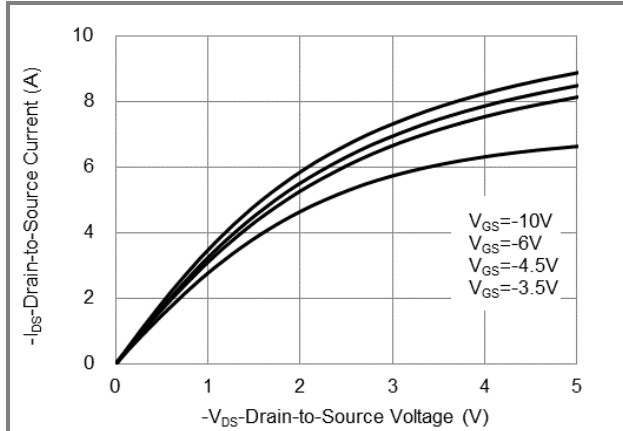
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-80	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-1.2	-1.4	-2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-1.8A	-	288	360	mΩ
		V _{GS} =-6V, I _D =-1.5A	-	303	400	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-80V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Dynamic ^(Note 5)						
Total Gate Charge	Q _g	V _{DS} =-40V, I _D =-1.8A, V _{GS} =-10V ^(Note 2)	-	8.4	12	nC
Gate-Source Charge	Q _{gs}		-	1.45	-	
Gate-Drain Charge	Q _{gd}		-	1.2	-	
Input Capacitance	C _{iss}	V _{DS} =-40V, V _{GS} =0V, f=1MHz	-	421	650	pF
Output Capacitance	C _{oss}		-	20	35	
Reverse Transfer Capacitance	C _{rss}		-	12	21	
Gate resistance	R _g	f=1MHz	-	11.3	-	Ω
Turn-On Delay Time	td _(on)	V _{DS} =-40V, I _D =-1.8A, V _{GS} =-10V, R _G =3Ω ^(Note 2)	-	3.6	-	ns
Turn-On Rise Time	tr		-	3	-	
Turn-Off Delay Time	td _(off)		-	22	-	
Turn-Off Fall Time	tf		-	9	-	
Drain-Source Diode						
Diode Forward Current	I _S	T _C =25 ^o C (Package Limit)	-	-	-1.8	A
Pulsed Diode Forward Current	I _{SM}		-	-	-10	
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V	-	-0.8	-1.3	V
Reverse Recovery Time	T _{rr}	V _{DD} =-40V, V _{GS} =0V,	-	15	-	ns
Reverse Recovery Charge	Q _{rr}	I _S =-1.8A, dI _S /dt=100A/us	-	11	-	nC

NOTES :

1. Pulse width≤300us, Duty cycle≤2%.
2. Essentially independent of operating temperature typical characteristics.
3. Chip capability with an R_{θJC}=7.2°C/W.
4. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
5. Guaranteed by design, not subject to production testing.

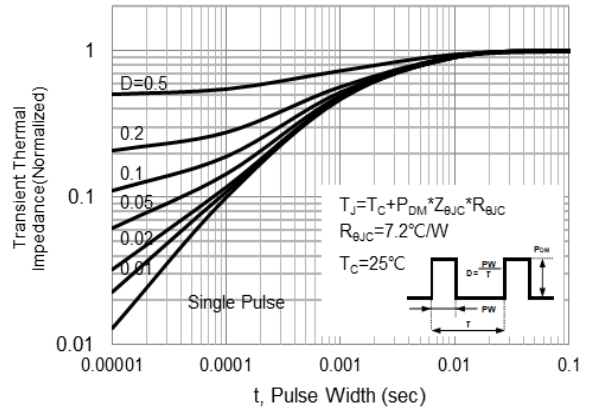
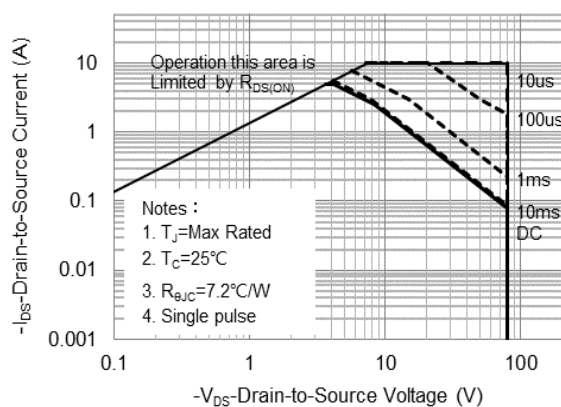
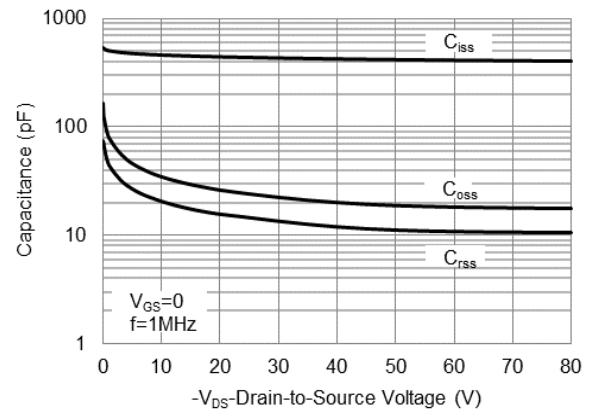
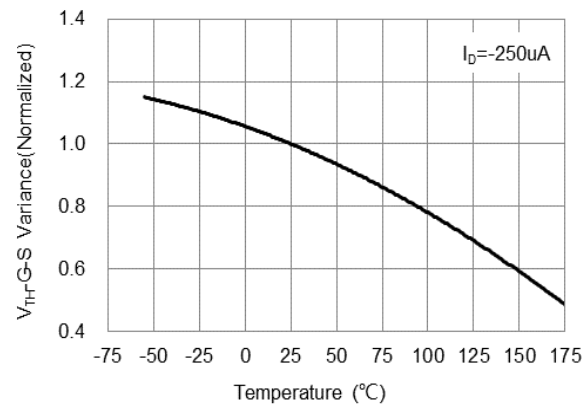
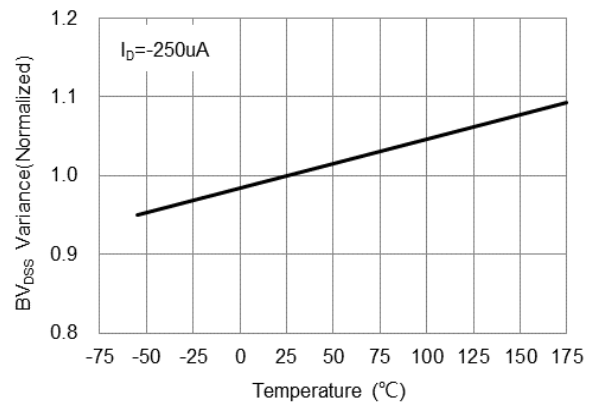
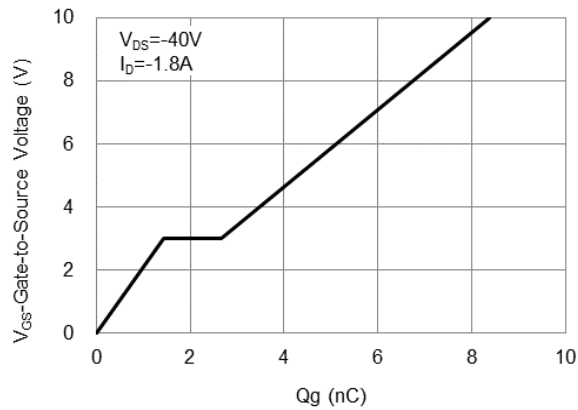
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TYPICAL CHARACTERISTIC CURVES



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TYPICAL CHARACTERISTIC CURVES

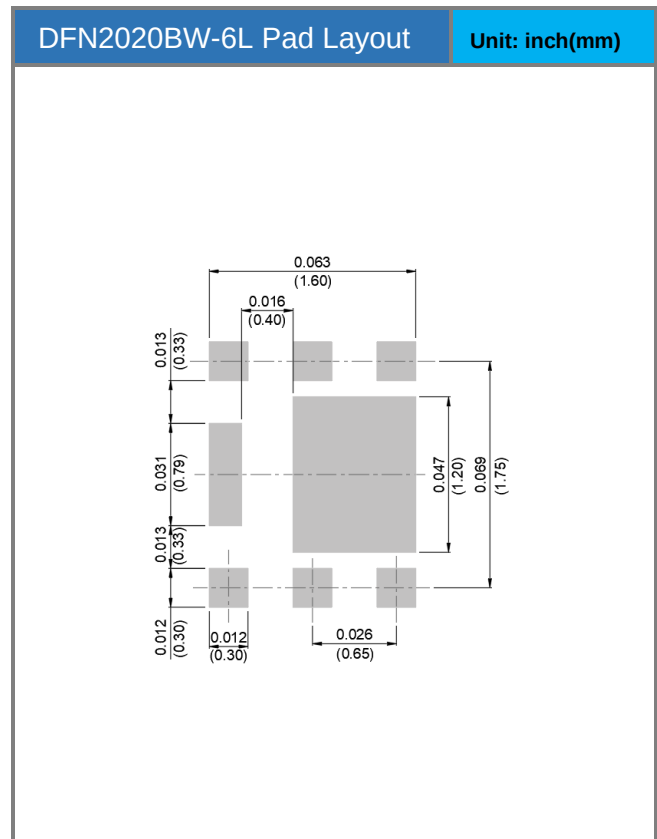
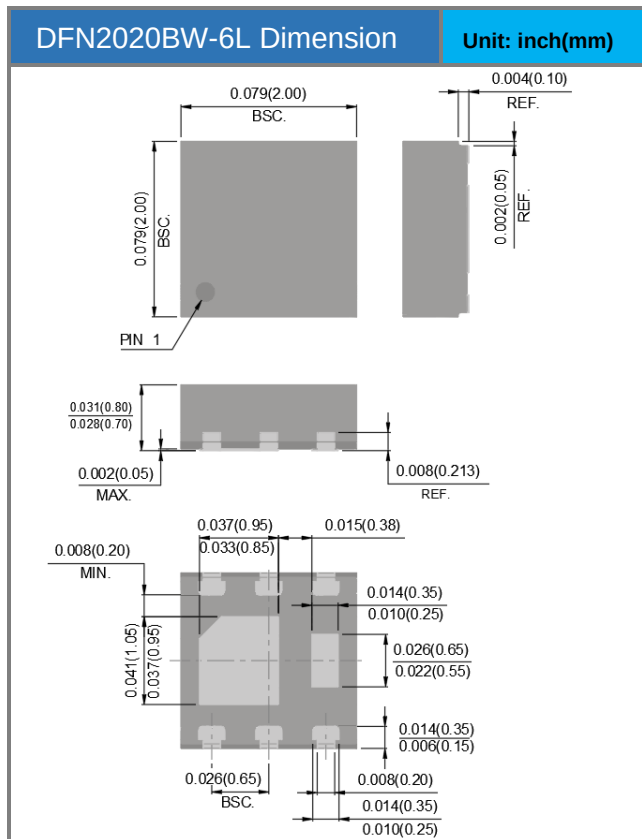


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Product and Packing Information

Part No.	Package Type	Packing type	Marking
PJQ24817AW-AU	DFN2020BW-6L	3K pcs / 7" reel	L87

Packaging Information & Mounting Pad Layout



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