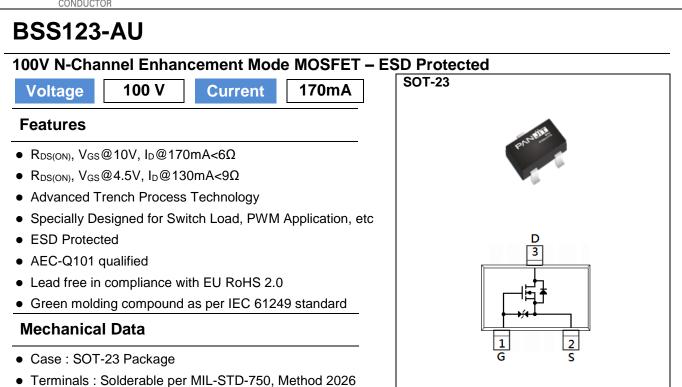
	1 A A A A A A A A A A A A A A A A A A A
ΡΛΝ	JIT
	SEMI
	CONDUCTOR



• Approx. Weight : 0.0084 grams

Maximum Ratings and Thermal Characteristics (T_A=25^oC unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	100		
Gate-Source Voltage	V _{GS}	<u>+</u> 20	V		
Continuous Drain Current ^(Note 5)		١ _D	170		
Pulsed Drain Current(Note 1)	ldм	680	mA		
Power Dissipation	T _a =25⁰C	PD	500	mW	
	Derate above 25°C		4	mW/°C	
Operating Junction and Storage	TJ,TSTG	-55~150	°C		
Thermal Resistance - Junction to Ambient ^(Note 3)		Reja	250	°C/W	



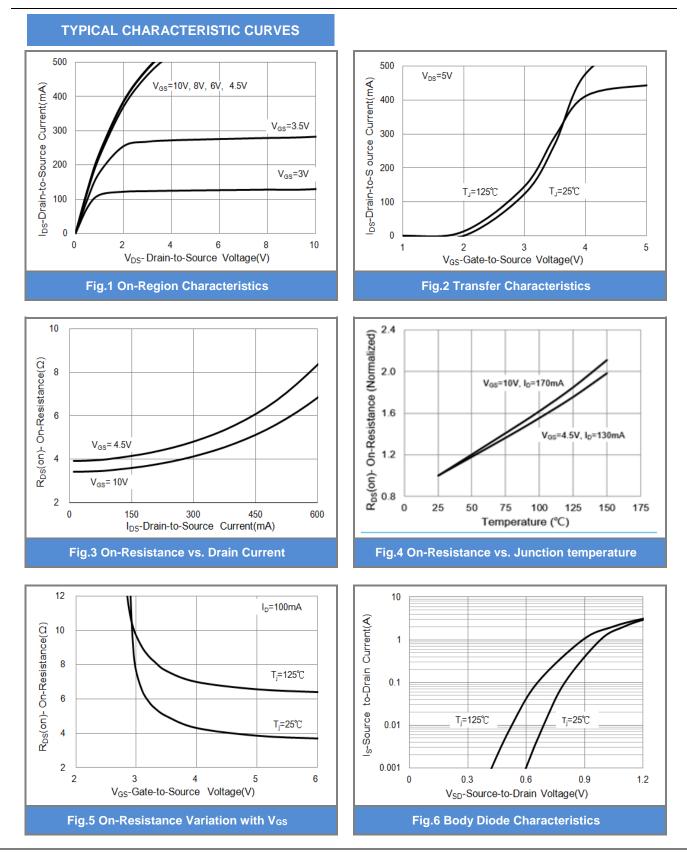
Electrical Characteristics (TA=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static			·			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	100	-	-	v
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	uA 1 1.7 2.5	V		
		Vgs=10V, Id=170mA	-	4	6	Ω
Drain-Source On-State Resistance	$R_{DS(on)}$	V _{GS} =4.5V, I _D =130mA	-	4.5	9	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =80V, V _{GS} =0V	-	-	1	
Gate-Source Leakage Current	lgss	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 10	uA
Dynamic ^(Note 6)		-		_	_	_
Total Gate Charge	Qg	V _{DS} =30V, I _D =170mA, V _{GS} =10V ^(Note 1,2)	-	1.8	-	
Gate-Source Charge	Qgs		-	0.4	-	nC
Gate-Drain Charge	Q _{gd}	VGS=10V(1000 1,2)	-	0.3	-	
Input Capacitance	Ciss	V _{DS} =25V, V _{GS} =0V, f=1MHz	-	45	-	_
Output Capacitance	Coss		-	14	-	pF
Reverse Transfer Capacitance	Crss		-	7.8	-	
Turn-On Delay Time	td _(on)		-	3.4	-	
Turn-On Rise Time	tr	$V_{DD}=30V, I_{D}=170mA,$ $V_{GS}=10V,$ $R_{G}=6\Omega^{(Note 1,2)}$	-	19	-	
Turn-Off Delay Time	td _(off)		-	8.2	-	ns
Turn-Off Fall Time	tf	KG=017(1000 1,2)	-	20	-	
Drain-Source Diode						
Maximum Continuous Drain-Source					170	~
Diode Forward Current	ls		-	-	170	mA
Diode Forward Voltage	V_{SD}	I _S =170mA, V _{GS} =0V	-	0.9	1.3	V

NOTES :

- 1. Pulse width<300us, Duty cycle<2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.







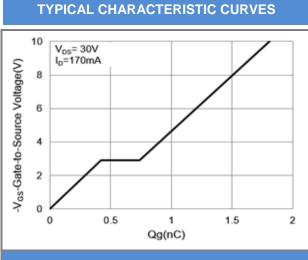
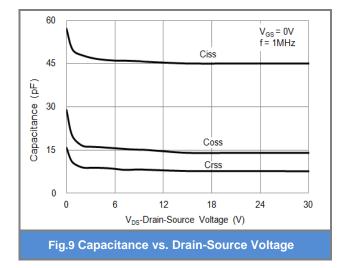


Fig.7 Gate-Charge Characteristics



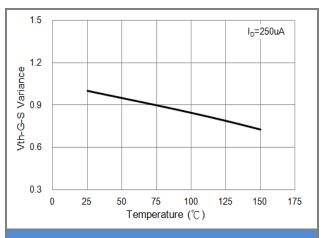


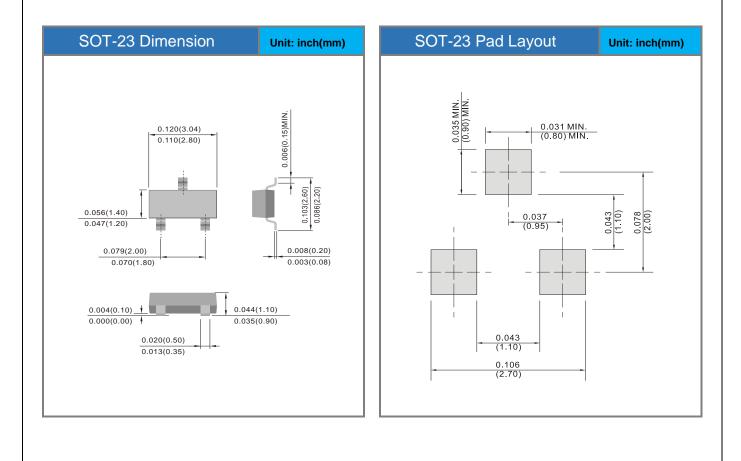
Fig.8 Threshold Voltage Variation with Temperature



Product and Packing Information

Part No.	Package Type	Packing Type	Marking	
BSS123-AU	SOT-23	3K pcs / 7" reel	A76	

Packaging Information & Mounting Pad Layout





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