

60V N-Channel Enhancement Mode MOSFET

Voltage	60 V	R_{DS(ON)}	7.3 mΩ
----------------	-------------	---------------------------	---------------

Current	60 A	Q_G (TYP)	38 nC
----------------	-------------	----------------------------	--------------

Feature

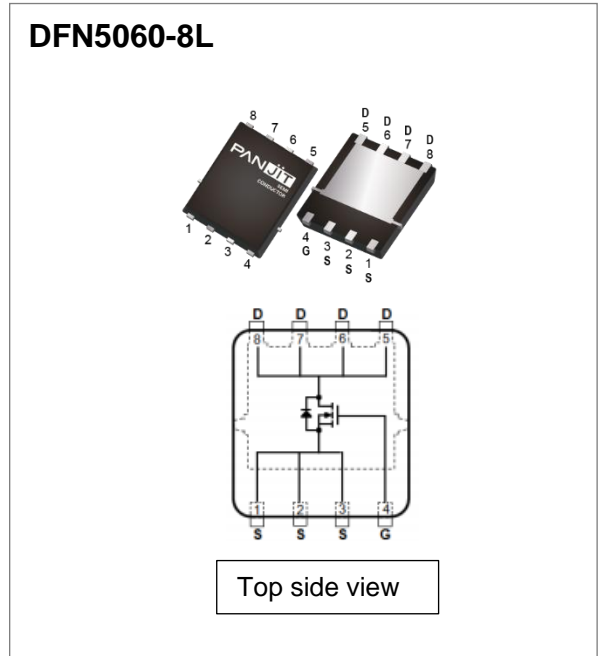
- R_{DS(ON)}, V_{GS}@10V, I_D@20A<7.3mΩ
- R_{DS(ON)}, V_{GS}@7V, I_D@15A<9.5mΩ
- High switching speed
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: DFN5060-8L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0028 ounces, 0.08 grams

Application

- SR solutions of PD Charger, BMS, BLDC motor driver switch



Absolute Maximum Ratings (T_A = 25 °C unless otherwise specified)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	60	V
Gate-Source Voltage		V _{GS}	+20/ -12	
Continuous Drain Current (Note 3)	T _C =25°C	I _D	60	A
	T _C =100°C		38	
Pulsed Drain Current		I _{DM}	240	A
Single Pulse Avalanche Current (Note 5)		I _{AS}	36	A
Single Pulse Avalanche Energy (Note 5)		E _{AS}	65	mJ
Power Dissipation	T _C =25°C	P _D	45	W
	T _C =100°C		18	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C

Thermal Characteristics

PARAMETER		SYMBOL	MAXIMUM	UNITS
Thermal Resistance	Junction-to-Case (Bottom)	R _{θJC}	2.8	°C/W
	Junction-to-Case (Top)	R _{θJT}	22	°C/W
	Junction-to-Ambient (Note.4)	R _{θJA}	50	°C/W

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	2.8	4	
Drain-Source On-State Resistance (Note 1)	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$	-	6.1	7.3	m Ω
		$V_{GS}=7V, I_D=15A$	-	7.6	9.5	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V$	-	-	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=20V, V_{DS}=0V$	-	-	100	nA
Transfer characteristics (Note 1)	gfs	$V_{DS}=3V, I_D=20A$	-	32	-	S
Dynamic (Note 6)						
Total Gate Charge	Q_g	$V_{DS}=30V, I_D=20A,$ $V_{GS}=7V$	-	29	-	nC
			-	38	-	
Gate-Source Charge	Q_{gs}	$V_{DS}=30V, I_D=20A,$ $V_{GS}=10V$	-	10	-	
Gate-Drain Charge	Q_{gd}		-	11	-	
Plateau Voltage	V_{GP}		-	4.6	-	V
Input Capacitance	C_{iss}	$V_{DS}=30V, V_{GS}=0V,$ $f=1.0\text{MHz}$	-	2093	-	pF
Output Capacitance	C_{oss}		-	672	-	
Reverse Transfer Capacitance	C_{rss}		-	15	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=30V, I_D=20A,$ $V_{GS}=10V, R_G=2\Omega$ (Note 2)	-	29	-	ns
Turn-On Rise Time	t_r		-	62	-	
Turn-Off Delay Time	$t_{d(off)}$		-	59	-	
Turn-Off Fall Time	t_f		-	27	-	
Gate Resistance	R_g	$f = 1.0\text{MHz}$	0.3	1.2	2.3	Ω
Drain-Source Diode						
Diode Forward Voltage	V_{SD}	$I_S=1A, V_{GS}=0V$	-	0.56	1	V
Reverse Recovery Charge	Q_{rr}	$I_{SD} = 20A$ $di/dt = 100A/\mu s$	-	12	-	nC
Reverse Recovery Time	T_{rr}		-	27	-	ns

NOTES :

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. The maximum current rating is silicon limited.
4. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
5. The test condition is $L=0.1\text{mH}$, $I_{AS}=36A$, $V_{DD}=25V$, $V_{GS}=10V$, $R_G=25\text{ohm}$, Starting $T_J=25^\circ\text{C}$
6. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTIC CURVES

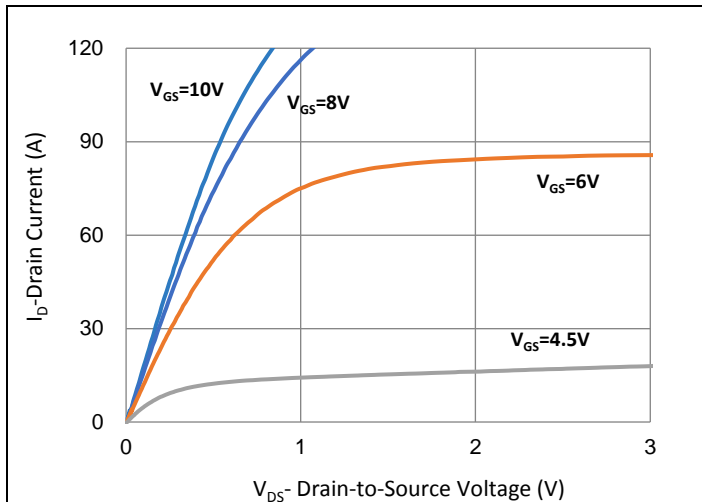


Fig.1 Output Characteristics

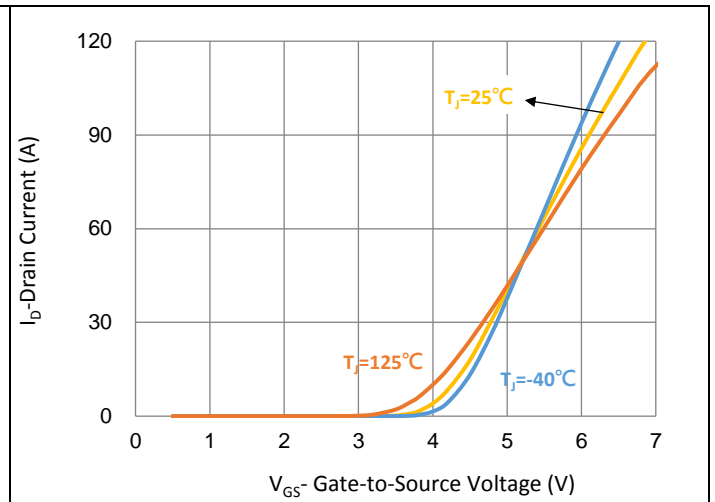


Fig.2 Transfer Characteristics

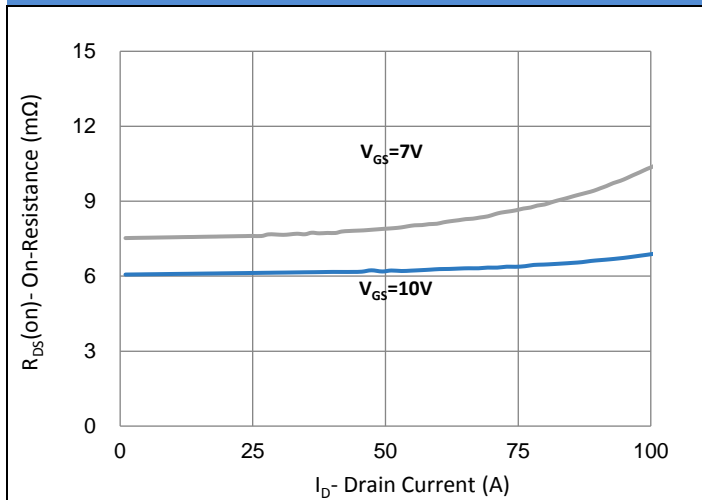


Fig.3 On-Resistance vs. Drain Current

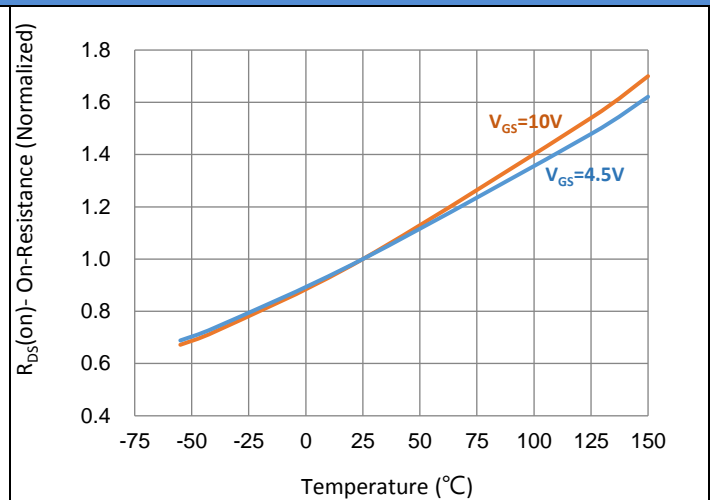


Fig.4 On-Resistance vs. Junction temperature

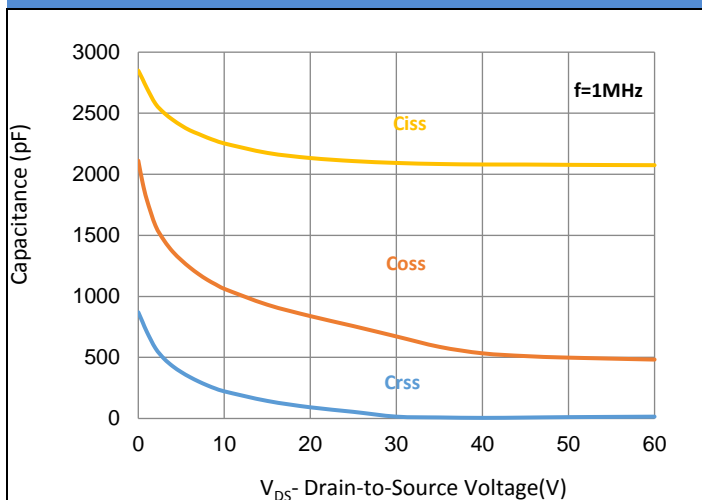


Fig.5 Capacitance vs. Drain-Source Voltage

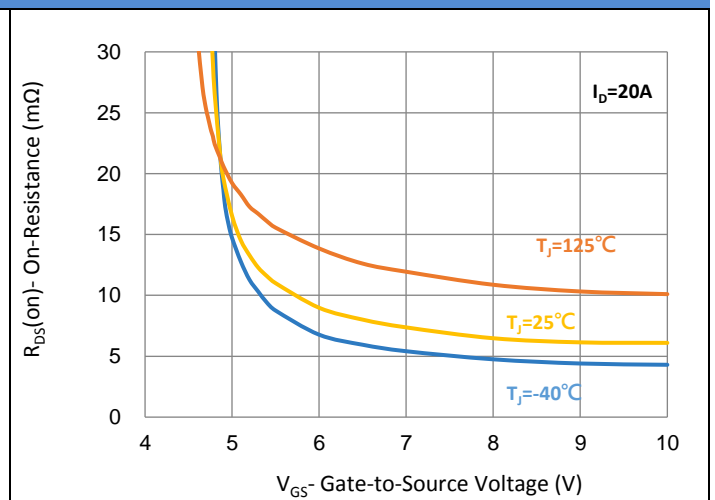


Fig.6 On-Resistance vs. Gate-Source Voltage

TYPICAL CHARACTERISTIC CURVES

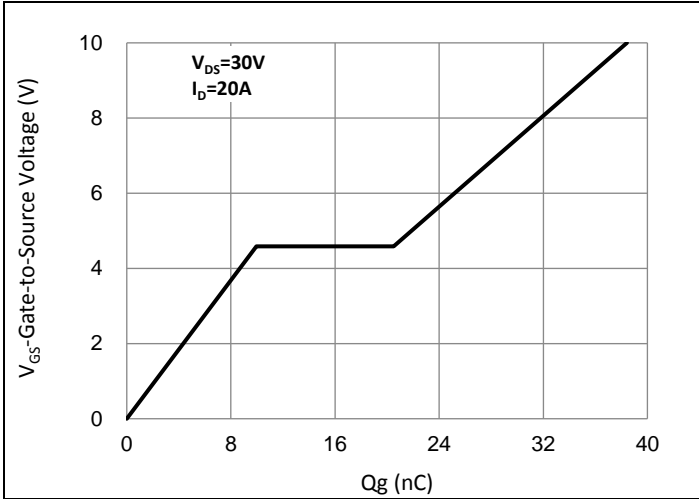


Fig.7 Gate-Charge Characteristics

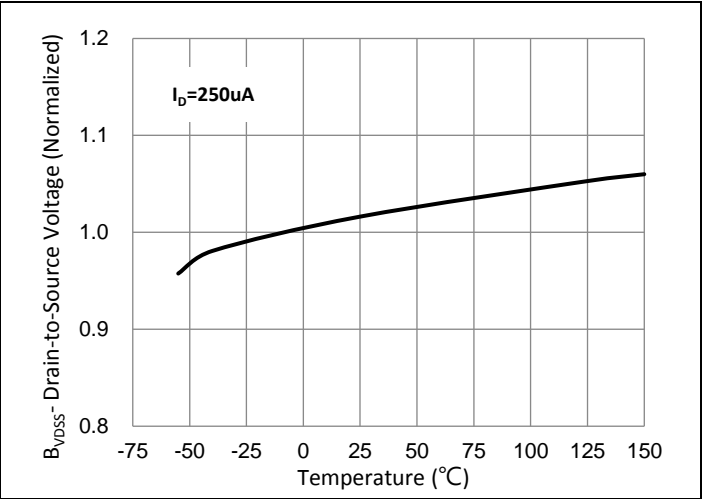


Fig.8 Breakdown Voltage Variation vs. Temperature

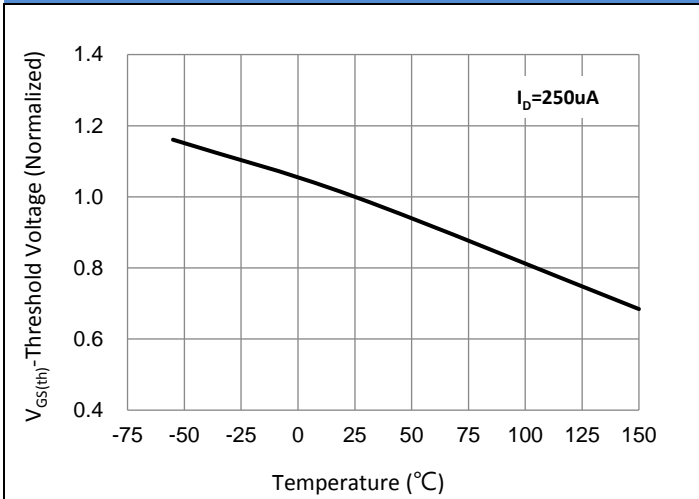


Fig.9 Threshold Voltage Variation with Temperature

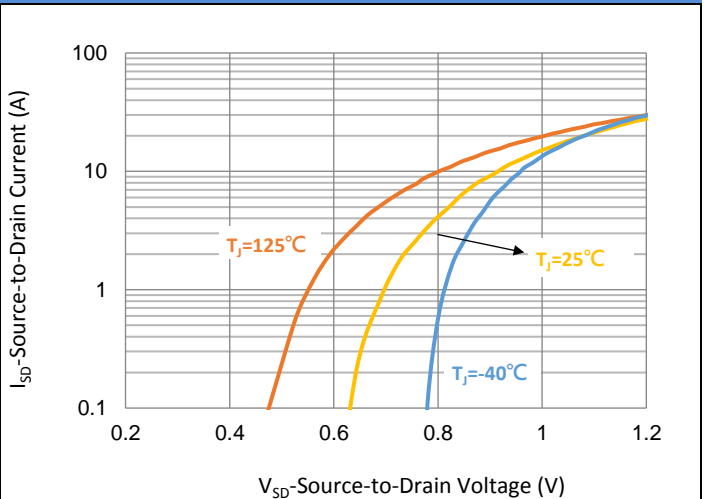


Fig.10 Source-Drain Diode Forward Voltage

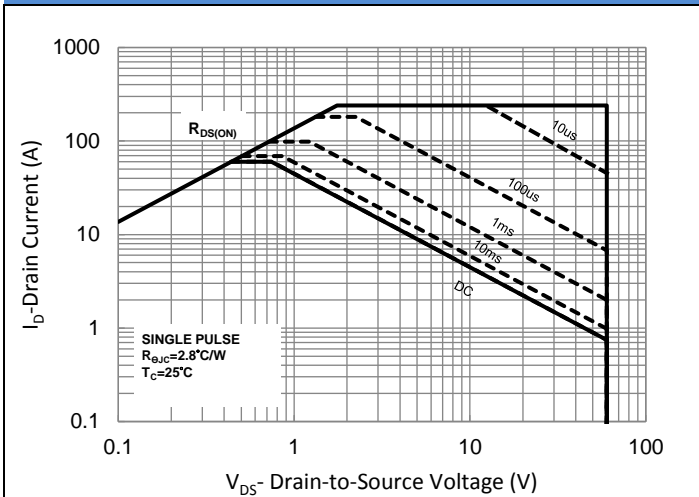


Fig.11 Maximum Safe Operating Area

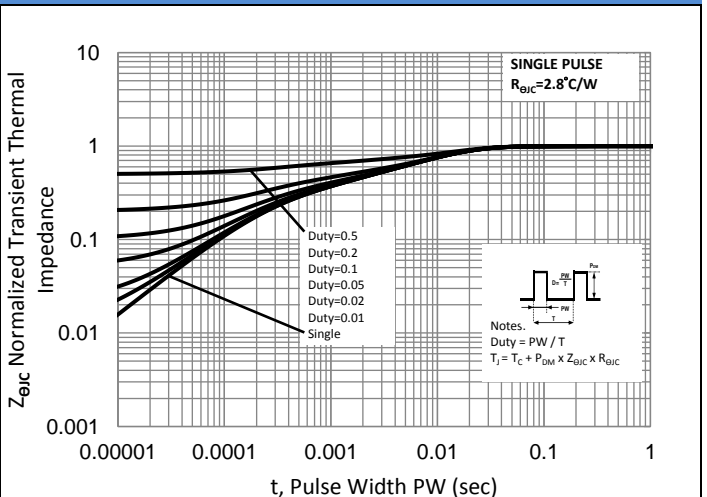


Fig.12 Normalized Transient Thermal Impedance

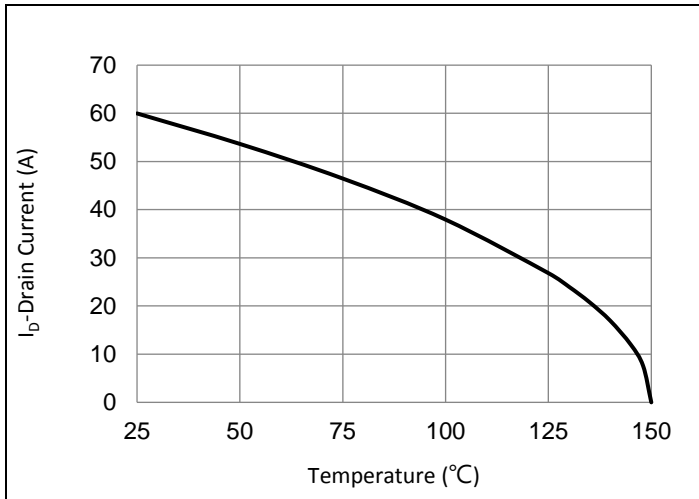
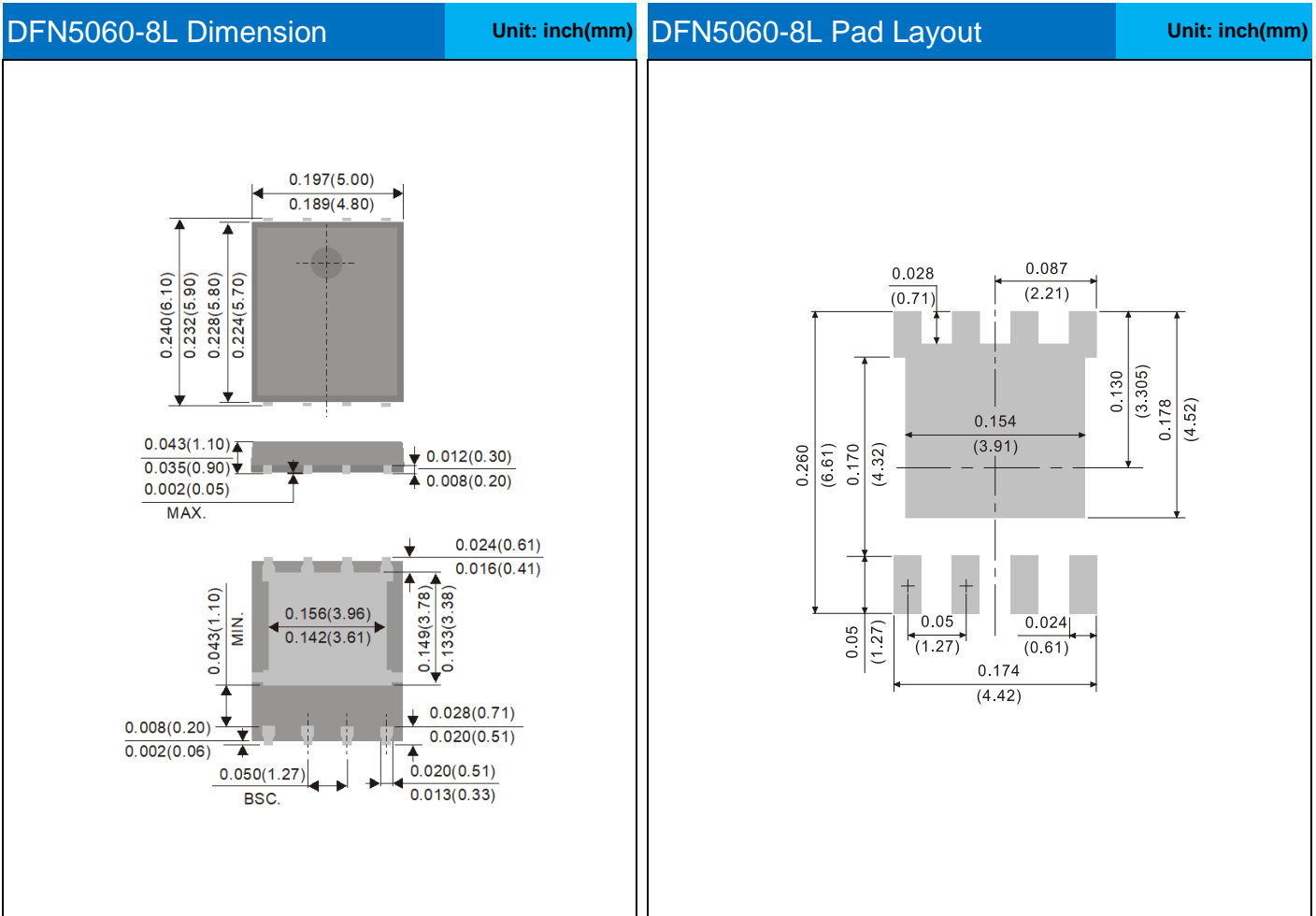


Fig.13 Drain Current vs. Case Temperature

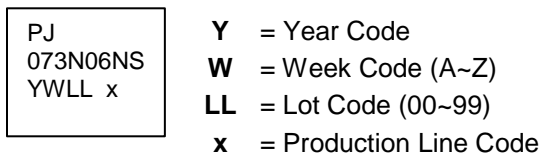
Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PSMQC073N06NS1	DFN5060-8L	3000pcs / 13" reel	073N06NS

Packaging Information & Mounting Pad Layout



Marking Diagram



Disclaimer

- Reproducing and modifying information of the document is prohibited without permission from Panjit International Inc..
- Panjit International Inc. reserves the rights to make changes of the content herein the document anytime without notification. Please refer to our website for the latest document.
- Panjit International Inc. disclaims any and all liability arising out of the application or use of any product including damages incidentally and consequentially occurred.
- Panjit International Inc. does not assume any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.
- Applications shown on the herein document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications. Panjit International Inc. makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.
- The products shown herein are not designed and authorized for equipments requiring high level of reliability or relating to human life and for any applications concerning life-saving or life-sustaining, such as medical instruments, transportation equipment, aerospace machinery et cetera. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Panjit International Inc. for any damages resulting from such improper use or sale.
- Since Panjit uses lot number as the tracking base, please provide the lot number for tracking when complaining.