

PJD16N06A-AU

60V N-Channel Enhancement Mode MOSFET

Voltage **60 V** **Current** **16 A**

Features

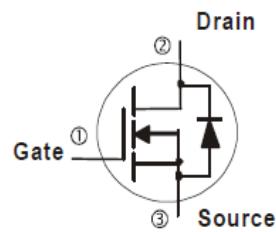
- $R_{DS(ON)}$, $V_{GS} @ 10V$, $I_D @ 8A < 50m\Omega$
- $R_{DS(ON)}$, $V_{GS} @ 4.5V$, $I_D @ 4A < 60m\Omega$
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : TO-252AA Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0104 ounces, 0.297grams



TO-252AA



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current (Note 4)	I_D	16	A
		10	
Pulsed Drain Current (Note 1)	I_{DM}	64	W
Power Dissipation	P_D	32.6	
		16.3	
Continuous Drain Current (Note 4)	I_D	4.4	A
		3.5	
Power Dissipation	P_D	2.4	W
		1.6	
Single Pulse Avalanche Energy (Note 6)	E_{AS}	11	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~175	°C
Typical Thermal Resistance (Note 4,5)	Junction to Case	$R_{\theta JC}$	4.6
	Junction to Ambient	$R_{\theta JA}$	62.5

- Limited only by Maximum Junction Temperature

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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	60	-	-	V
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1	1.77	2.5	
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=8\text{A}$	-	37	50	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=4\text{A}$	-	42	60	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Dynamic (Note 7)						
Total Gate Charge	Q_g	$V_{\text{DS}}=30\text{V}, I_{\text{D}}=4\text{A}, V_{\text{GS}}=10\text{V}$ (Note 1,2)	-	14	-	nC
Gate-Source Charge	Q_{gs}		-	2.9	-	
Gate-Drain Charge	Q_{gd}		-	2.3	-	
Input Capacitance	C_{iss}	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	815	-	pF
Output Capacitance	C_{oss}		-	379	-	
Reverse Transfer Capacitance	C_{rss}		-	110	-	
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=30\text{V}, I_{\text{D}}=1\text{A}, V_{\text{GS}}=10\text{V}, R_{\text{G}}=3.3\Omega$ (Note 1,2)	-	3.9	-	ns
Turn-On Rise Time	t_{r}		-	13	-	
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	23	-	
Turn-Off Fall Time	t_{f}		-	6.7	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_{s}	---	-	-	16	A
Diode Forward Voltage	V_{SD}	$I_{\text{s}}=1\text{A}, V_{\text{GS}}=0\text{V}$	-	0.73	1	V

NOTES :

1. Pulse width $\leq 300\text{us}$, Duty cycle $\leq 2\%$.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature $T_{\text{J(MAX)}}=150^\circ\text{C}$. Ratings are based on low frequency and duty cycles to keep initial $T_{\text{J}}=25^\circ\text{C}$.
4. The maximum current rating is package limited.
5. R_{QJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
6. The test condition is $L=0.1\text{mH}, I_{\text{AS}}=15\text{A}, V_{\text{DD}}=25\text{V}, V_{\text{GS}}=10\text{V}$.
7. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES

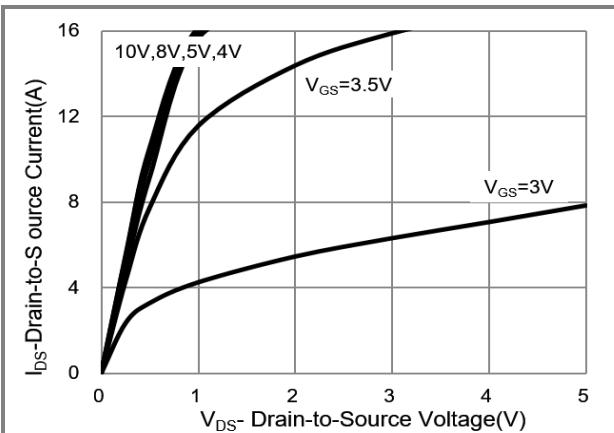


Fig.1 Output Characteristics

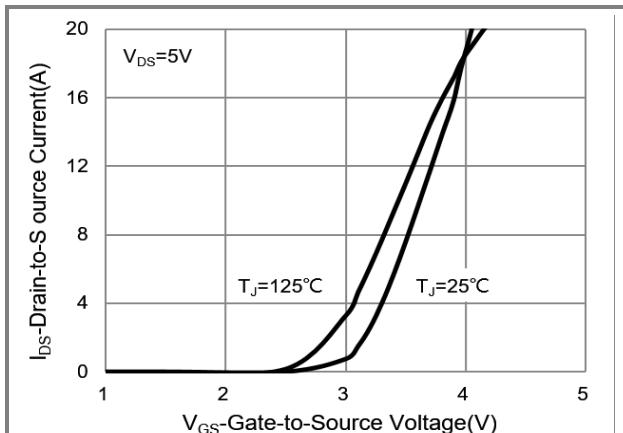


Fig.2 Transfer Characteristics

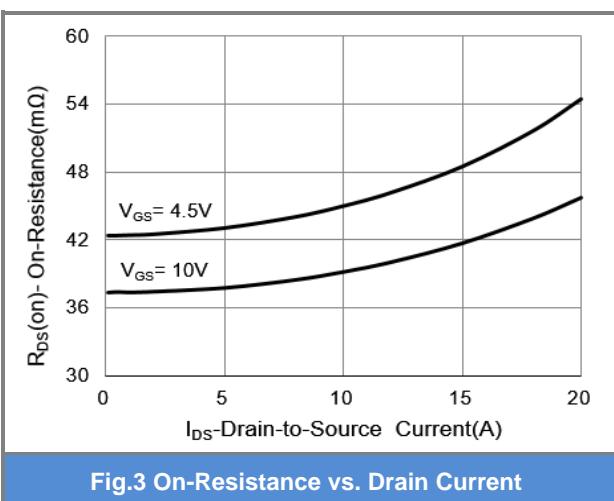


Fig.3 On-Resistance vs. Drain Current

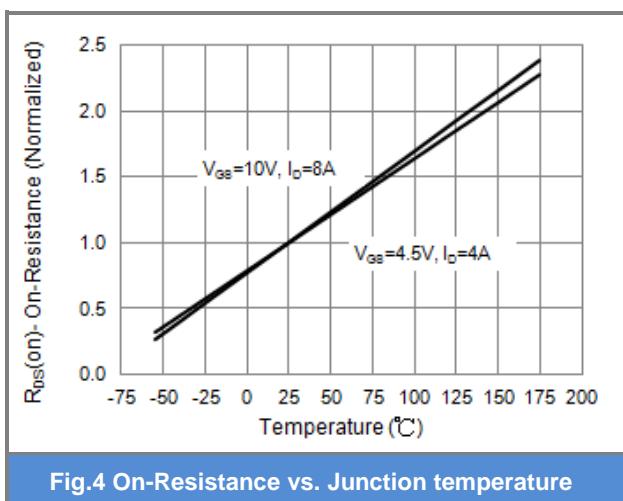


Fig.4 On-Resistance vs. Junction temperature

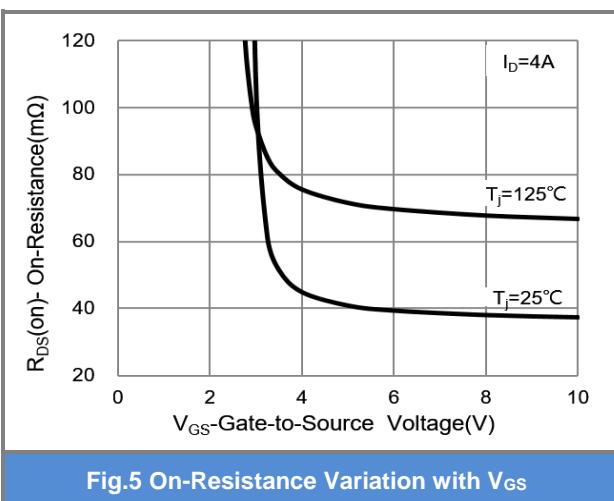


Fig.5 On-Resistance Variation with V_{GS}

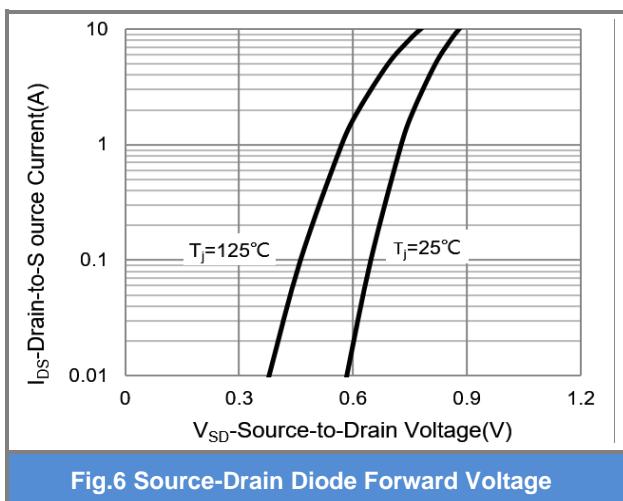


Fig.6 Source-Drain Diode Forward Voltage

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TYPICAL CHARACTERISTIC CURVES

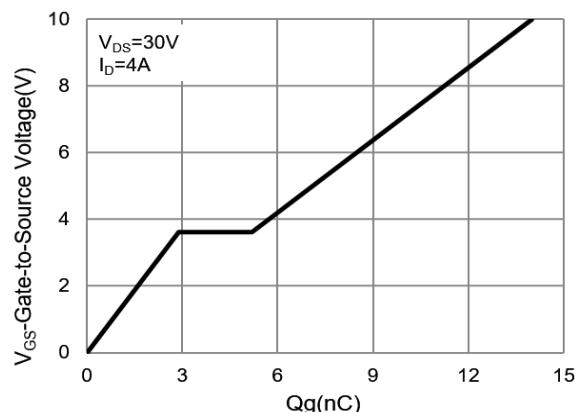


Fig.7 Gate-Charge Characteristics

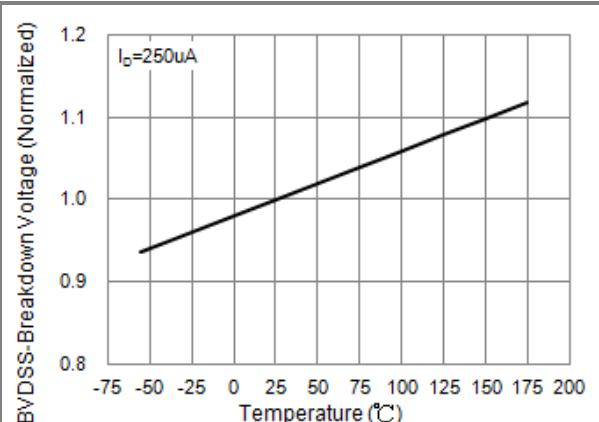


Fig.8 Breakdown Voltage Variation vs. Temperature

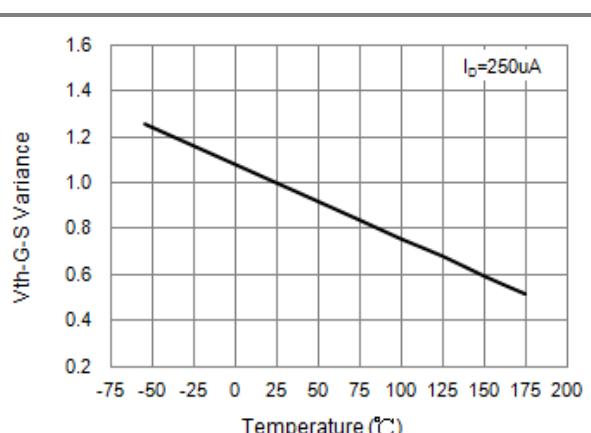


Fig.9 Threshold Voltage Variation with Temperature

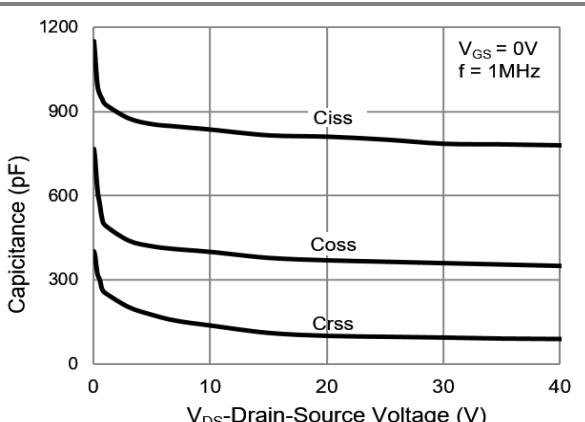


Fig.10 Capacitance vs. Drain-Source Voltage

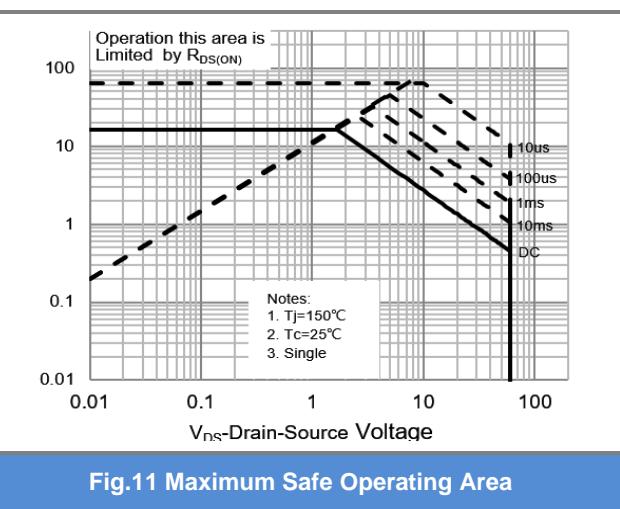


Fig.11 Maximum Safe Operating Area

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TYPICAL CHARACTERISTIC CURVES

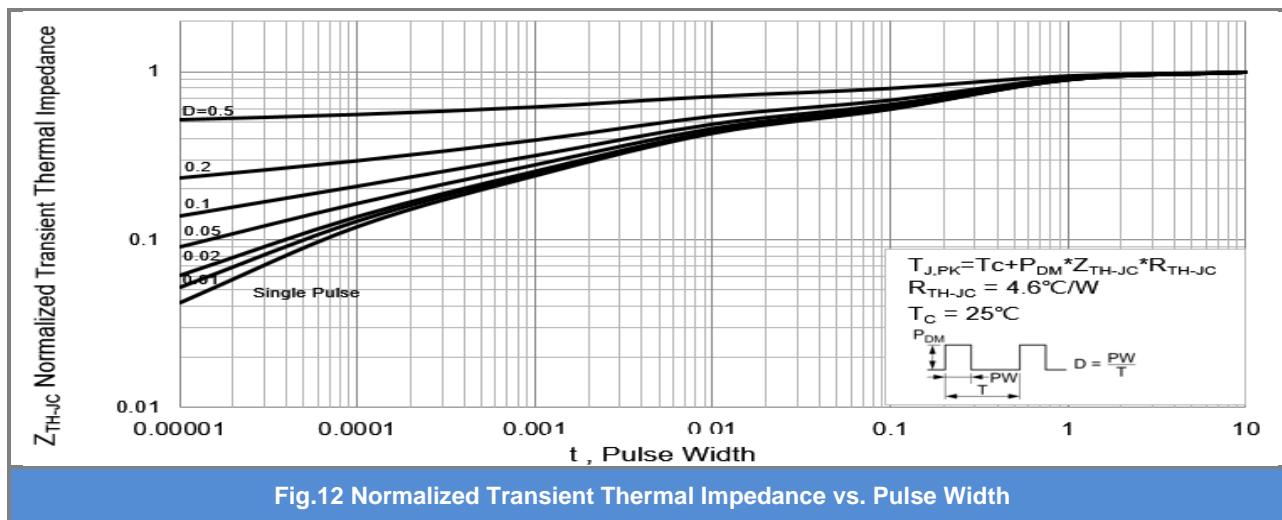


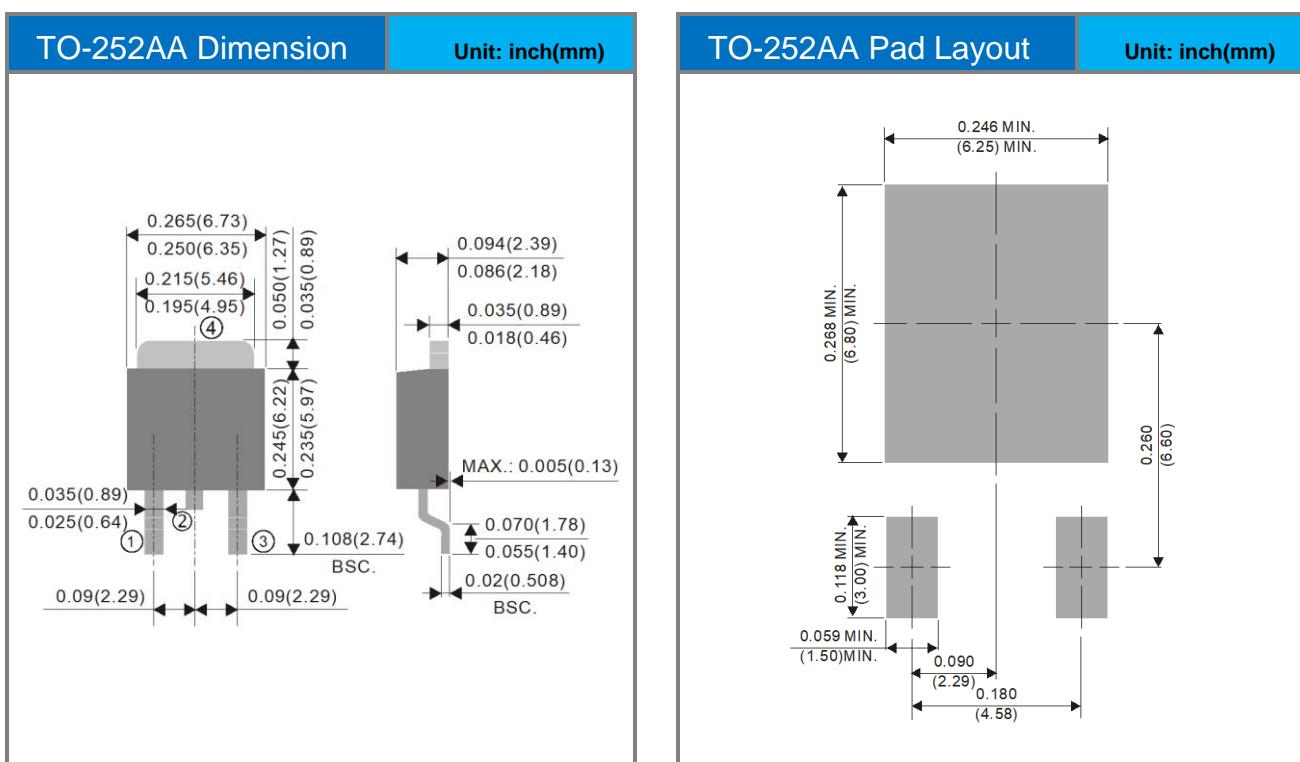
Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

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Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJD16N06A-AU	TO-252AA	3,000pcs / 13" reel	D16N06A

Packaging Information & Mounting Pad Layout



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