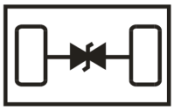
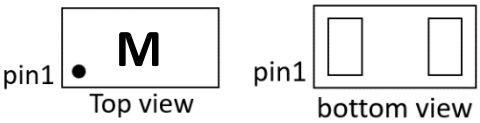
PS1703-D32

Low Capacitance ESD Protection

Voltage **3.3 V**

DFN0603-2L





Part Marking	Parameter
M	M = Marking Code

Features

- IEC61000-4-2(ESD) : ±30kV Air, ±30kV Contact
- IEC61000-4-4(EFT) : 40A(5/50ns)
- IEC61000-4-5(Lightning) : 12A(8/20uS)
- Low leakage current, maximum of 0.5uA at rated voltage
- Low clamping voltage
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : DFN0603-2L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0004 grams

Applications

- USB 2.0
- Notebooks
- SATA ports

Maximum Ratings and Thermal Characteristics (T_A = 25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
ESD IEC61000-4-2(Air) ESD IEC61000-4-2(Contact)	V_{ESD}	±30 ±30	kV
Typical Thermal Resistance(Note 1)	$R_{\theta JA}$	500	°C/W
Operating Junction Temperature Range	T_J	-55~125	°C
Storage Temperature Range	T_{STG}	-55~150	°C

PS1703-D32

Electrical Characteristics (T_A = 25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Reverse Stand-Off Voltage(Note 2)	V_{RWM}	Pin1 to Pin2	-	-	3.3	V
Reverse Breakdown Voltage	V_{BR}	I_{BR} = 1mA, Pin1 to Pin2	5.5	-	11	V
Reverse Leakage Current	I_R	V_R = ±3.3V, Pin1 to Pin2	-	0.4	0.5	uA
Clamping Voltage (8/20 us)	V_{CL}	I_{PP} = 5A, Pin1 to Pin2	-	2.4	3.4	V
Clamping Voltage TLP (tperiod=100ns,tr=1ns)(Note 3)	V_{CL}	I_{TLP} = 16A, Pin1 to Pin2	-	2.6	-	V
Off State Junction Capacitance(Note 4)	C_J	V_R=1.65V, f=1MHz, Pin1 to Pin2	-	0.77	0.9	pF

NOTES :

1. Mounted on a FR4 PCB, Single-sided copper, mini pad.
2. A transient suppressor is selected according to the working peak reverse voltage(V_{RWM}), which should be equal to or greater than the DC or continuous peak operation voltage level.
3. Testing using Transmission Line Pulse (TLP) conditions: Z0 = 50 Ω, t_P = 100 ns.
4. This parameter is guaranteed by design.
5. This snap-back behavior strongly reduces the clamping voltage to the system behind the ESD protection during an ESD event. Do not connect unlimited DC current sources to the data lines to avoid the ESD protection device maintain in snap-back state after exceeding breakdown voltage.

PS1703-D32

TYPICAL CHARACTERISTIC CURVES

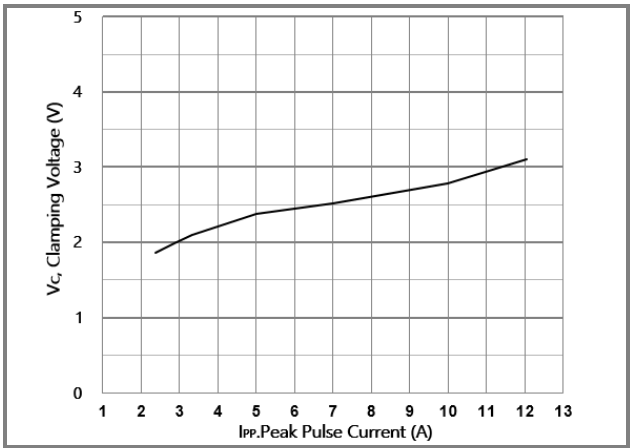


Fig.1 Typical Peak Clamping Voltage

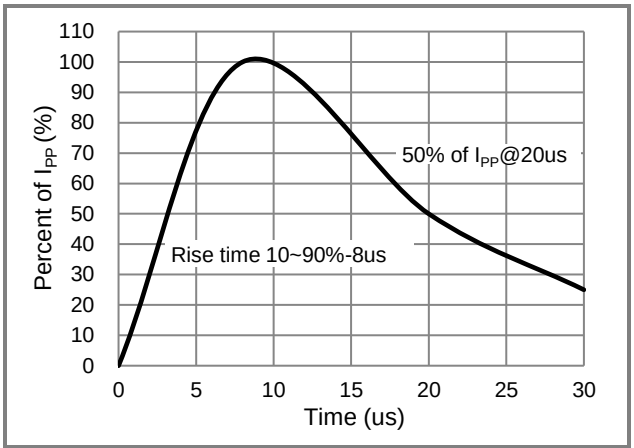


Fig.2 Pulse Waveform

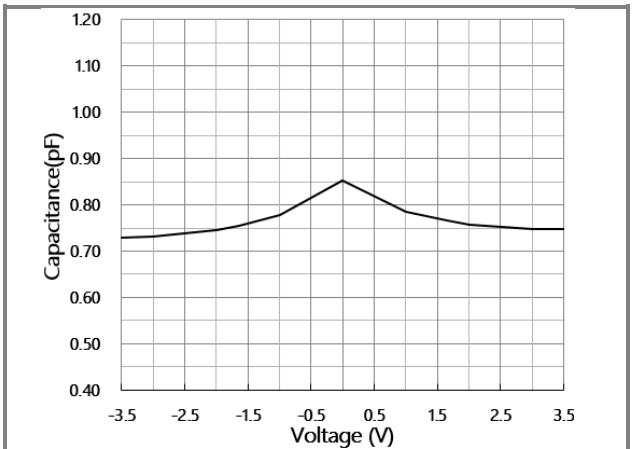


Fig.3 Typical Junction Capacitance

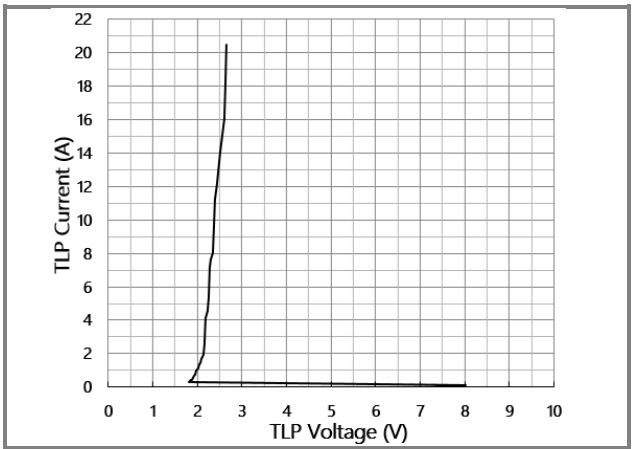


Fig.4 TLP Measurement

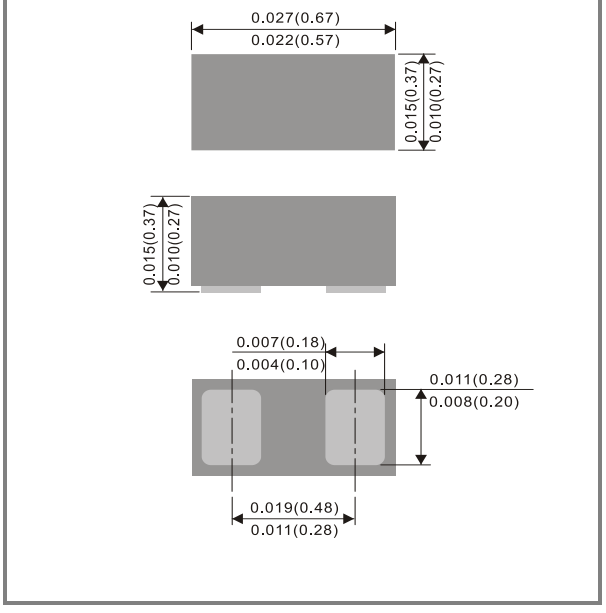
PS1703-D32

Product and Packing Information

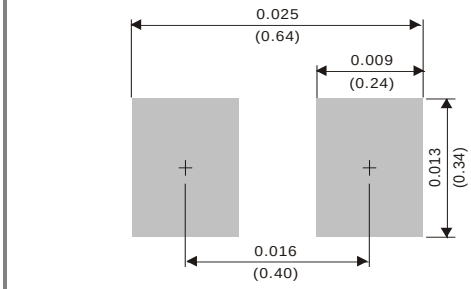
Part No.	Package Type	Packing Type	Marking
PS1703-D32	DFN0603-2L	10K pcs / 7" reel	M

Packaging Information & Mounting Pad Layout

DFN0603-2L Dimension — Unit: inch(mm)



DFN0603-2L Pad Layout — Unit: inch(mm)



PS1703-D32

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