

# PJW4P06A-AU

## 60V P-Channel Enhancement Mode MOSFET

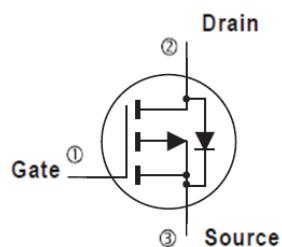
Voltage

-60 V

Current

-4 A

SOT-223



### Features

- $R_{DS(ON)}$ ,  $V_{GS} @ -10V$ ,  $I_D @ -4A < 110m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS} @ -4.5V$ ,  $I_D @ -2A < 130m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : SOT-223 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.123grams

### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	$V_{DS}$	-60	V
Gate-Source Voltage	$V_{GS}$	+20	
Continuous Drain Current( <sup>Note 4</sup> )	$I_D$	-4	A
		-3.2	
Pulsed Drain Current( <sup>Note 1</sup> )	$I_{DM}$	-16	
Power Dissipation	$P_D$	3.1	W
		2	
Single Pulse Avalanche Energy( <sup>Note 6</sup> )	$E_{AS}$	12.8	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	°C
Typical Thermal Resistance - Junction to Ambient( <sup>Note 4,5</sup> )	$R_{\theta JA}$	40.3	°C/W

- Limited only By Maximum Junction Temperature

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## Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-60	-	-	V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1	-1.7	-2.5	
Drain-Source On-State Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-4\text{A}$	-	87	110	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-2\text{A}$	-	110	130	
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	$\text{nA}$
<b>Dynamic</b> <sup>(Note 7)</sup>						
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-30\text{V}, I_{\text{D}}=-4\text{A}, V_{\text{GS}}=-10\text{V}$ <sup>(Note 2,3)</sup>	-	10	-	$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$		-	1.6	-	
Gate-Drain Charge	$Q_{\text{gd}}$		-	3	-	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	785	-	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		-	175	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	112	-	
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DS}}=-30\text{V}, R_{\text{L}}=30\Omega$ $V_{\text{GS}}=-10\text{V}, R_{\text{G}}=6.2\Omega$ <sup>(Note 2,3)</sup>	-	8	-	$\text{ns}$
Turn-On Rise Time	$t_r$		-	15	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	43	-	
Turn-Off Fall Time	$t_f$		-	8.4	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_s$	---	-	-	-4	A
Diode Forward Voltage	$V_{\text{SD}}$	$I_s=1\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.76	-1	V

### NOTES :

1. Pulse width $\leq 300\mu\text{s}$ , Duty cycle $\leq 2\%$ .
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature  $T_{\text{J}(\text{MAX})}=150^\circ\text{C}$ . Ratings are based on low frequency and duty cycles to keep initial  $T_{\text{J}}=25^\circ\text{C}$ .
4. The maximum current rating is package limited.
5.  $R_{\text{OA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. The test condition is  $L=0.1\text{mH}, I_{\text{AS}}=16\text{A}, V_{\text{DD}}=25\text{V}, V_{\text{GS}}=10\text{V}$ .
7. Guaranteed by design, not subject to production testing.

# PJW4P06A-AU

## TYPICAL CHARACTERISTIC CURVES

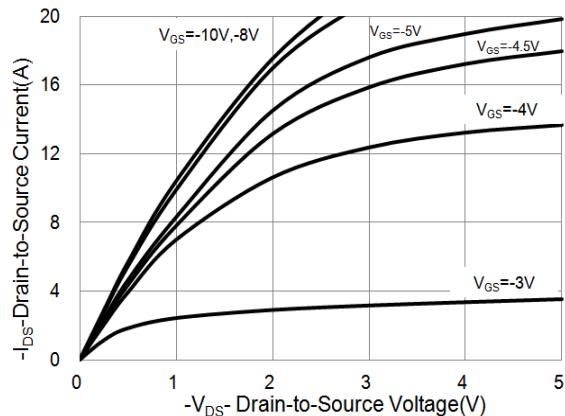


Fig.1 Output Characteristics

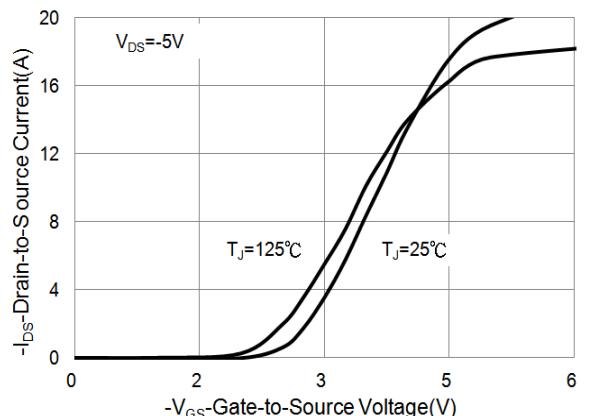


Fig.2 Transfer Characteristics

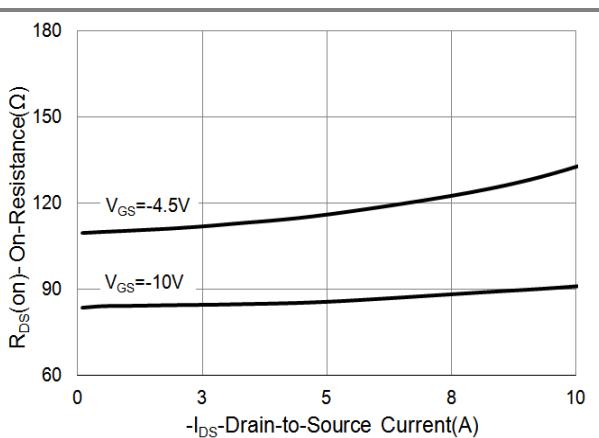


Fig.3 On-Resistance vs. Drain Current

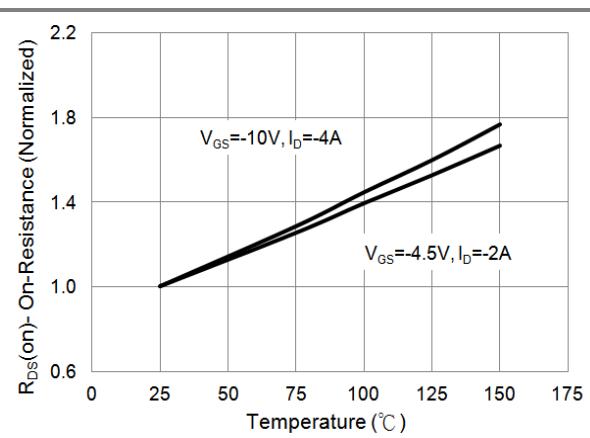


Fig.4 On-Resistance vs. Junction temperature

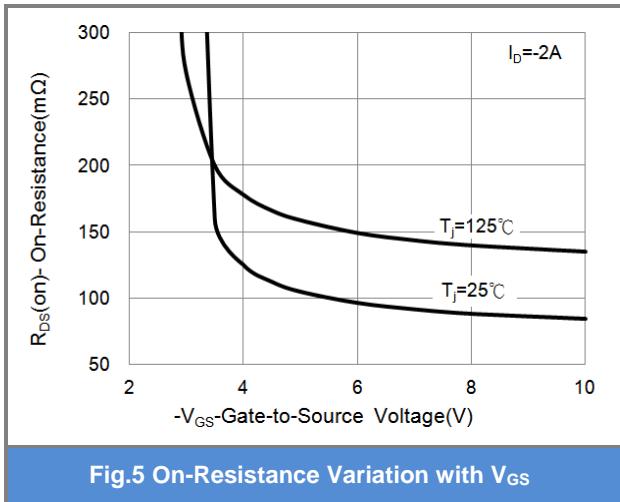


Fig.5 On-Resistance Variation with  $V_{GS}$

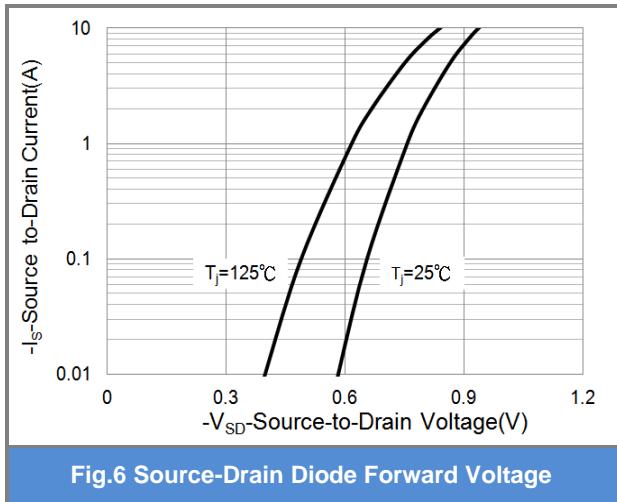


Fig.6 Source-Drain Diode Forward Voltage

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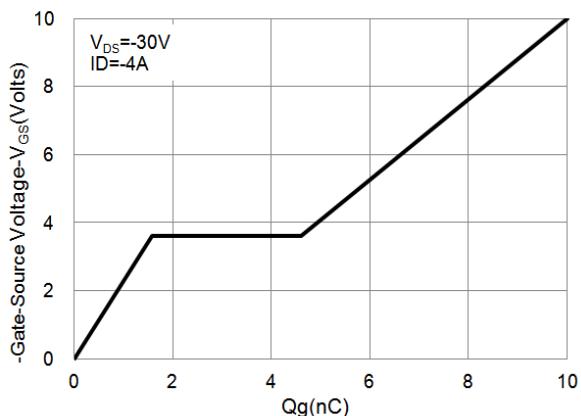


Fig.7 Gate-Charge Characteristics

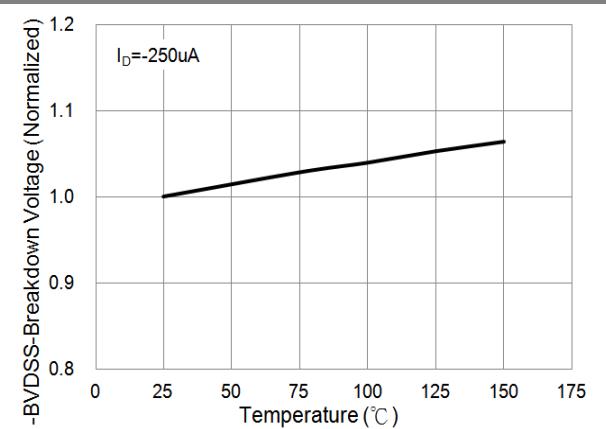


Fig.8 Breakdown Voltage Variation vs. Temperature

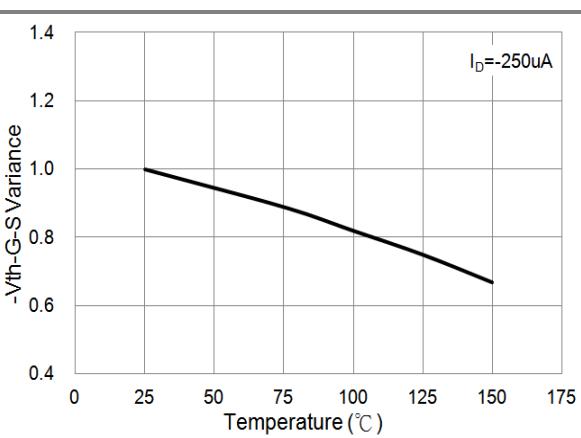


Fig.9 Threshold Voltage Variation with Temperature

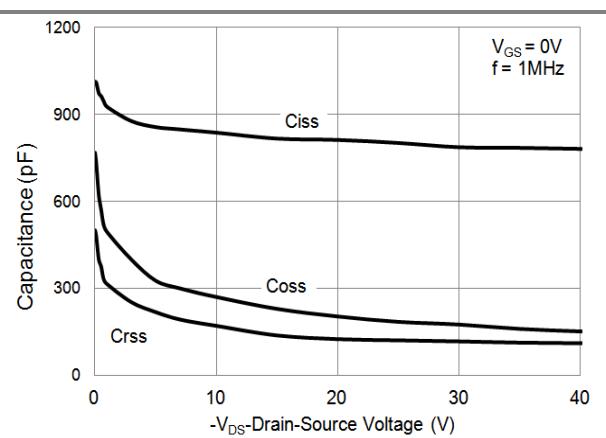


Fig.10 Capacitance vs. Drain-Source Voltage

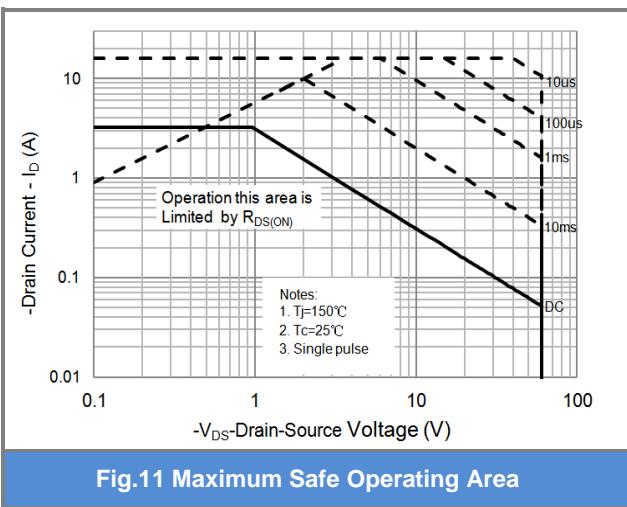


Fig.11 Maximum Safe Operating Area

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### TYPICAL CHARACTERISTIC CURVES

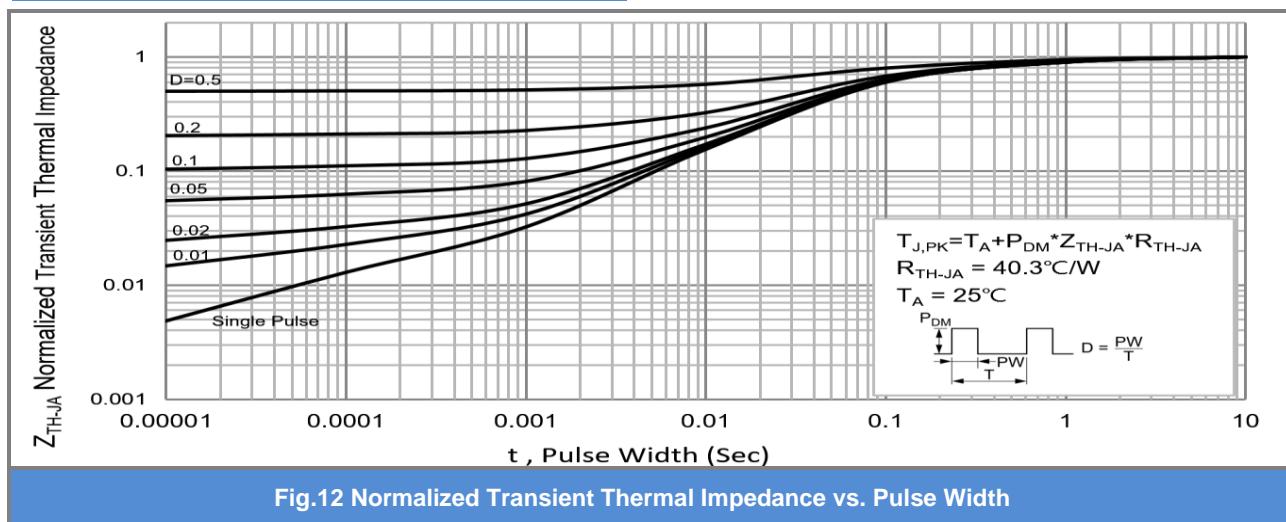


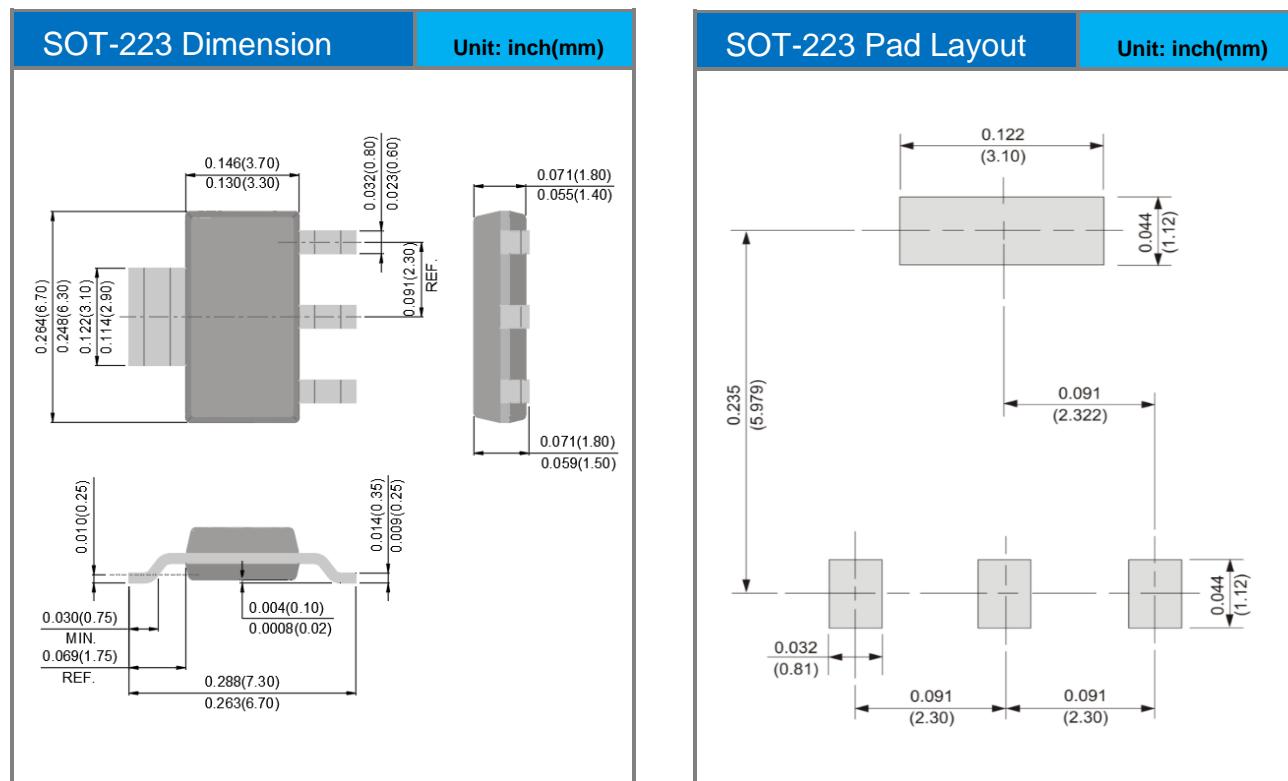
Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

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## Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJW4P06A-AU	SOT-223	2.5K pcs / 13" reel	W4P06A

## Packaging Information & Mounting Pad Layout



## **PJW4P06A-AU**

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