

# PJT7838

## 50V N-Channel Enhancement Mode MOSFET

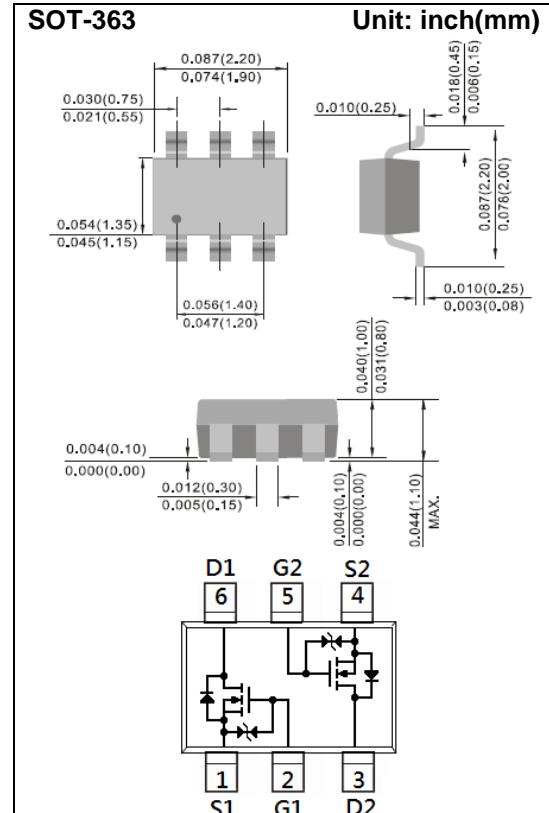
Voltage      50 V      Current      400mA

### Features

- RDS(ON) , VGS@10V, ID@500mA<1.45Ω
- RDS(ON) , VGS@4.5V, ID@200mA<1.95Ω
- RDS(ON) , VGS@2.5V, ID@100mA<4.0Ω
- RDS(ON) , VGS@1.8V, ID@10mA<6.0Ω
- Advanced Trench Process Technology
- ESD Protected 2KV HBM
- Specially Designed for Relay driver, Speed line drive, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std. (Halogen Free)

### Mechanical Data

- Case : SOT-363 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0002 ounces, 0.006 grams
- Marking: T38



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	$V_{DS}$	50	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	400	mA
Pulsed Drain Current	$I_{DM}$	1200	mA
Power Dissipation	$P_D$	350	mW
		2.8	$\text{mW}/^\circ\text{C}$
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	°C
Typical Thermal resistance - Junction to Ambient (Note 3)	$R_{\theta JA}$	357	°C/W

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## Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

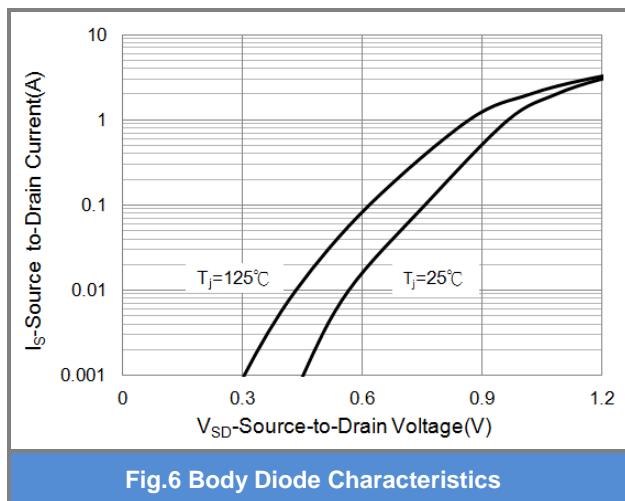
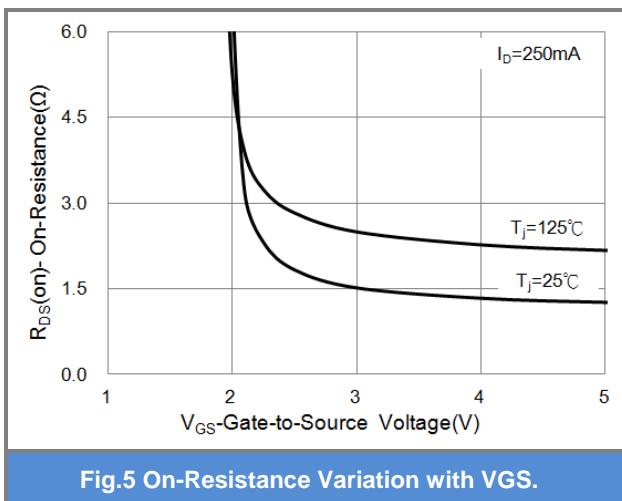
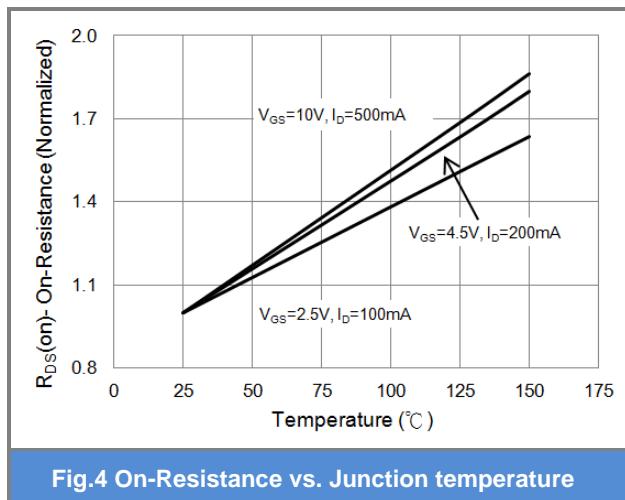
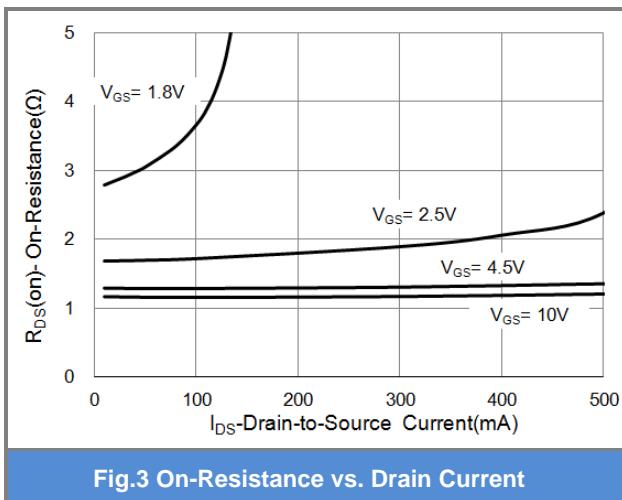
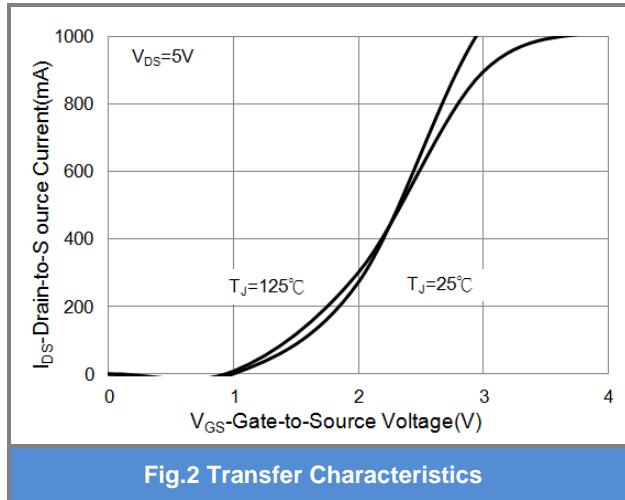
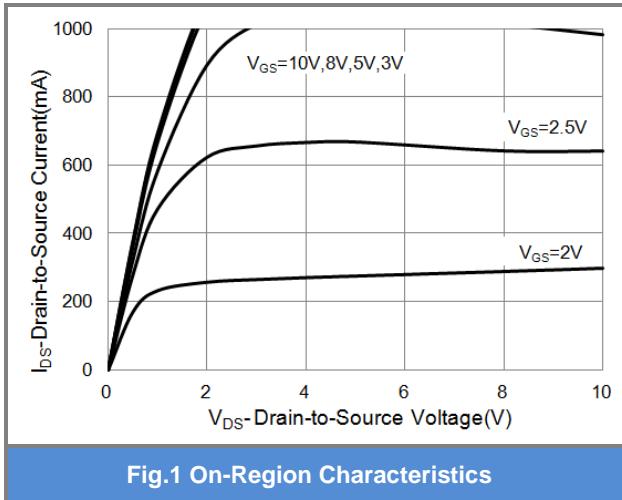
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	50	-	-	V
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	0.5	0.86	1.0	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=500\text{mA}$	-	1.2	1.45	$\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=200\text{mA}$	-	1.3	1.95	
		$\text{V}_{\text{GS}}=2.5\text{V}, \text{I}_D=100\text{mA}$	-	1.7	4.0	
		$\text{V}_{\text{GS}}=1.8\text{V}, \text{I}_D=10\text{mA}$	-	4.0	6.0	
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=50\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate-Source Leakage Current	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	$\pm 10$	$\mu\text{A}$
<b>Dynamic</b> (Note 4)						
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=25\text{V}, \text{I}_D=500\text{mA}, \text{V}_{\text{GS}}=4.5\text{V}$	-	0.95	-	nC
Gate-Source Charge	$\text{Q}_{\text{gs}}$		-	0.34	-	
Gate-Drain Charge	$\text{Q}_{\text{gd}}$		-	0.32	-	
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$	-	36	-	pF
Output Capacitance	$\text{C}_{\text{oss}}$		-	11	-	
Reverse Transfer Capacitance	$\text{Crss}$		-	6.6	-	
Turn-On Delay Time	$\text{t}_{\text{d(on)}}$	$\text{V}_{\text{DD}}=25\text{V}, \text{I}_D=500\text{mA}, \text{V}_{\text{GS}}=10\text{V}, R_{\text{G}}=6\Omega$ (Note 1,2)	-	2.3	-	ns
Turn-On Rise Time	$\text{tr}$		-	20	-	
Turn-Off Delay Time	$\text{t}_{\text{d(off)}}$		-	7	-	
Turn-Off Fall Time	$\text{tf}$		-	20	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$\text{I}_s$	---	-	-	500	$\text{mA}$
Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{I}_s=500\text{mA}, \text{V}_{\text{GS}}=0\text{V}$	-	0.9	1.5	V

### NOTES :

1. Pulse width  $\leq 300\text{us}$ , Duty cycle  $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3.  $R_{\text{QJA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
4. Guaranteed by design, not subject to production testing.

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## TYPICAL CHARACTERISTIC CURVES



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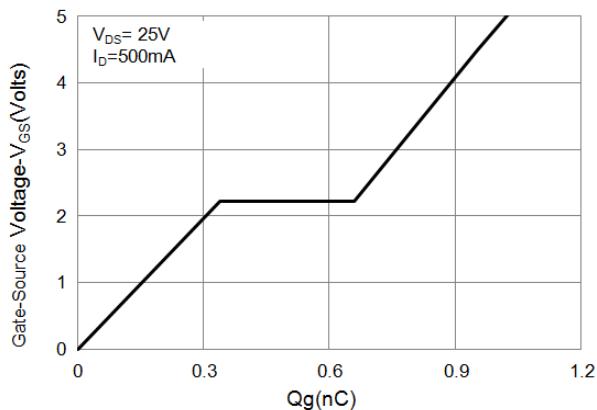


Fig.7 Gate-Charge Characteristics

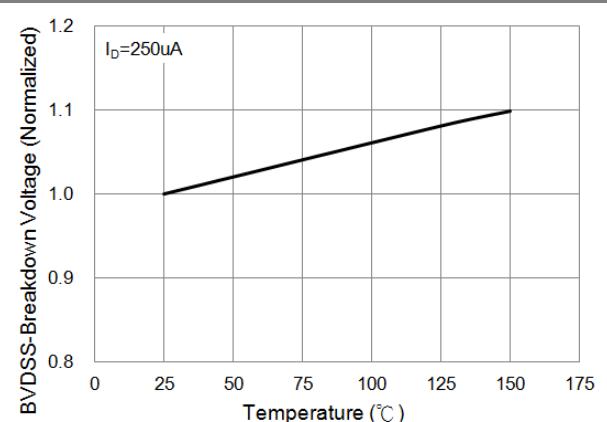


Fig.8 Breakdown Voltage Variation vs. Temperature

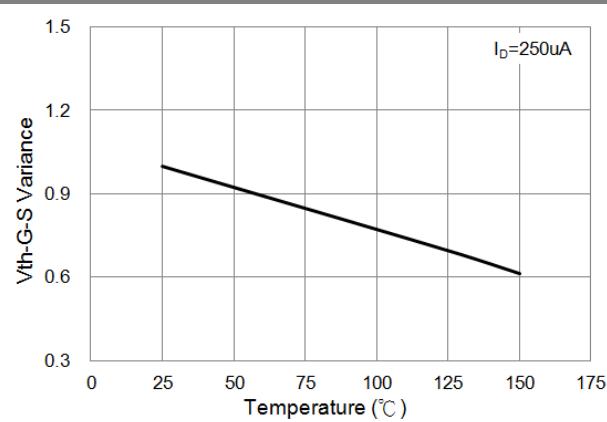


Fig.9 Threshold Voltage Variation with Temperature.

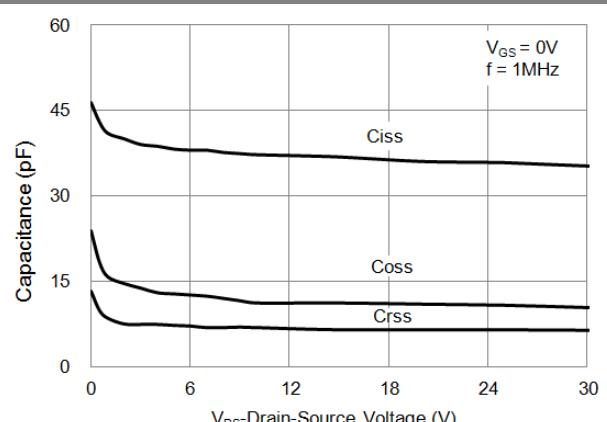


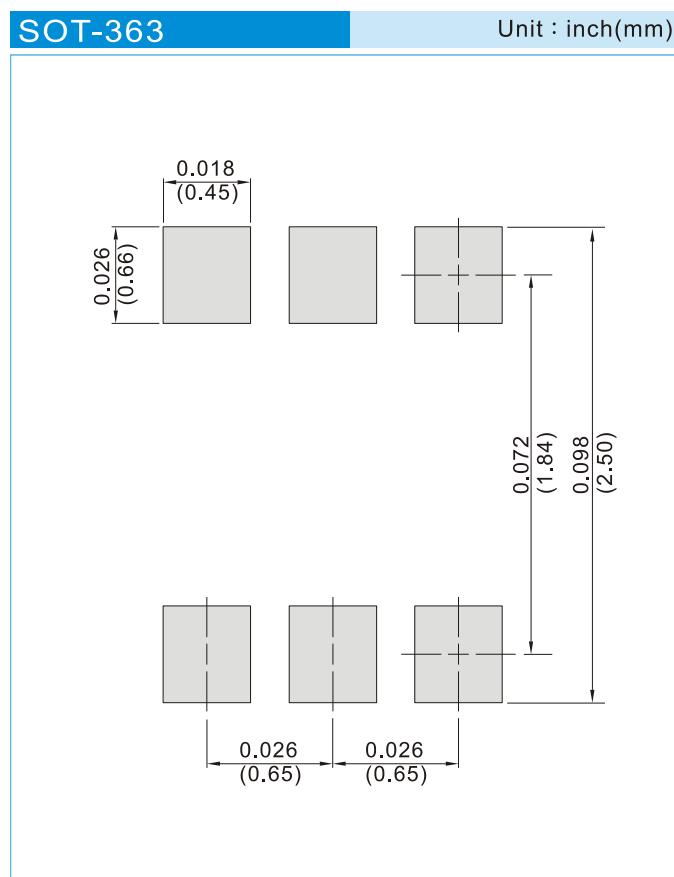
Fig.10 Capacitance vs. Drain-Source Voltage.

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## Product and Packing Information

Part No.	Package Type	Packing type	Marking
PJT7838	SOT-363	3K pcs / 7" reel	T38
PJT7838	SOT-363	10K pcs / 13" reel	T38

## Mounting Pad Layout



## PJT7838

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