



# PJSD03LCFN2

## Bi-Directional ESD Protection Diode

**V<sub>RWM</sub>**      **3.3 V**

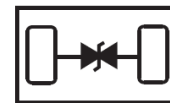
### Features

- 40W Power Dissipation (8/20us Waveform)
- Low Leakage Current, Maximum of 2.5uA@3.3Vdc
- Very low Clamping voltage
- IEC 61000-4-2 ESD 30kV air, 30kV Contact Compliance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : DFN1006-2L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0006 grams

DFN1006-2L



## Maximum Ratings and Thermal Characteristics (T<sub>A</sub> = 25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Peak Pulse Power (8/20us Waveform)	P <sub>PP</sub>	40	W
Peak Pulse Current (8/20us Waveform)	I <sub>PPM</sub>	6	A
Operating Junction Temperature Range	T <sub>J</sub>	-55~150	°C
Storage Temperature Range	T <sub>STG</sub>	-55~150	°C



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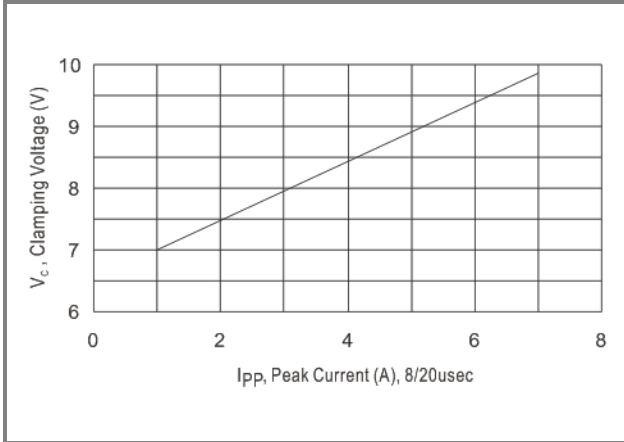
## Electrical Characteristics (T<sub>A</sub> = 25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Reverse Stand-Off Voltage	V <sub>RWM</sub>	-	-	-	3.3	V
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>BR</sub> = 1 mA	5.4	-	7	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>R</sub> = 3.3 V	-	-	2.5	uA
Clamping Voltage(8/20us)	V <sub>C</sub>	I <sub>PP</sub> = 6 A	-	-	10	V
Off State Junction Capacitance	C <sub>J</sub>	0 Vdc Bias f = 1 MHz	-	-	25	pF

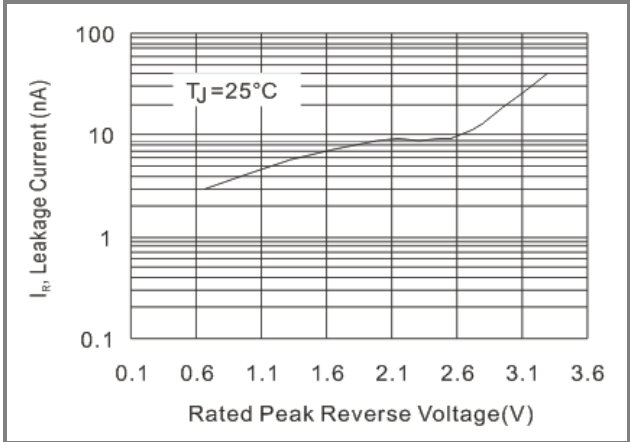


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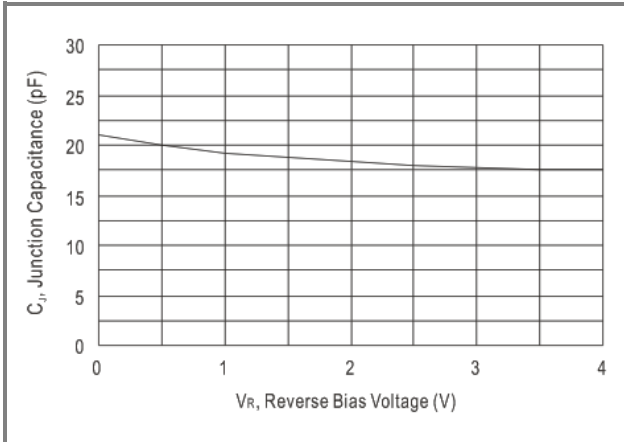
## TYPICAL CHARACTERISTIC CURVES



**Fig.1 Typical Peak Clamping Voltage**



**Fig.2 Typical Reverse Characteristics**



**Fig.3 Typical Junction Capacitance**

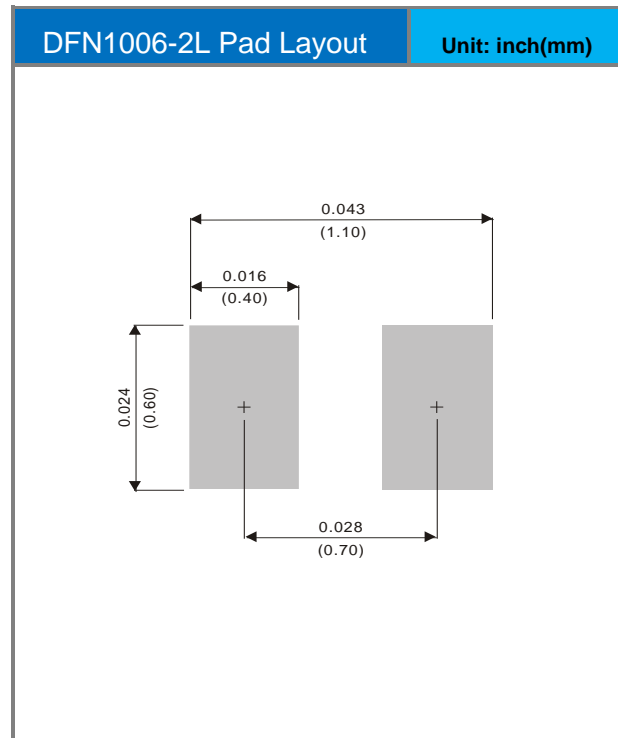
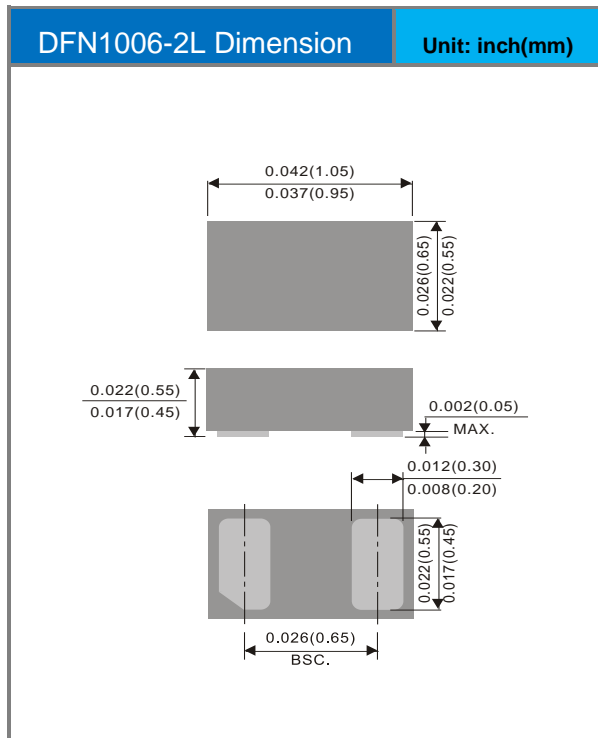


# PJSD03LCFN2

## Product and Packing Information

Part No.	Package Type	Packing Type	Marking	Version
PJSD03LCFN2	DFN1006-2L	10K pcs / 7" Reel	BS	Halogen free RoHS compliant

## Packaging Information & Mounting Pad Layout





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