

# PJQ5848-AU

## 40V Dual N-Channel Enhancement Mode MOSFET

**Voltage**

**40 V**

**Current**

**30 A**

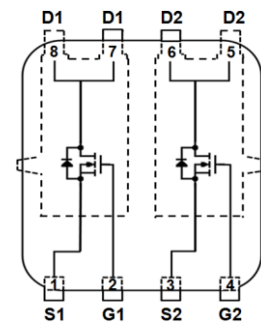
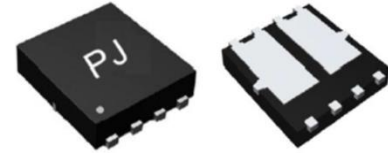
### Features

- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@12A<12m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@4.5V$ ,  $I_D@8A<16m\Omega$
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : DFN5060B-8L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0035 ounces, 0.092 grams

DFN5060B-8L



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		$V_{DS}$	40	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	
Continuous Drain Current (Note 4)	$T_C=25^\circ\text{C}$	$I_D$	30	A
	$T_C=100^\circ\text{C}$		19	
Pulsed Drain Current (Note 1)	$T_C=25^\circ\text{C}$	$I_{DM}$	100	
Power Dissipation	$T_C=25^\circ\text{C}$	$P_D$	23.8	W
	$T_C=100^\circ\text{C}$		11.9	
Continuous Drain Current (Note 4)	$T_A=25^\circ\text{C}$	$I_D$	8.6	A
	$T_A=70^\circ\text{C}$		7	
Power Dissipation	$T_A=25^\circ\text{C}$	$P_D$	2.0	W
	$T_A=70^\circ\text{C}$		1.4	
Single Pulse Avalanche Energy (Note 6)		$E_{AS}$	62	mJ
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~175	$^\circ\text{C}$
Typical Thermal Resistance (Note 4,5)	Junction to Case	$R_{\theta JC}$	6.3	$^\circ\text{C/W}$
	Junction to Ambient	$R_{\theta JA}$	73.5	

- Limited only By Maximum Junction Temperature

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## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

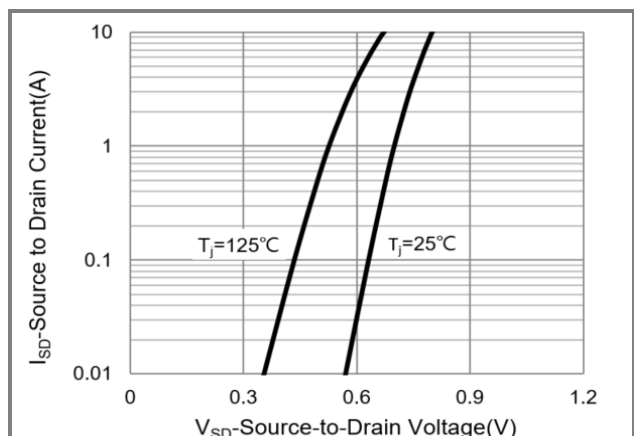
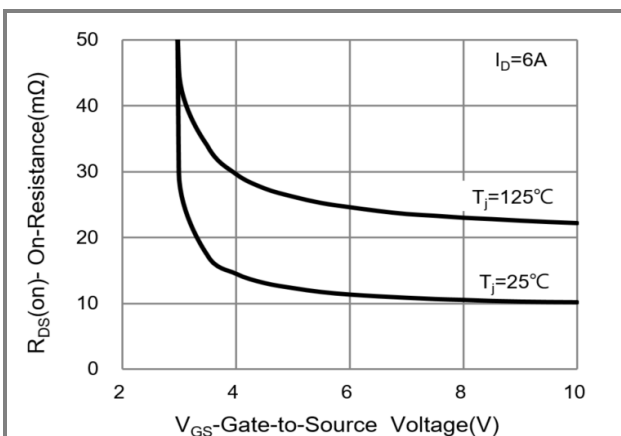
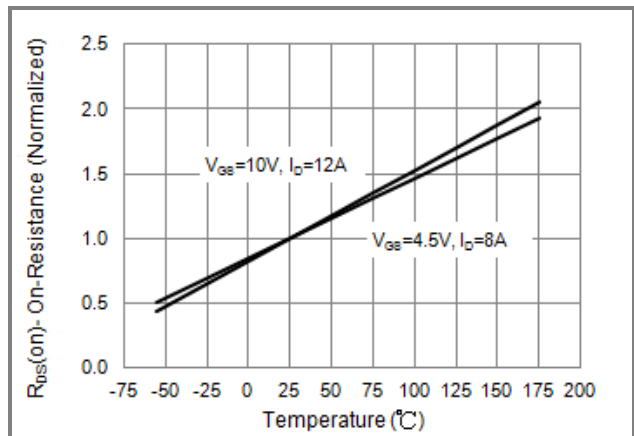
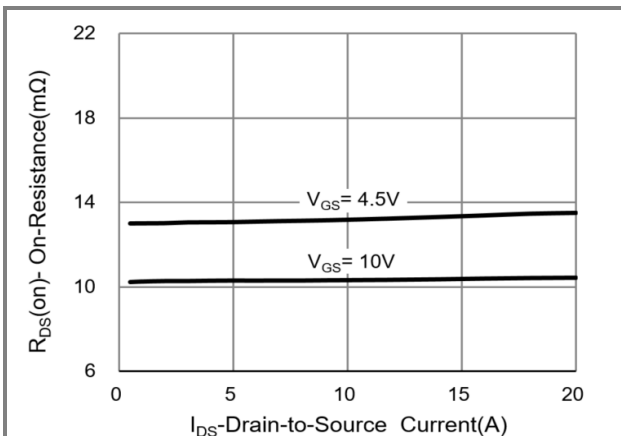
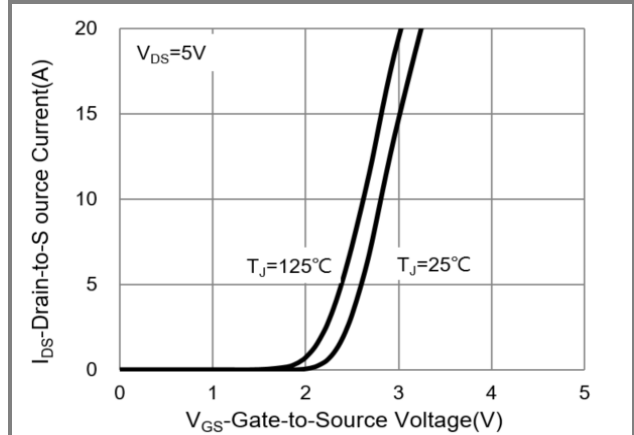
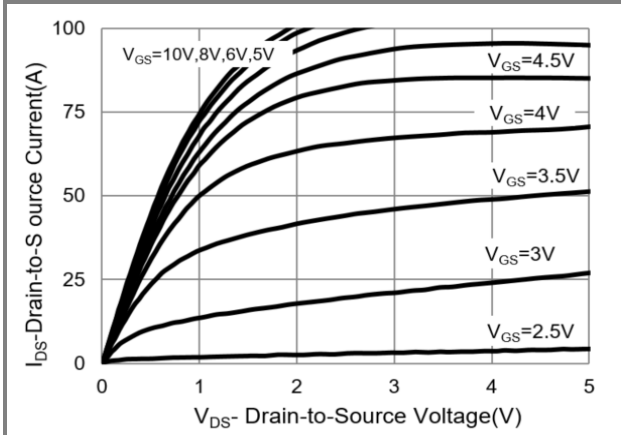
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	40	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1	1.7	2.5	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =12A	-	10	12	mΩ
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =8A	-	13	16	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	-	-	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>Dynamic</b> (Note 7)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =20V, I <sub>D</sub> =10A, V <sub>GS</sub> =4.5V (Note 2,3)	-	10	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	3.5	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	3.6	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1MHZ	-	1040	-	pF
Output Capacitance	C <sub>oss</sub>		-	117	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	84	-	
Turn-On Delay Time	td(on)	V <sub>DS</sub> =20V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω (Note 2,3)	-	9.4	-	ns
Turn-On Rise Time	tr		-	19	-	
Turn-Off Delay Time	td(off)		-	66	-	
Turn-Off Fall Time	tf		-	67	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>	---	-	-	30	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V	-	0.7	1	V

**NOTES :**

1. Pulse width ≤ 300us, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> = 25°C.
4. The maximum current rating is package limited.
5. R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. The test condition is L=0.1mH, I<sub>AS</sub>=35A, V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, Starting T<sub>J</sub>=25°C.
7. Guaranteed by design, not subject to production testing.

# PJQ5848-AU

## TYPICAL CHARACTERISTIC CURVES



# PJQ5848-AU

## TYPICAL CHARACTERISTIC CURVES

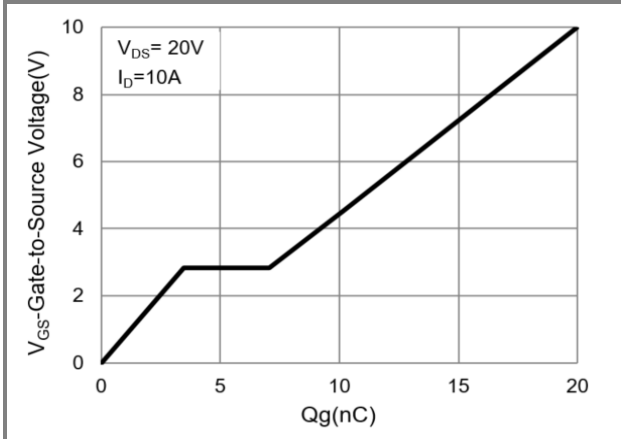


Fig.7 Gate-Charge Characteristics

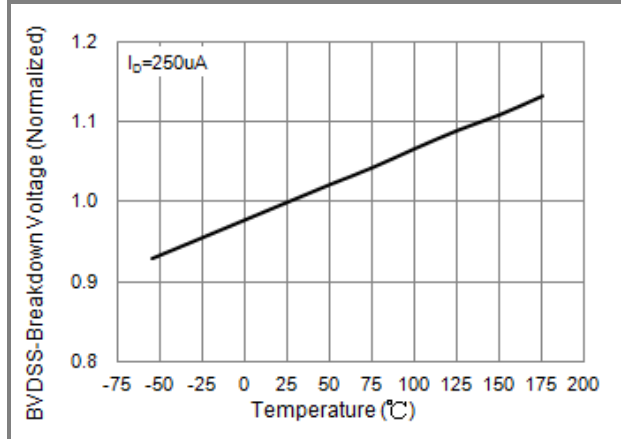


Fig.8 Breakdown Voltage Variation vs. Temperature

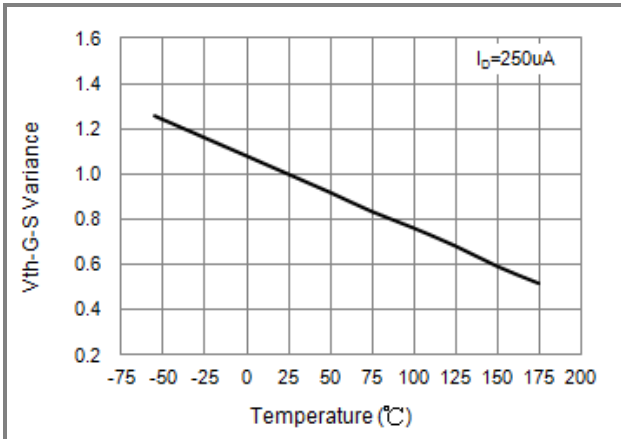


Fig.9 Threshold Voltage Variation with Temperature

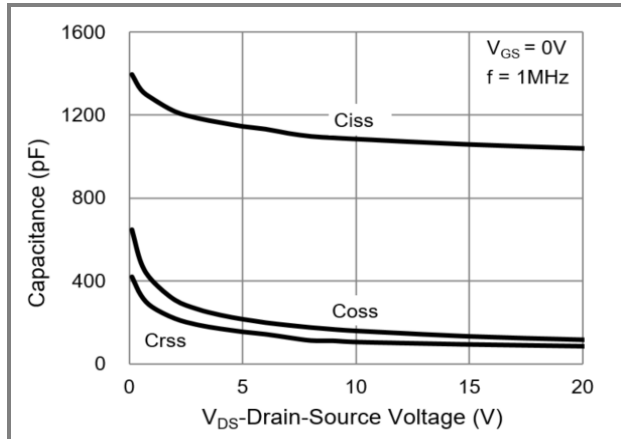


Fig.10 Capacitance vs. Drain-Source Voltage

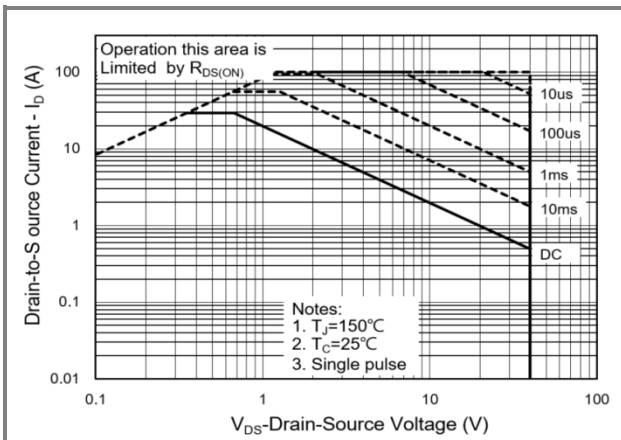


Fig.11 Maximum Safe Operating Area

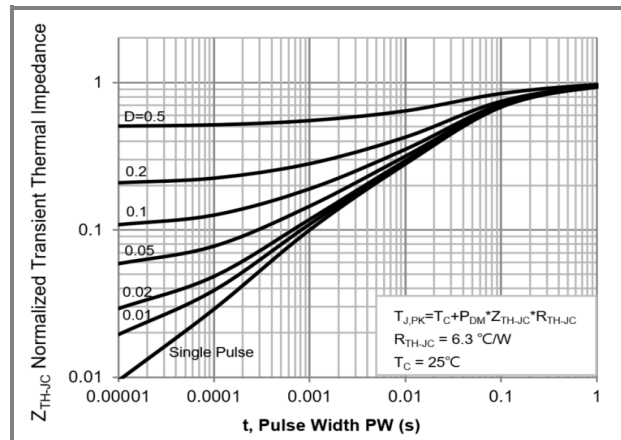


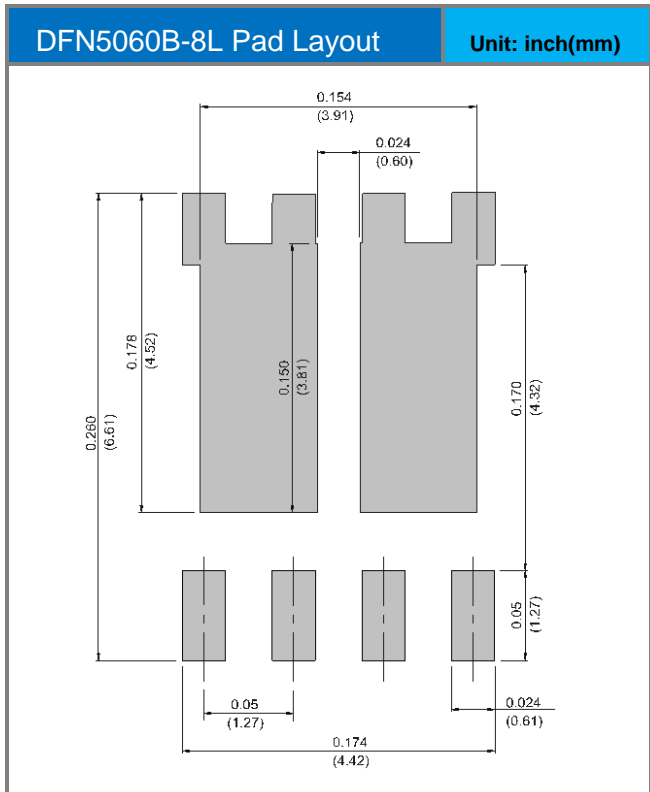
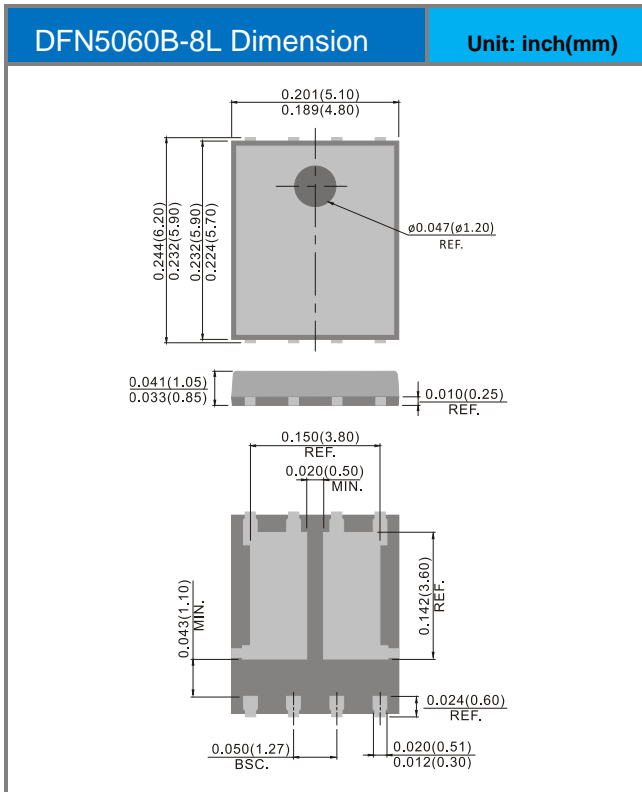
Fig.12 Normalized Transient Thermal Impedance

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## Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJQ5848-AU	DFN5060B-8L	3000pcs / 13" reel	Q5848

## Packaging Information & Mounting Pad Layout



## PJQ5848-AU

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