

PJQ4410P

30V N-Channel Enhancement Mode MOSFET

Voltage

30 V

Current

35 A

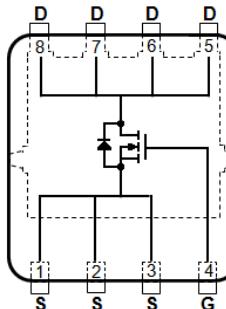
Features

- $R_{DS(ON)}$, $V_{GS} @ 10V, I_D @ 10A < 12m\Omega$
- $R_{DS(ON)}$, $V_{GS} @ 4.5V, I_D @ 5A < 18m\Omega$
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : DFN3333-8L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.03 grams

DFN3333-8L



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	35	A
		23	
Pulsed Drain Current ^(Note 1)	I_{DM}	140	
Power Dissipation	P_D	27	W
		11	
Continuous Drain Current	I_D	10	A
		8	
Power Dissipation	P_D	2.0	W
Power Dissipation		1.3	
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ C$
Typical Thermal Resistance ^(Note 4,5)	Junction to Case	$R_{\theta JC}$	$^\circ C/W$
	Junction to Ambient	$R_{\theta JA}$	

- Limited only By Maximum Junction Temperature

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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	30	-	-	V
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1.0	1.53	2.5	
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=10\text{A}$	-	9.7	12	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=5\text{A}$	-	13	18	
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1.0	μA
Gate-Source Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Dynamic ^(Note 6)						
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=15\text{V}, \text{I}_D=5\text{A}, \text{V}_{\text{GS}}=4.5\text{V}$ ^(Note 3)	-	7.1	-	nC
Gate-Source Charge	Q_{gs}		-	2.0	-	
Gate-Drain Charge	Q_{gd}		-	2.8	-	
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1.0\text{MHZ}$	-	660	-	pF
Output Capacitance	C_{oss}		-	92	-	
Reverse Transfer Capacitance	Crss		-	71	-	
Turn-On Delay Time	$\text{t}_{\text{d(on)}}$	$\text{V}_{\text{DD}}=15\text{V}, \text{I}_D=1\text{A}, \text{V}_{\text{GS}}=10\text{V}, \text{R}_G=6\Omega$ ^(Note 3)	-	6.7	-	ns
Turn-On Rise Time	t_r		-	11	-	
Turn-Off Delay Time	$\text{t}_{\text{d(off)}}$		-	27	-	
Turn-Off Fall Time	t_f		-	8.3	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_s	---	-	-	35	A
Diode Forward Voltage	V_{SD}	$\text{I}_s=1\text{A}, \text{V}_{\text{GS}}=0\text{V}$	-	0.71	1.0	V

NOTES :

1. Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature $\text{TJ}(\text{MAX})=150^\circ\text{C}$. Ratings are based on low frequency and duty cycles to keep initial $\text{TJ}=25^\circ\text{C}$.
4. The maximum current rating is package limited.
5. R_{QJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES

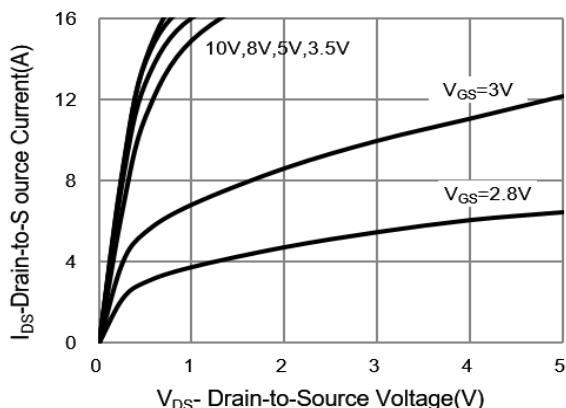


Fig.1 On-Region Characteristics

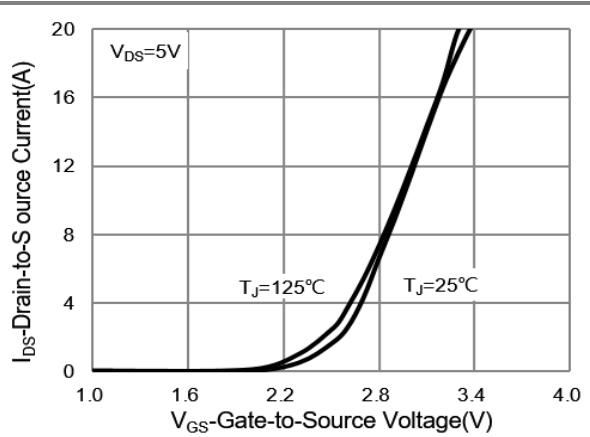


Fig.2 Transfer Characteristics

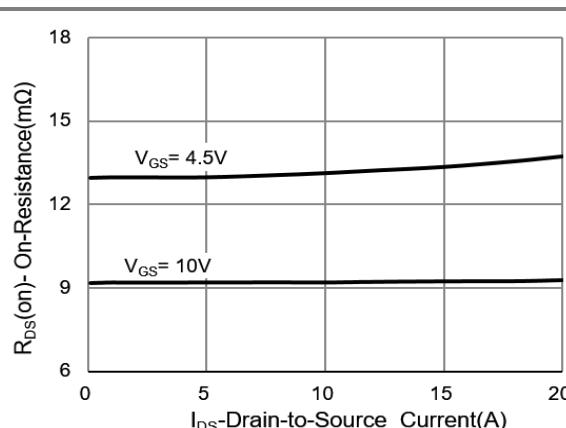


Fig.3 On-Resistance vs. Drain Current

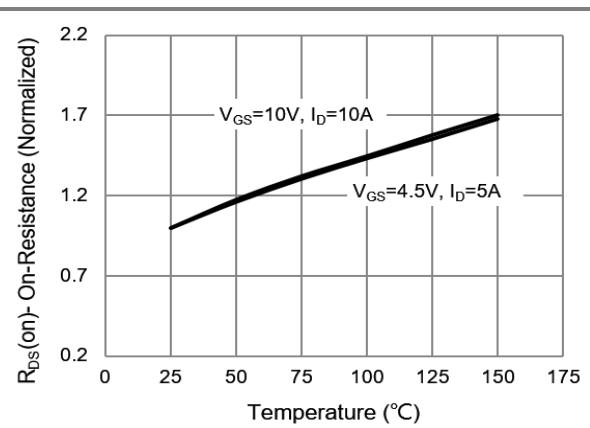


Fig.4 On-Resistance vs. Junction temperature

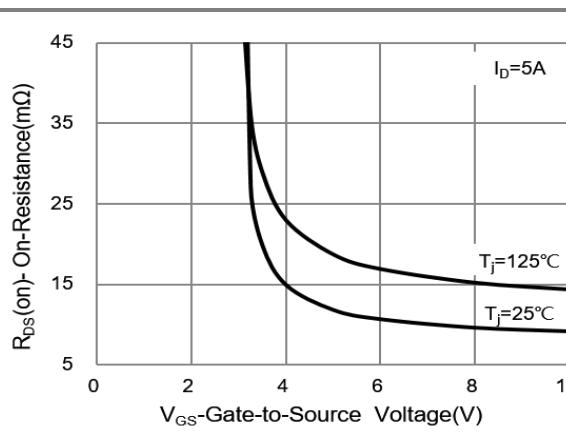


Fig.5 On-Resistance Variation with VGS.

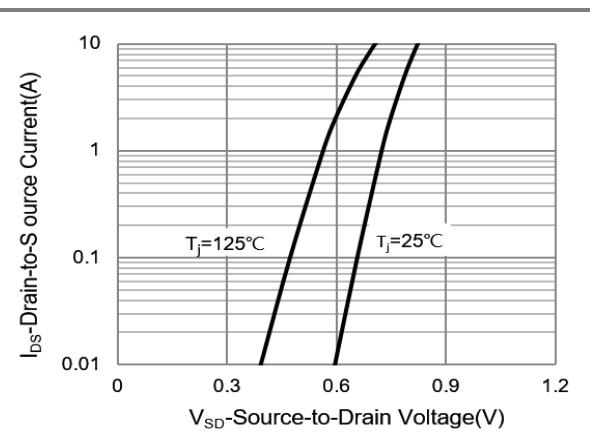


Fig.6 Source-Drain Diode Forward Voltage

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TYPICAL CHARACTERISTIC CURVES

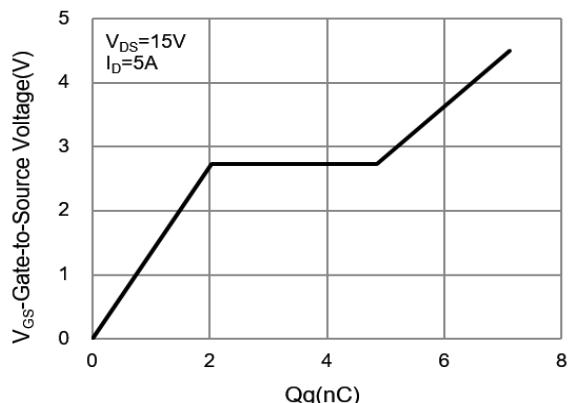


Fig.7 Gate-Charge Characteristics

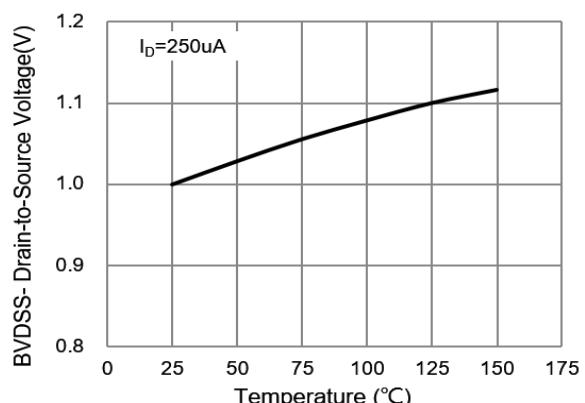


Fig.8 Breakdown Voltage Variation vs. Temperature.

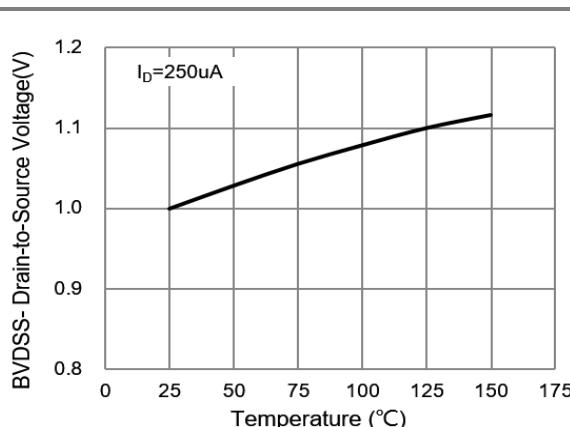


Fig.9 Threshold Voltage Variation with Temperature

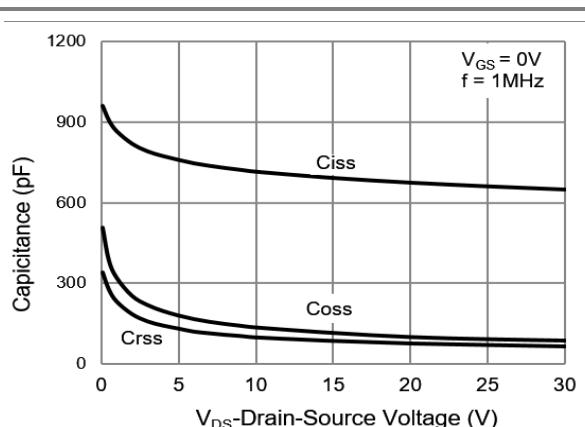


Fig.10 Capacitance vs. Drain-Source Voltage.

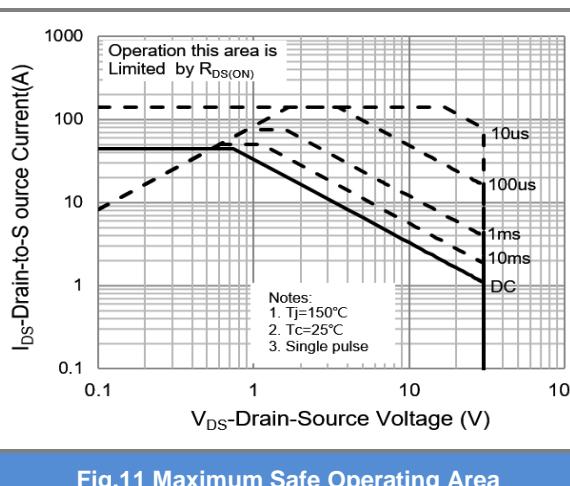
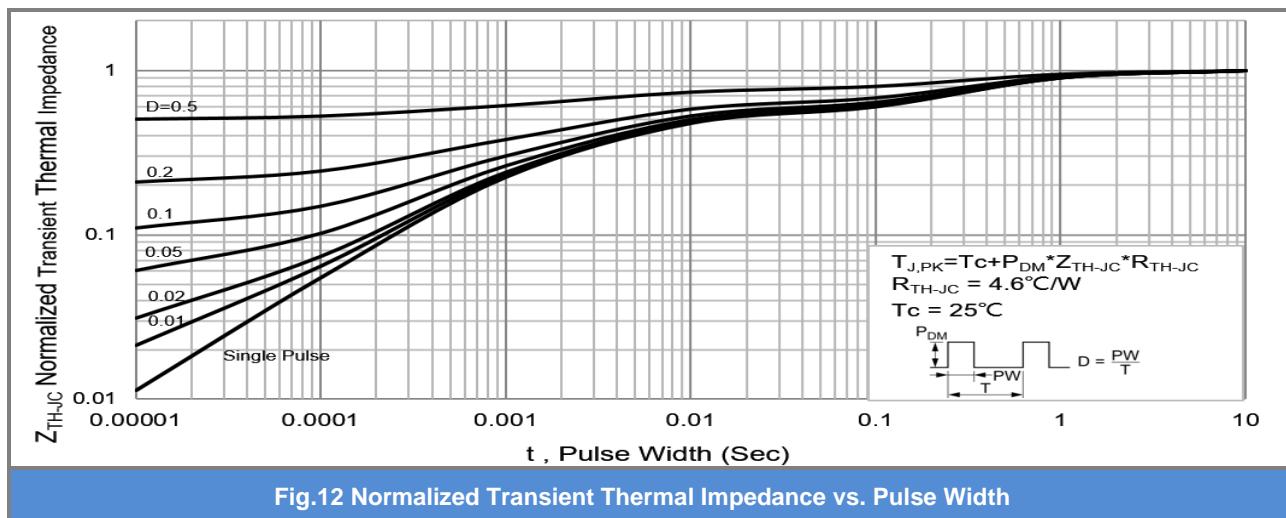


Fig.11 Maximum Safe Operating Area

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TYPICAL CHARACTERISTIC CURVES

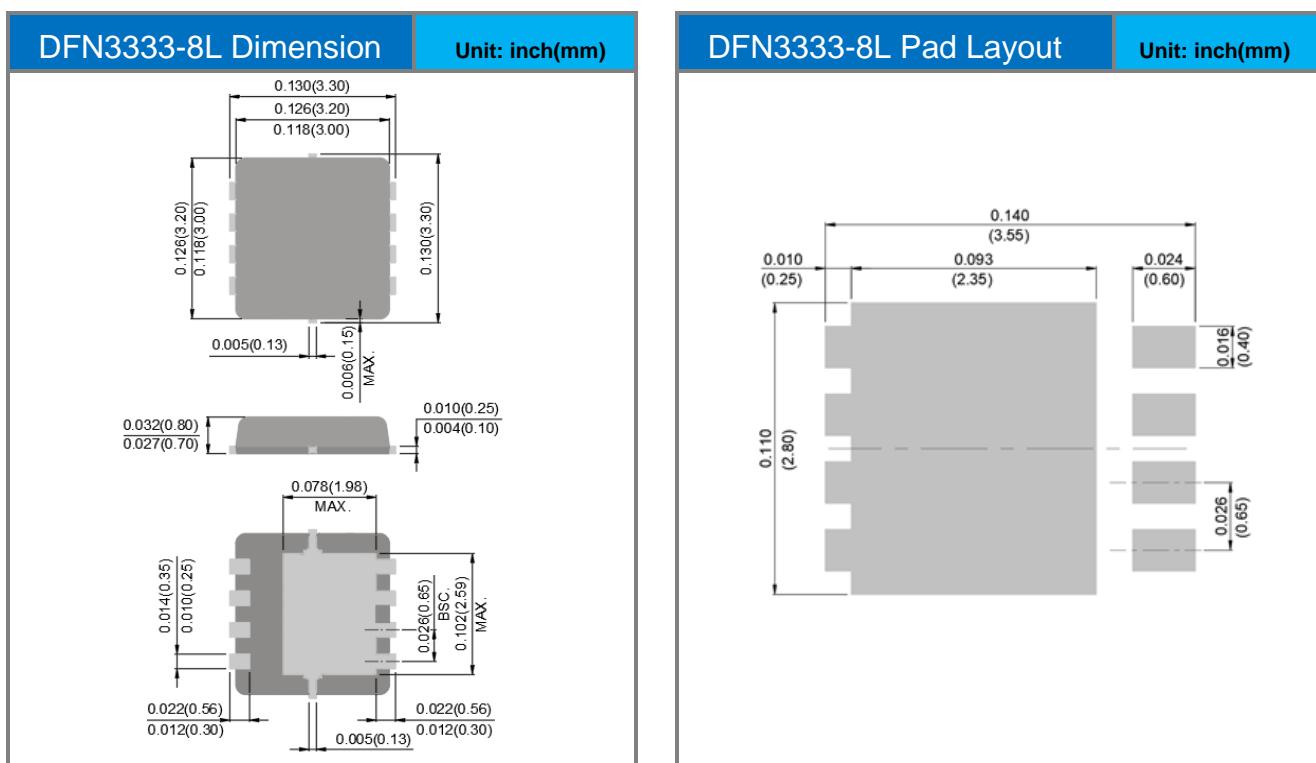


PJQ4410P

Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ4410P_R2_00001	DFN3333-8L	5K pcs / 13" reel	4410	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout



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