

600V N-Channel Super Junction MOSFET

Voltage	600 V	R _{dson}	42 mΩ
Current	69 A	Q _g	145 nC

Feature:

- R_{DSON} Max, V_{GS}@10V: 42mΩ
- Fast recovery Qrr/Trr performance
- High Speed Switching and Low R_{DSON}
- 100% Avalanche Tested
- 100% R_g Tested
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

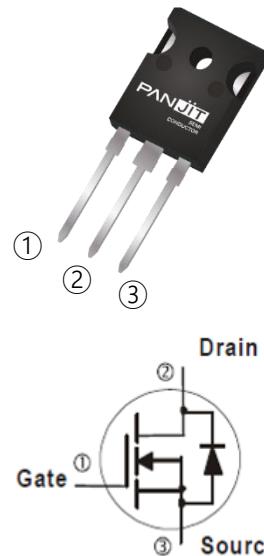
Mechanical Data

- Case: TO-247AD-3LD package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 6.231 grams

Application

- PFC of Industrial Power/ Server Power / Telecom Power

TO-247AD-3LD



Absolute Maximum Ratings (T_A = 25 °C unless otherwise specified)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage @ T _{jmax}	V _{DS}	650	V
Drain-Source Voltage	V _{DS}	600	
Gate-Source Voltage	V _{GS}	±30	
Continuous Drain Current	I _D	69	A
		43	
Pulsed Drain Current	I _{DM}	223	A
Single Pulse Avalanche Energy ^(Note 6)	E _{AS}	1950	mJ
MOSFET dv/dt ruggedness ^(Note 7)	dv/dt	120	V/ns
Reverse diode dv/dt ^(Note 8)	dv/dt	100	V/ns
Maximum diode commutation speed	di _F /dt	1200	A/μs
Power Dissipation	P _D	500	W
		200	
Operating Junction and Storage Temperature Range	T _{J,T STG}	-55~150	°C

Thermal Characteristics

PARAMETER	SYMBOL	MAXIMUM	UNITS
Thermal Resistance	R _{θJC}	0.25	°C/W
	R _{θJA}	50	°C/W

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	600	710	-	V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	4.3	4.8	5.8	
Drain-Source On-State Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=40.5\text{A}$ (Note 1)	-	36	42	$\text{m}\Omega$
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=600\text{V}, V_{\text{GS}}=0\text{V}$	-	-	10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 30\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Transfer characteristics	g_{fs}	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=40.5\text{A}$	-	73	-	S
Dynamic (Note 5)						
Total Gate Charge	Q_g	$V_{\text{DS}}=480\text{V}, I_{\text{D}}=40.5\text{A}, V_{\text{GS}}=10\text{V}$	-	145	-	nC
Gate-Source Charge	Q_{gs}		-	52	-	
Gate-Drain Charge	Q_{gd}		-	58	-	
Input Capacitance	C_{iss}	$V_{\text{DS}}=400\text{V}, V_{\text{GS}}=0\text{V}, f=100\text{kHz}$	-	7400	-	pF
Output Capacitance	C_{oss}		-	170	-	
Reverse Transfer Capacitance	C_{rss}		-	2	-	
Effective Output Capacitance Energy Related	$C_{\text{o(er)}}$	$V_{\text{DS}}=0\text{V}$ to 400V , $V_{\text{GS}}=0\text{V}, f=100\text{kHz}$ (Note 4)	-	268	-	
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=300\text{V}, I_{\text{D}}=40.5\text{A}, V_{\text{GS}}=10\text{V}, R_{\text{G}}=25\Omega$ (Note 2)	-	248	-	ns
Turn-On Rise Time	t_r		-	89	-	
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	412	-	
Turn-Off Fall Time	t_f		-	105	-	
Gate Resistance	R_g	$f=1.0\text{MHz}$	-	13	-	Ω
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_s		-	-	69	A
Diode Forward Voltage	V_{SD}	$I_s=40.5\text{A}, V_{\text{GS}}=0\text{V}$	-	0.9	1.5	V
Reverse Recovery Charge	Q_{rr}	$I_s=40.5\text{A}, V_{\text{DD}}=400\text{V}$ $dI/dt=100\text{A}/\mu\text{s}$	-	1.3	-	μC
Reverse Recovery Time	T_{rr}		-	181	-	ns

NOTES :

1. Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.
2. Essentially independent of operating temperature typical characteristics.
3. R_{\thetaJA} is the sum of the junction-to-case and case-to-ambient thermal resistance.
4. $C_{\text{o(er)}}$ is a capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0V to 80% $V_{(\text{BR})\text{DSS}}$.
5. Guaranteed by design, not subject to production testing.
6. E_{AS} is calculated based on the condition of $I_{\text{D}}=14\text{A}$; $V_{\text{DD}}=50\text{V}$; $L=20\text{mH}$.
7. $V_{\text{DS}}=0\ldots 400\text{V}$.
8. $V_{\text{DS}}=0\ldots 400\text{V}, I_{\text{SD}}=40.5\text{A}$.

TYPICAL CHARACTERISTIC CURVES

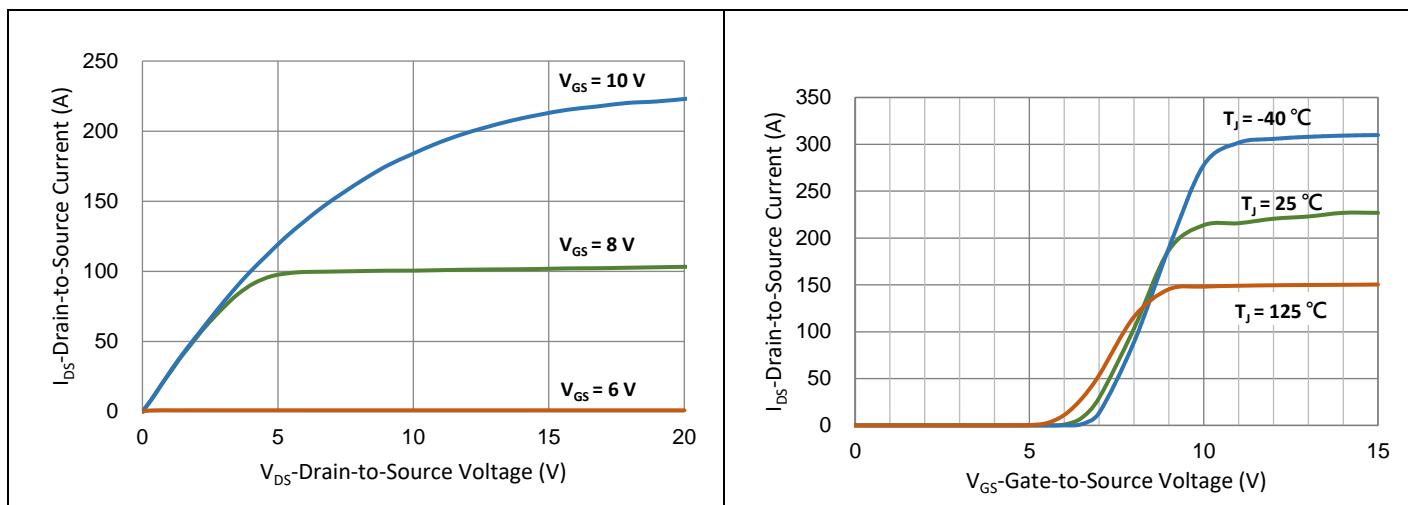


Fig.1 Output Characteristics

Fig.2 Transfer Characteristics

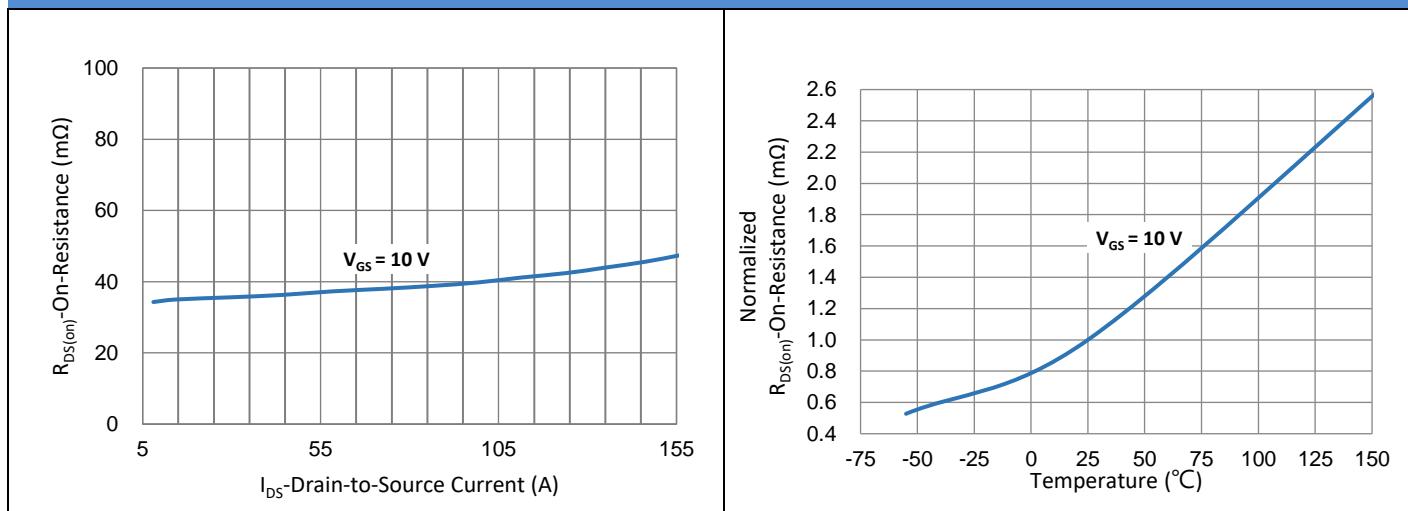


Fig.3 On-Resistance vs. Drain Current

Fig.4 On-Resistance vs. Junction Temperature

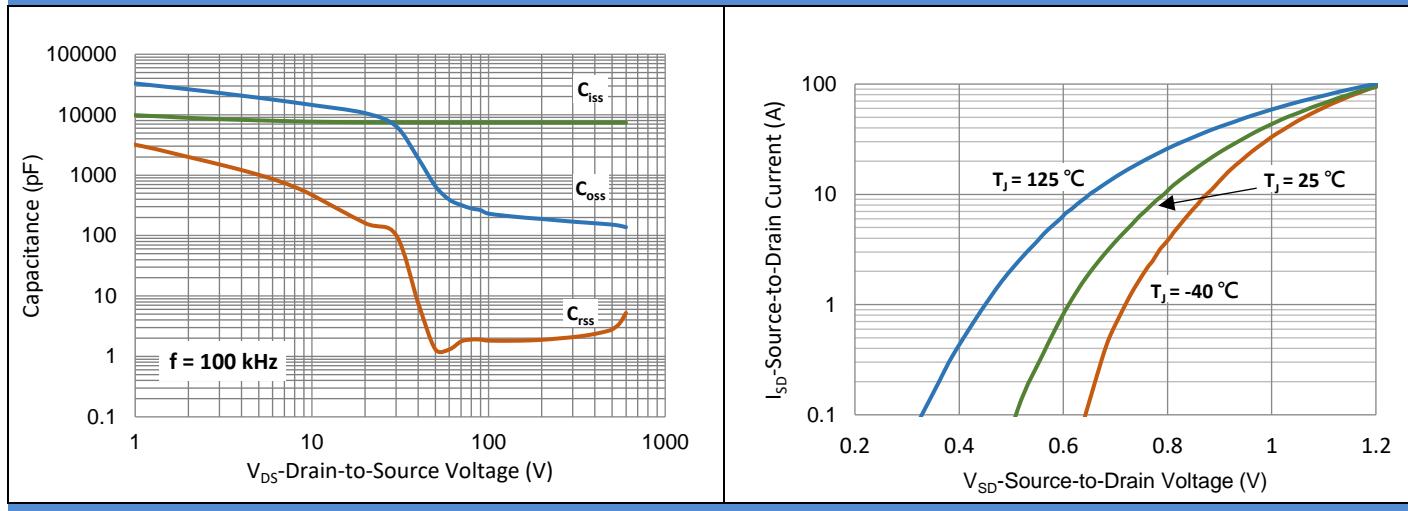
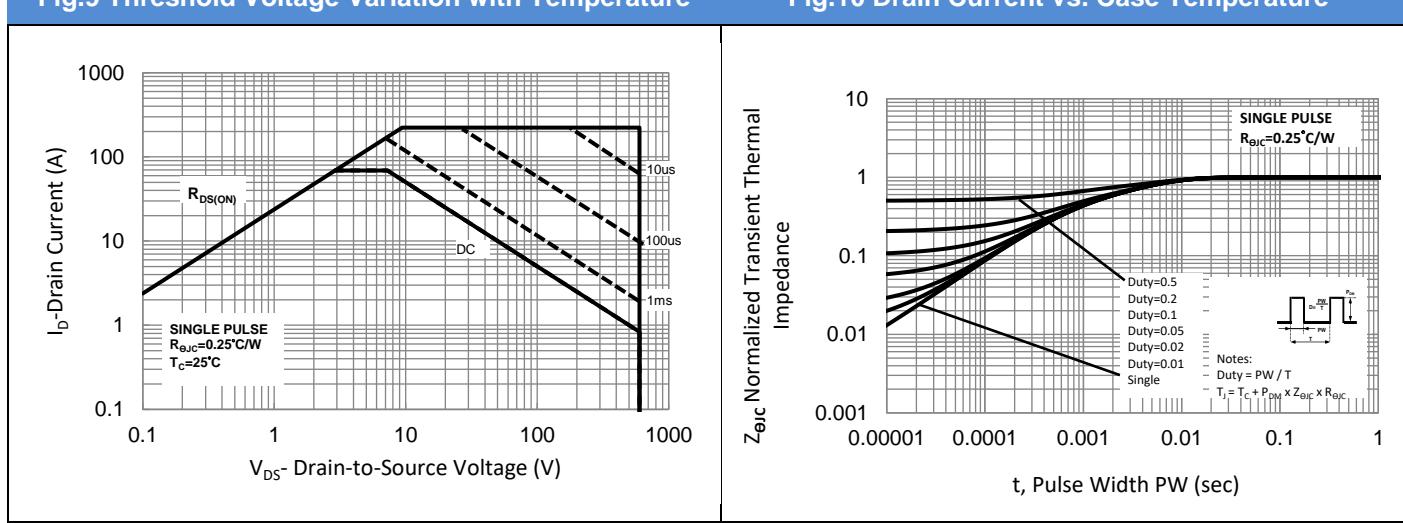
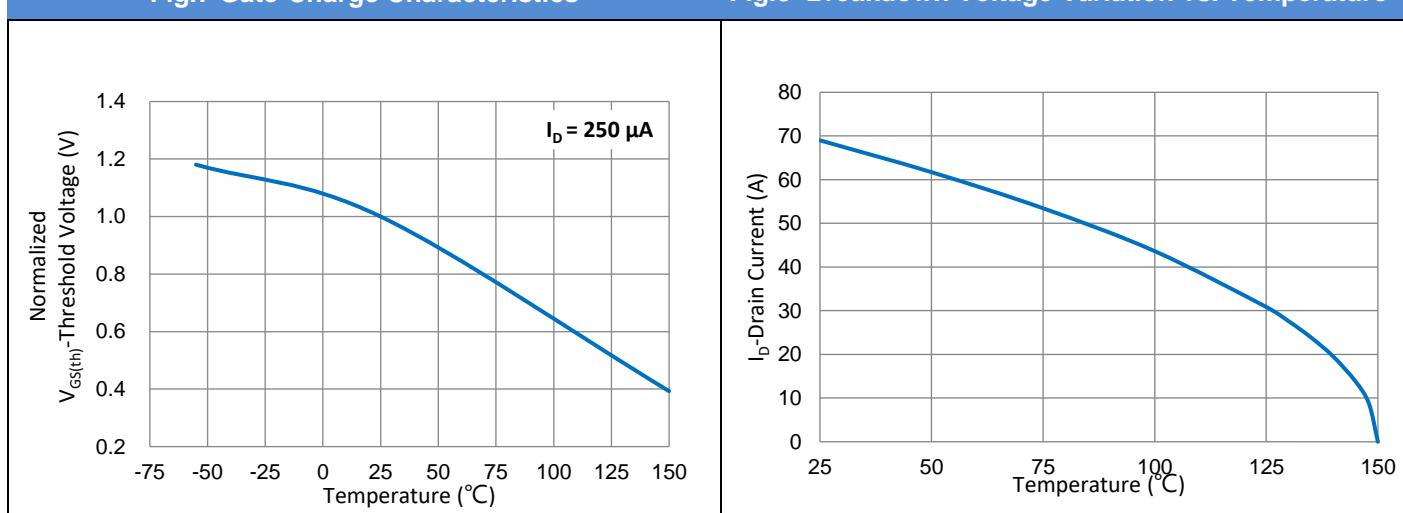
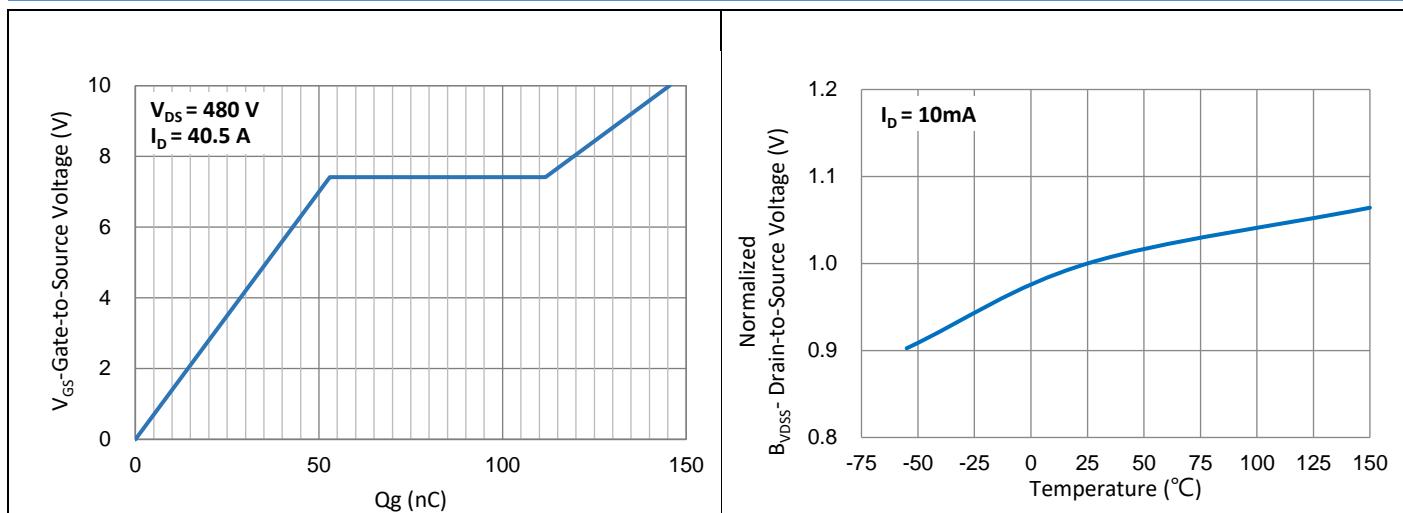


Fig.5 Capacitance vs. Drain-Source Voltage

Fig.6 Source-Drain Diode Forward Voltage

TYPICAL CHARACTERISTIC CURVES



TYPICAL CHARACTERISTIC CURVES

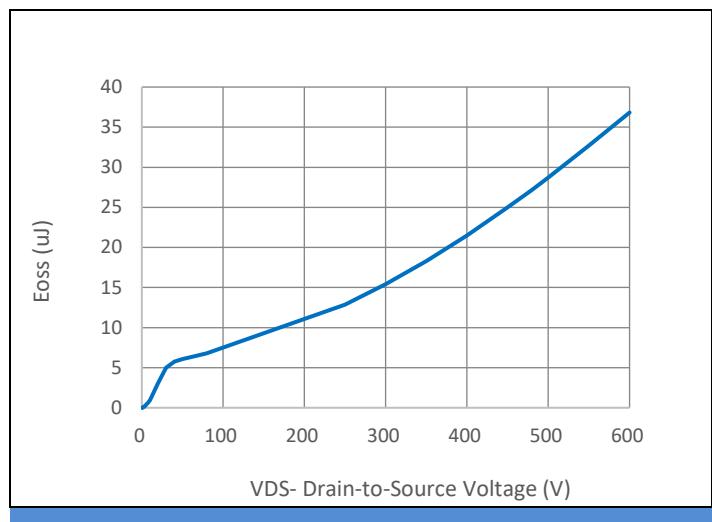
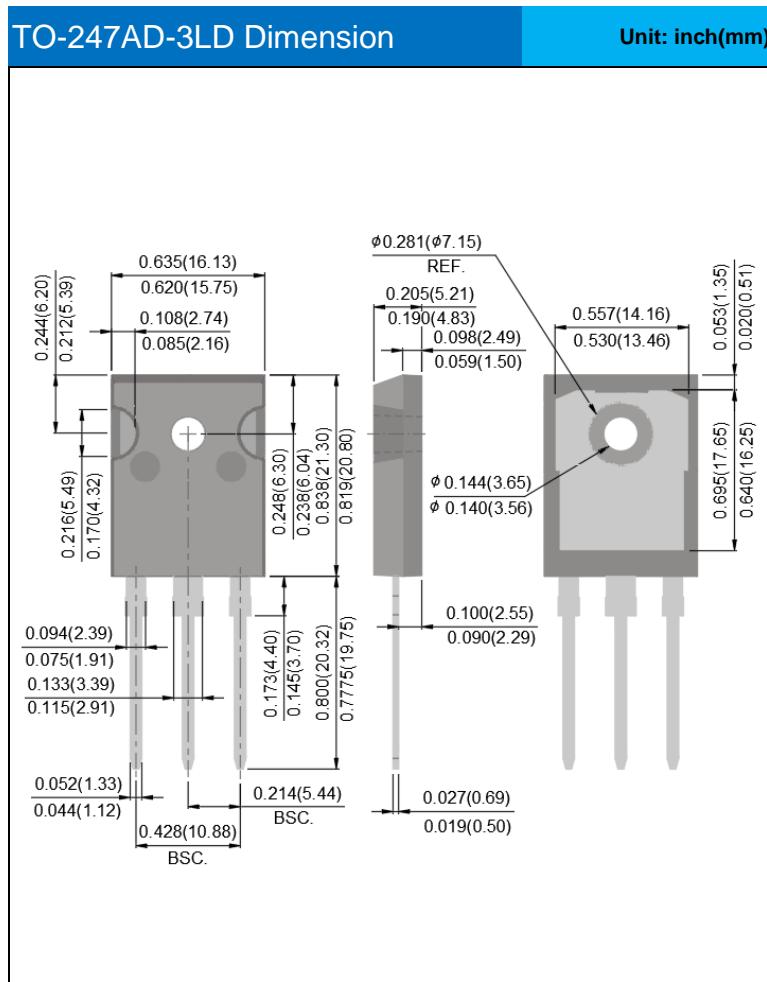


Fig.13 Typ. Coss Stored Energy

Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJMH042N60FRC	TO-247AD-3LD	30pcs / Tube	042N60FRC

Packaging Information



Marking Diagram

PJ
042N60FRC
YWLL x

- PJ** = Year Code
- W** = Week Code (A~Z)
- LL** = Lot Code (00~99)
- x** = Production Line Code

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