

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAM	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 10	V
Continuous Drain Current		ID	500	mA
Pulsed Drain Current ^(Note 4)		I _{DM}	1000	mA
Power Dissipation	T _a =25⁰C	PD	300	mW
	Derate above 25°C		2.4	mW/ºC
Operating Junction and Storage Temperature Range		Tյ,Tsтg	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		R _{0JA}	417	°C/W

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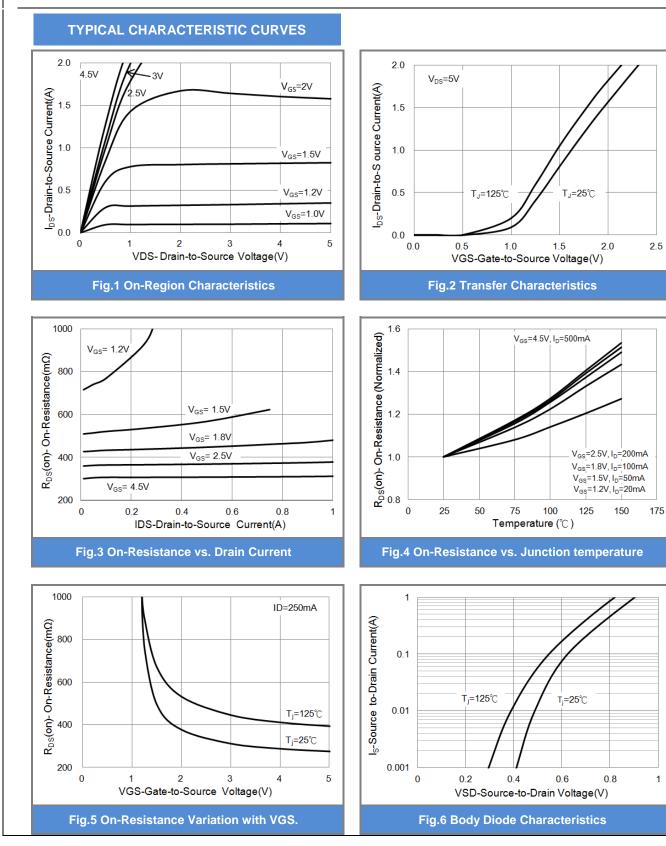
Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =250uA	0.3	0.64	0.9	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =500mA	-	310	400	mΩ
		V_{GS} =2.5V, I_{D} =200mA	-	360	650	
		V _{GS} =1.8V, I _D =100mA	-	430	800	
		V _{GS} =1.5V, I _D =50mA	-	510	1200	
		V _{GS} =1.2V, I _D =20mA	-	710	3000	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =16V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	<u>+</u> 0.5	<u>+</u> 10	uA
Dynamic ^(Note 5)						
Total Gate Charge	Qg		-	1.4	-	nC
Gate-Source Charge	Q_{gs}	V _{DS} =10V, I _D =500mA, V _{GS} =4.5V ^(Note 1,2)	-	0.22	-	
Gate-Drain Charge	Q_gd		-	0.21	-	
Input Capacitance	Ciss		-	67	-	pF
Output Capacitance	Coss	V _{DS} =10V, V _{GS} =0V, f=1.0MHZ	-	19	-	
Reverse Transfer Capacitance	Crss	I=1.0IVIHZ	-	6	-	
Turn-On Delay Time	td _(on)		-	2.8	-	
Turn-On Rise Time	tr	V _{DD} =10V, I _D =150mA, V _{GS} =4.0V,	-	20	-	ns
Turn-Off Delay Time	td _(off)		-	23	-	
Turn-Off Fall Time	tf	R _G =10Ω ^(Note 1,2)	-	23	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	500	mA
Diode Forward Voltage	V _{SD}	Is=500mA, Vgs=0V	-	0.87	1.3	V

NOTES :

- 1. Pulse width <300us, Duty cycle <2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{BJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.





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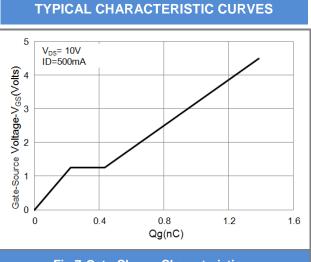


Fig.7 Gate-Charge Characteristics

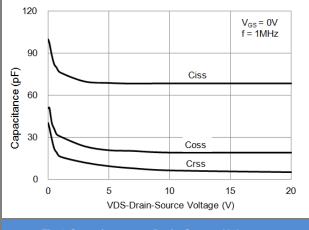


Fig.9 Capacitance vs. Drain-Source Voltage.

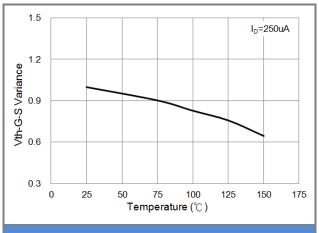


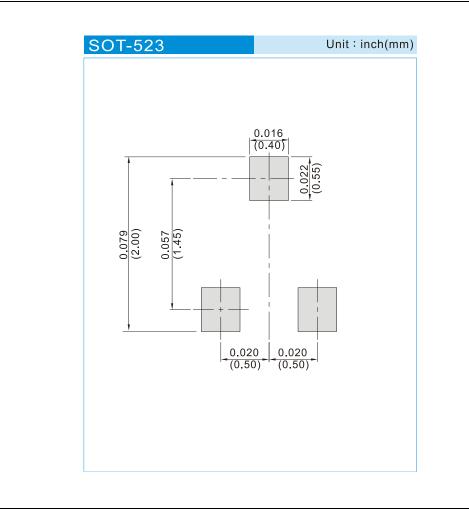
Fig.8 Threshold Voltage Variation with Temperature.



Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJE8408_R1_00001	SOT-523	4K pcs / 7" reel	E08	Halogen free RoHS compliant

Mounting Pad Layout







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