

# PJD13N10A

## 100V N-Channel Enhancement Mode MOSFET

**Voltage**

**100 V**

**Current**

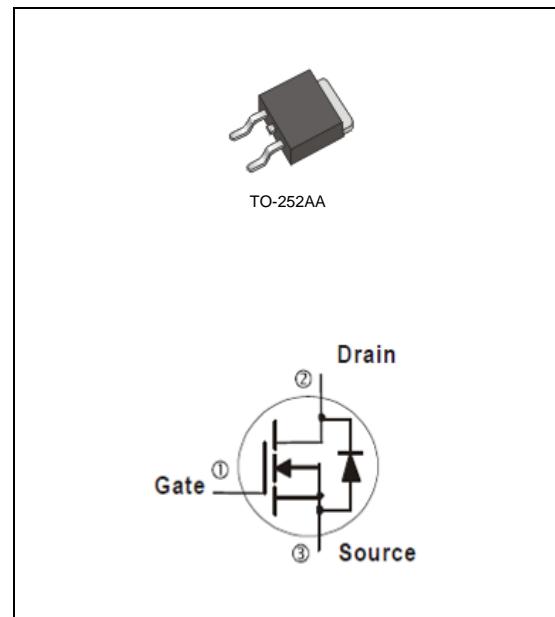
**13A**

### Features

- RDS(ON) , VGS@10V, ID@6.5A<115mΩ
- RDS(ON) , VGS@4.5V, ID@4A<120mΩ
- Advanced Trench Process Technology
- High density cell design for ultra low on-resistance
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std.. (Halogen Free)

### Mechanical Data

- Case: TO-252AA Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0104 ounces, 0.297 grams



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current $T_c=25^\circ\text{C}$	$I_D$	13	A
		8	
Pulsed Drain Current (Note 1)	$I_{DM}$	52	
Power Dissipation $T_c=25^\circ\text{C}$	$P_D$	41	W
		16	
Continuous Drain Current $T_A=25^\circ\text{C}$	$I_D$	2.9	A
		2.3	A
Power Dissipation $T_A=25^\circ\text{C}$	$P_D$	2.0	W
		1.3	
Single Pulse Avalanche Energy (Note 6)	$E_{AS}$	6.1	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	°C
Typical Thermal Resistance (Note 4,5)	Junction to Case	$R_{\theta JC}$	3.05
	Junction to Ambient	$R_{\theta JA}$	62.5

- Limited only By Maximum Junction Temperature

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## Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	100	-	-	V
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1.0	1.76	2.5	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=6.5\text{A}$	-	92	115	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=4\text{A}$	-	95	120	
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=100\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1.0	$\mu\text{A}$
Gate-Source Leakage Current	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
<b>Dynamic</b> (Note 7)						
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=50\text{V}, \text{I}_D=2\text{A}, \text{V}_{\text{GS}}=10\text{V}$ (Note 1,2)	-	20	-	nC
Gate-Source Charge	$\text{Q}_{\text{gs}}$		-	3.2	-	
Gate-Drain Charge	$\text{Q}_{\text{gd}}$		-	3.6	-	
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1.0\text{MHZ}$	-	1413	-	pF
Output Capacitance	$\text{C}_{\text{oss}}$		-	60	-	
Reverse Transfer Capacitance	$\text{Crss}$		-	34	-	
Turn-On Delay Time	$\text{td}(\text{on})$	$\text{V}_{\text{DD}}=50\text{V}, \text{I}_D=1\text{A}, \text{V}_{\text{GS}}=10\text{V}, \text{R}_G=3.3\Omega$ (Note 1,2)	-	18	-	ns
Turn-On Rise Time	$\text{tr}$		-	4.3	-	
Turn-Off Delay Time	$\text{td}(\text{off})$		-	41	-	
Turn-Off Fall Time	$\text{tf}$		-	4.2	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$\text{I}_s$	---	-	-	13	A
Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{I}_s=1\text{A}, \text{V}_{\text{GS}}=0\text{V}$	-	0.73	1	V

### NOTES :

1. Pulse width $\leq 300\mu\text{s}$ , Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature  $\text{TJ}(\text{MAX})=150^\circ\text{C}$ . Ratings are based on low frequency and duty cycles to keep initial  $\text{TJ}=25^\circ\text{C}$ .
4. The maximum current rating is package limited.
5.  $\text{R}_{\text{JA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. The test condition is  $L=0.1\text{mH}, \text{I}_{\text{AS}}=11\text{A}, \text{V}_{\text{DD}}=25\text{V}, \text{V}_{\text{GS}}=10\text{V}$
7. Guaranteed by design, not subject to production testing.

# PJD13N10A

## TYPICAL CHARACTERISTIC CURVES

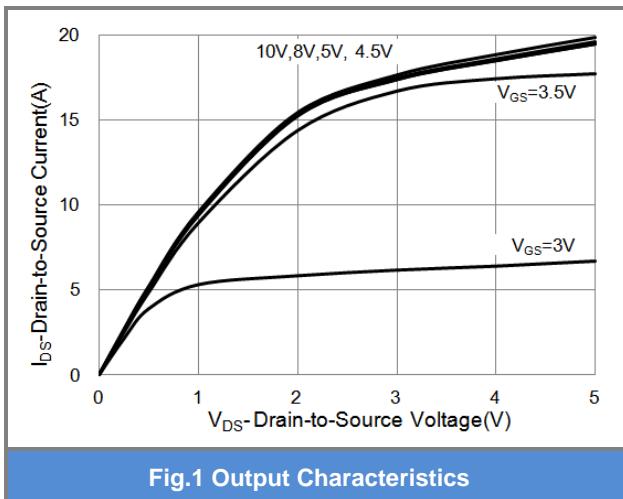


Fig.1 Output Characteristics

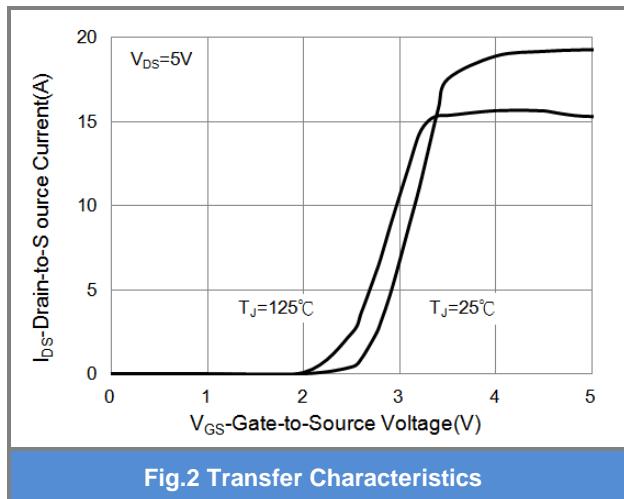


Fig.2 Transfer Characteristics

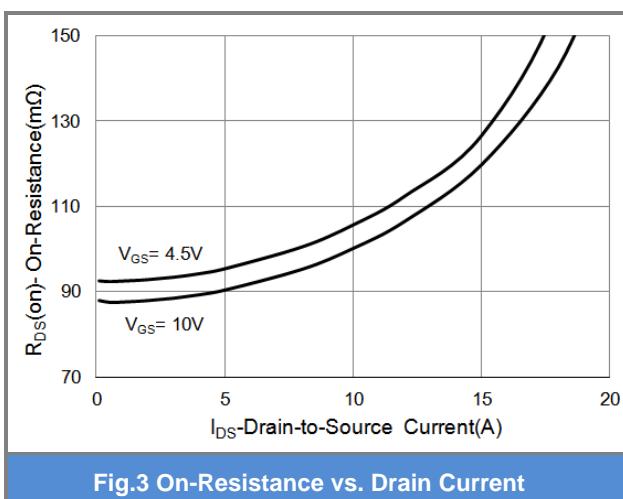


Fig.3 On-Resistance vs. Drain Current

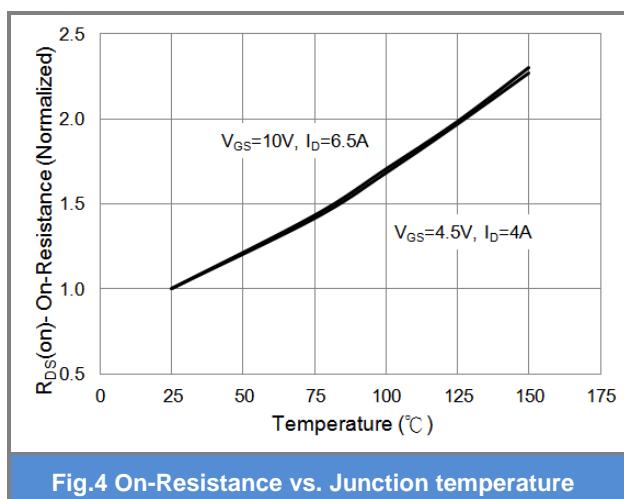


Fig.4 On-Resistance vs. Junction temperature

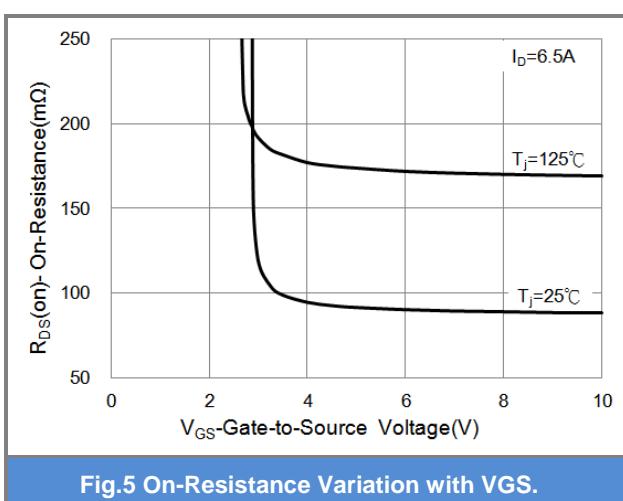


Fig.5 On-Resistance Variation with VGS.

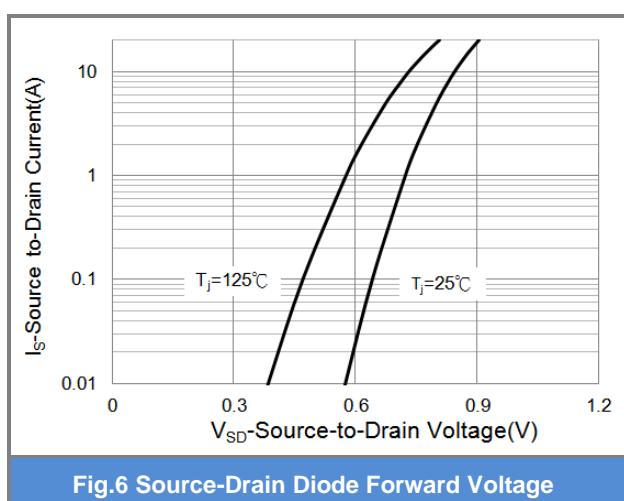


Fig.6 Source-Drain Diode Forward Voltage

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## TYPICAL CHARACTERISTIC CURVES

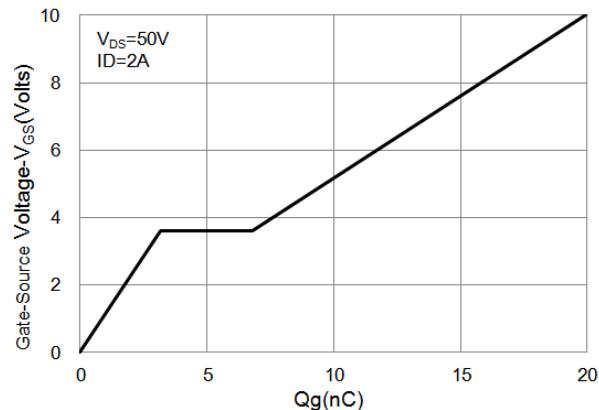


Fig.7 Gate-Charge Characteristics

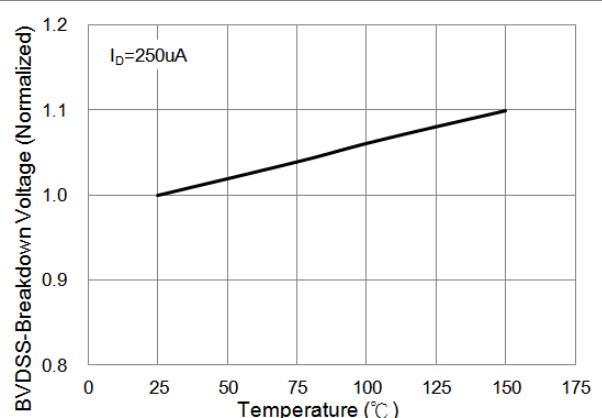


Fig.8 Breakdown Voltage Variation vs. Temperature

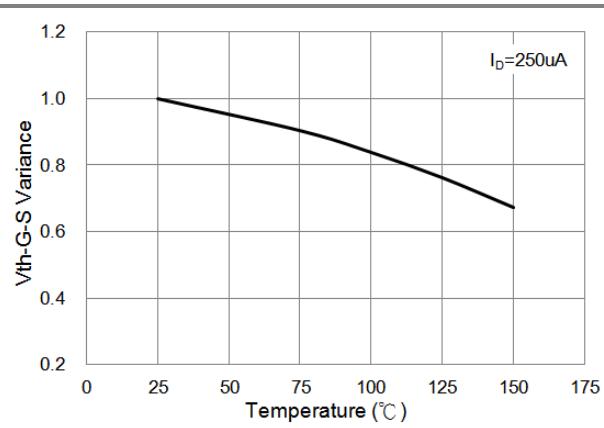


Fig.9 Threshold Voltage Variation with Temperature

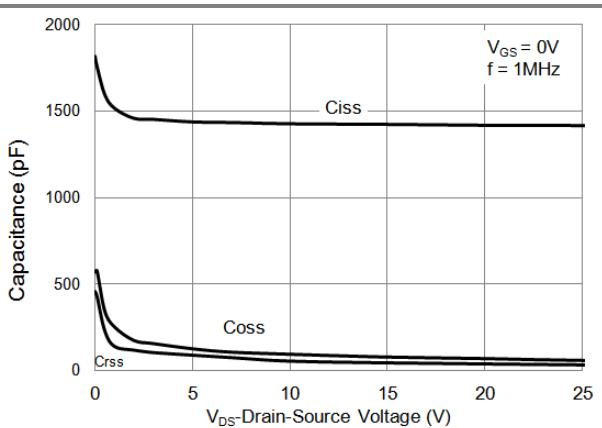


Fig.10 Capacitance vs. Drain-Source Voltage

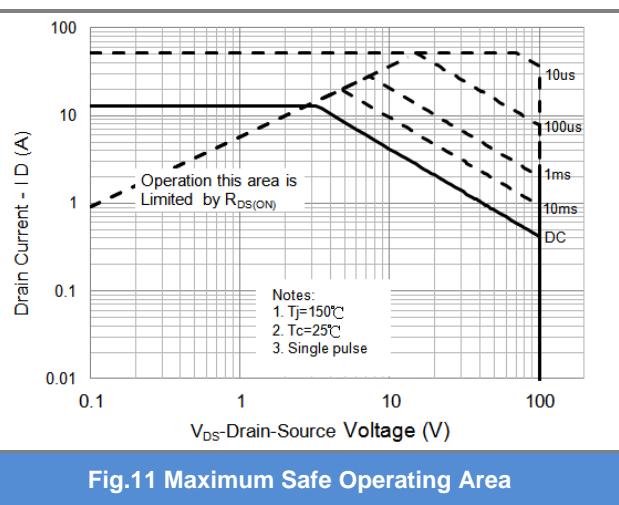


Fig.11 Maximum Safe Operating Area

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## TYPICAL CHARACTERISTIC CURVES

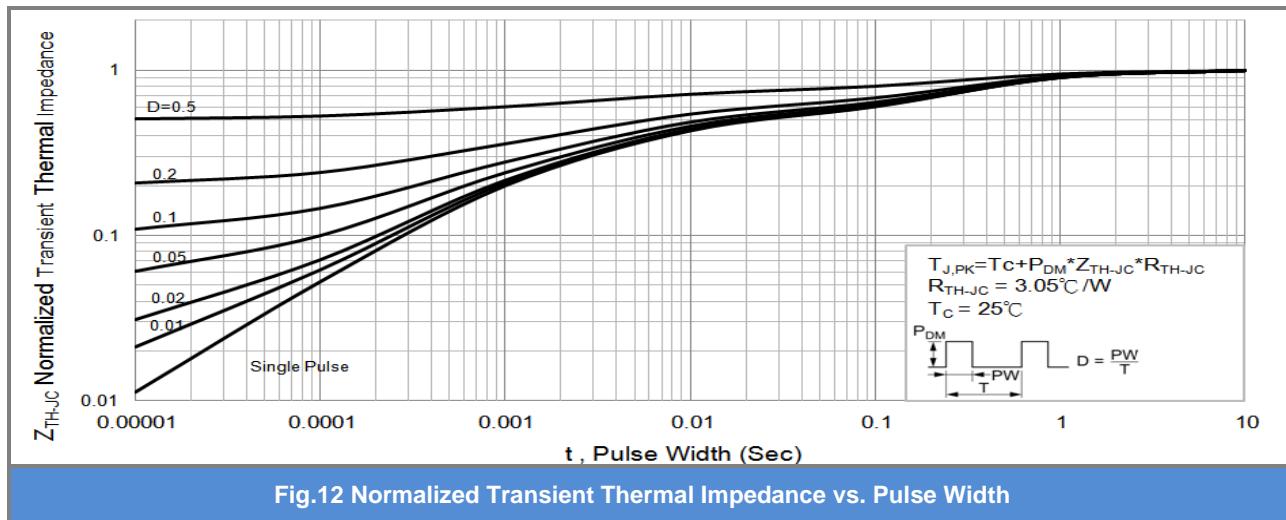
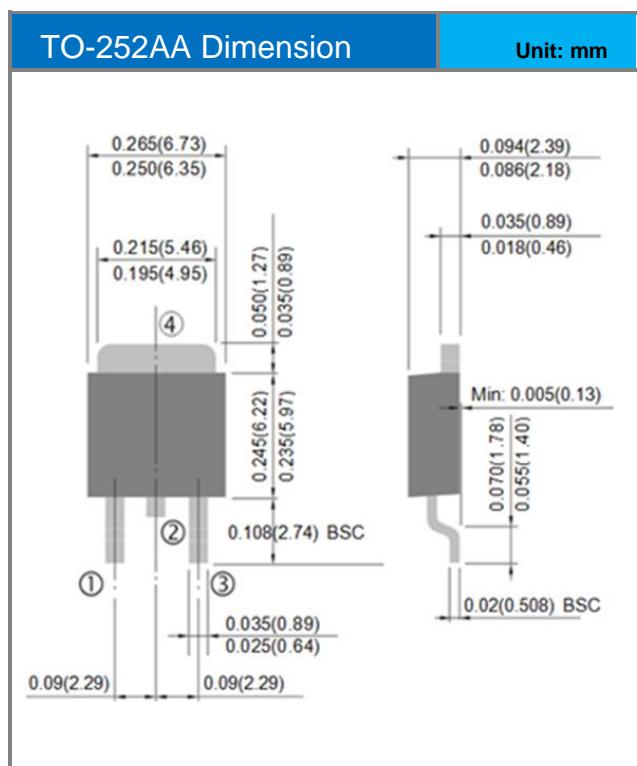


Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

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## Packaging Information

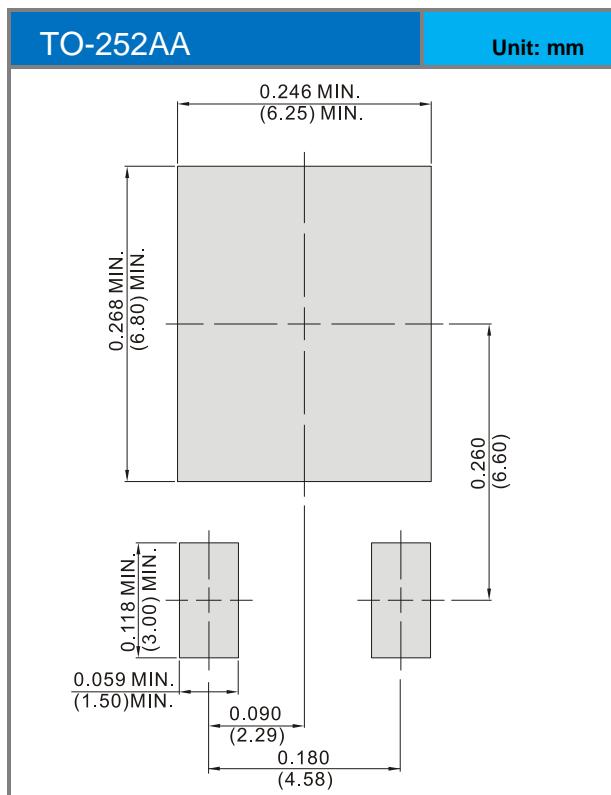


## PJD13N10A

### Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJD13N10A	TO-252AA	3K pcs / 13" reel	D13N10A

### Mounting Pad Layout



## PJD13N10A

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