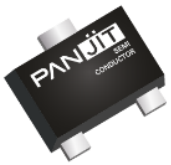
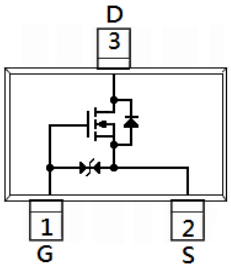
PJV138L

60V N-Channel Enhancement Mode Mosfet

Voltage	60 V	Current	130mA

SOT-723





Features

- $R_{DS(ON)}$, V_{GS}@10V, I_D@130mA<4.2Ω
- $R_{DS(ON)}$, V_{GS}@4.5V, I_D@100mA<5Ω
- $R_{DS(ON)}$, V_{GS}@2.5V, I_D@50mA<7Ω
- Advanced Trench Process Technology
- ESD Protected
- Specially Designed for Relay driver, Speed line drive, etc.
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case：SOT-723 Package
- Terminals：Solderable per MIL-STD-750, Method 2026
- Approx. Weight：0.0013 gram

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current(Note 4)		I_D	130	mA
Pulsed Drain Current(Note 1)		I_{DM}	800	
Power Dissipation	T_A=25°C	P_D	150	mW
	Derate above 25°C		1.2	mW/°C
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient(Note 3,4)		$R_{\theta JA}$	833	°C/W

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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250uA	60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	0.8	1.2	1.5	
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=10V, I_D=130mA	-	3.2	4.2	Ω
		V_{GS}=4.5V, I_D=100mA	-	3.8	5	
		V_{GS}=2.5V, I_D=50mA	-	5.3	7	
		V_{GS}=1.8V, I_D=10mA	-	12	-	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=60V, V_{GS}=0V	-	-	1	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	-	-	±10	
Dynamic(Note 5)						
Total Gate Charge	Q_g	V_{DS}=15V, I_D=130mA, V_{GS}=4.5V(Note 2)	-	0.7	-	nC
Gate-Source Charge	Q_{gs}		-	0.33	-	
Gate-Drain Charge	Q_{gd}		-	0.2	-	
Input Capacitance	Ciss	V_{DS}=15V, V_{GS}=0V, f=1.0MHZ	-	15	-	pF
Output Capacitance	Coss		-	8.4	-	
Reverse Transfer Capacitance	Crss		-	4.2	-	
Turn-On Delay Time	$td_{(on)}$	V_{DD}=10V, I_D=130mA, V_{GS}=10V, R_G=6Ω(Note 2)	-	7	-	ns
Turn-On Rise Time	tr		-	22	-	
Turn-Off Delay Time	$td_{(off)}$		-	21	-	
Turn-Off Fall Time	tf		-	25	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	130	mA
Diode Forward Voltage	V_{SD}	I_S=130mA, V_{GS}=0V	-	0.8	1.1	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%.
2. Essentially independent of operating temperature typical characteristics.
3. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 cm square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES

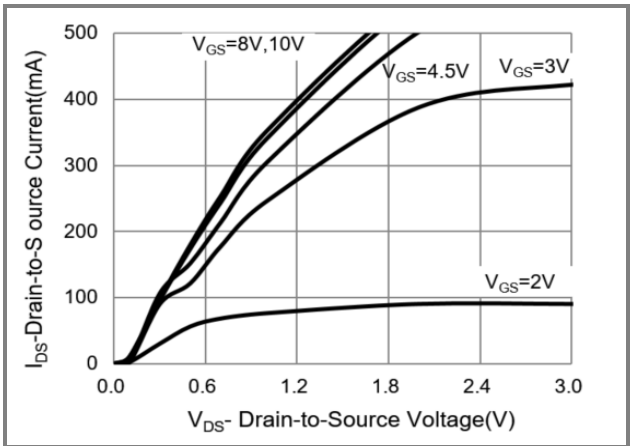

Fig.1 On-Region Characteristics

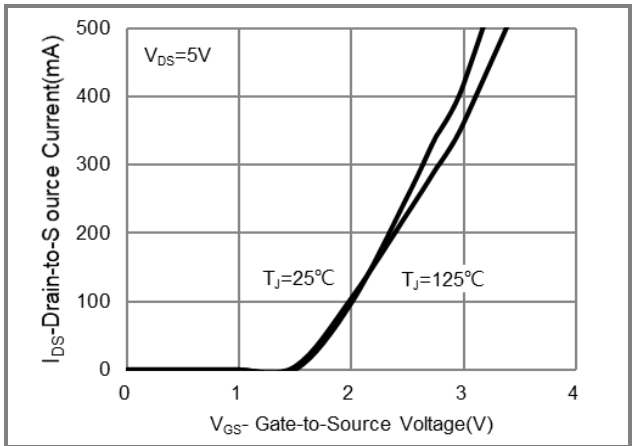

Fig.2 Transfer Characteristics

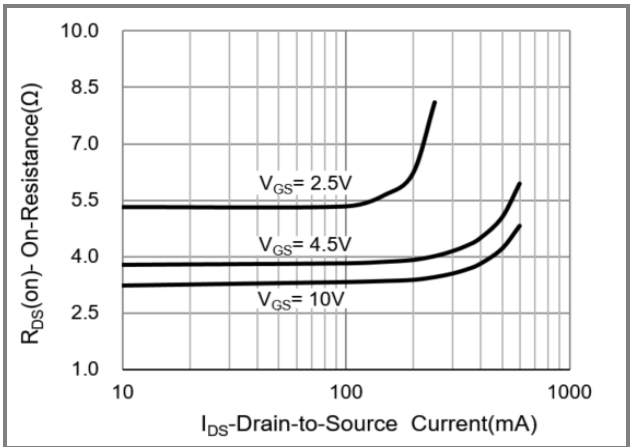

Fig.3 On-Resistance vs. Drain Current

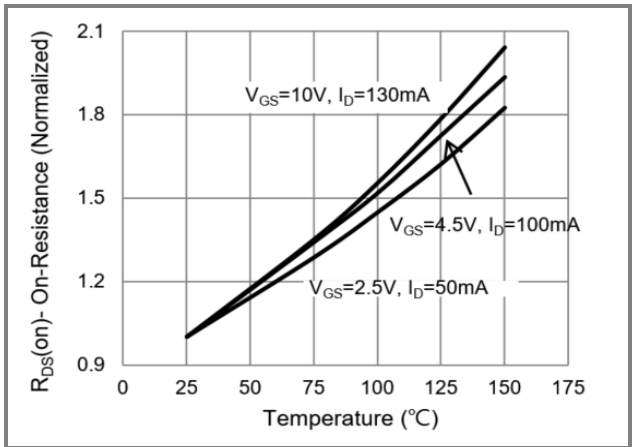

Fig.4 On-Resistance vs. Junction temperature

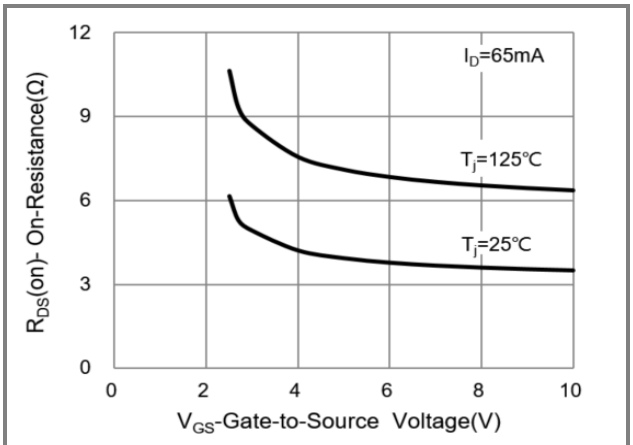

Fig.5 On-Resistance Variation with V_{GS}

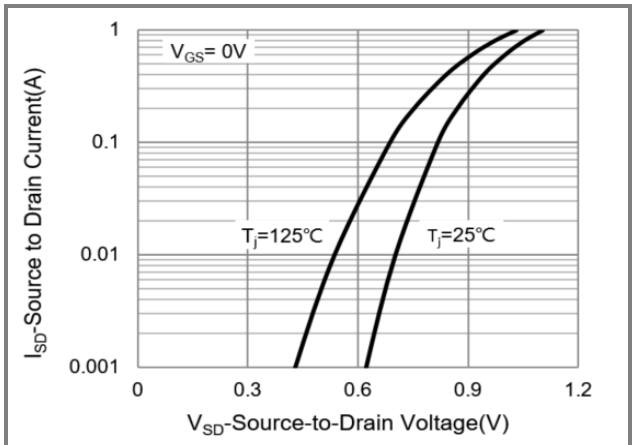

Fig.6 Body Diode Characteristics

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TYPICAL CHARACTERISTIC CURVES

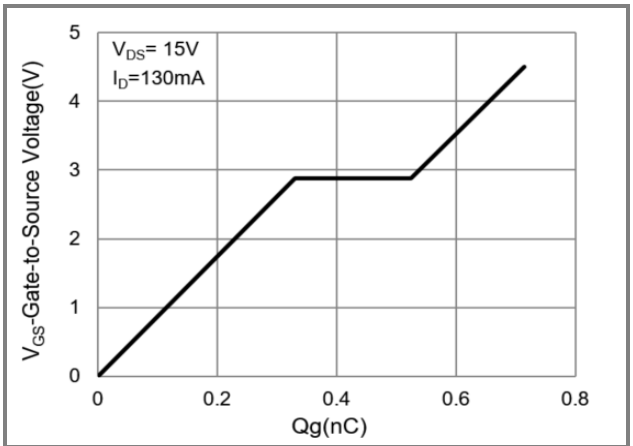


Fig.7 Gate-Charge Characteristics

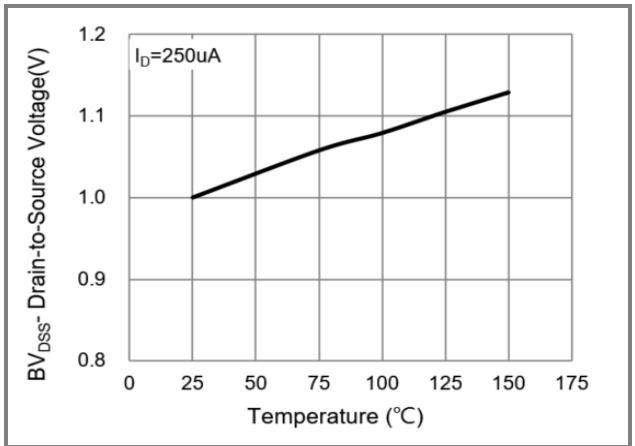


Fig.8 Breakdown Voltage Variation vs. Temperature

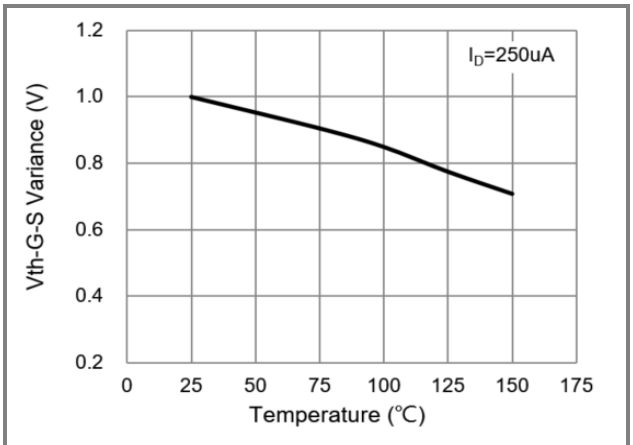


Fig.9 Threshold Voltage Variation with Temperature

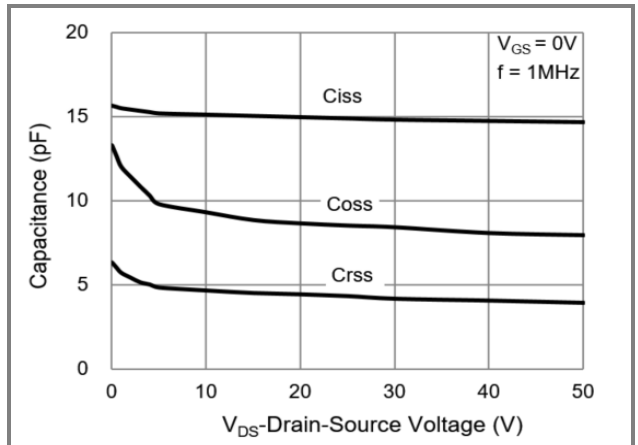


Fig.10 Capacitance vs. Drain-Source Voltage

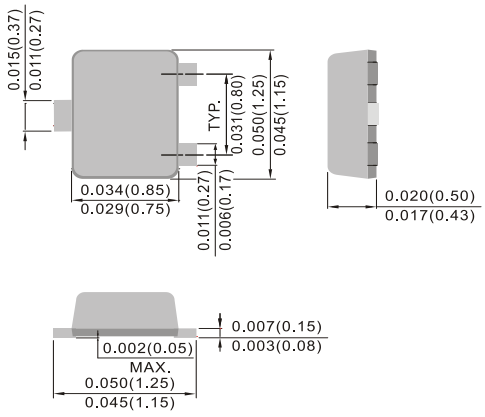
PJV138L

Product and Packing Information

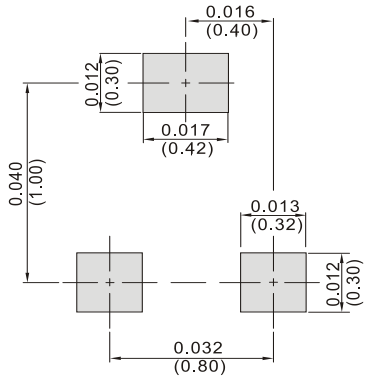
Part No.	Package Type	Packing Type	Marking
PJV138L	SOT-723	8K pcs / 7” reel	8L

Packaging Information & Mounting Pad Layout

SOT-723 Dimension — Unit: inch(mm)



SOT-723 Pad Layout — Unit: inch(mm)



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