

PJT7872B

60V N-Channel Enhancement Mode MOSFET

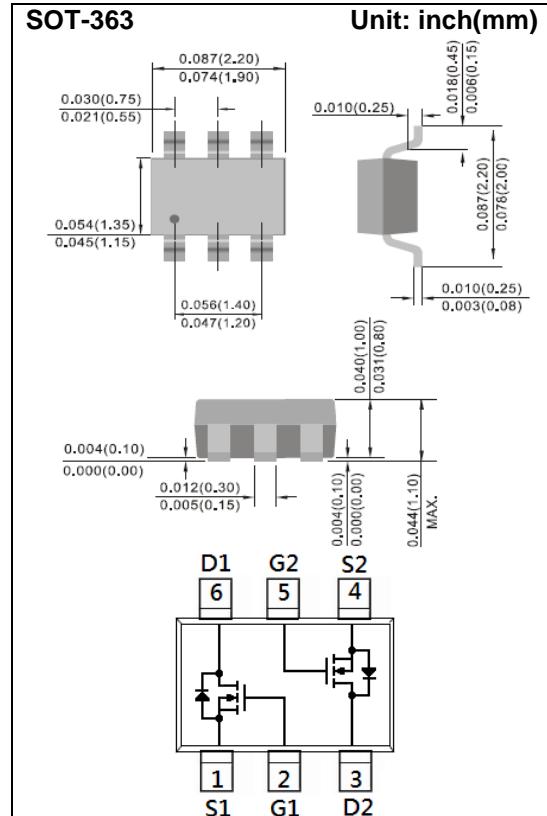
Voltage **60 V** Current **250mA**

Features

- RDS(ON) , VGS@10V, ID@600mA<3Ω
- RDS(ON) , VGS@4.5V, ID@200mA<4Ω
- Advanced Trench Process Technology
- Specially Designed for Relay driver, Speed line drive, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: SOT-363 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0002 ounces, 0.006 grams
- Marking: T2B



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current	I_D	250	mA
Pulsed Drain Current	I_{DM}	1000	mA
Power Dissipation	$T_A=25^\circ\text{C}$	350	mW
	Derate above 25°C	4	mW/°C
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient (Note 3)	$R_{\theta JA}$	357	°C/W

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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

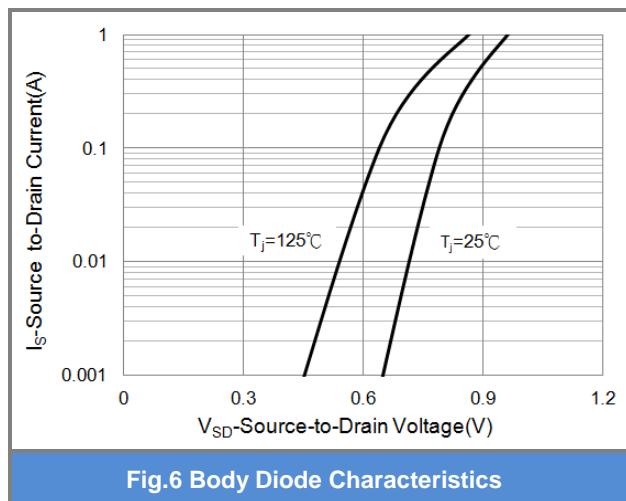
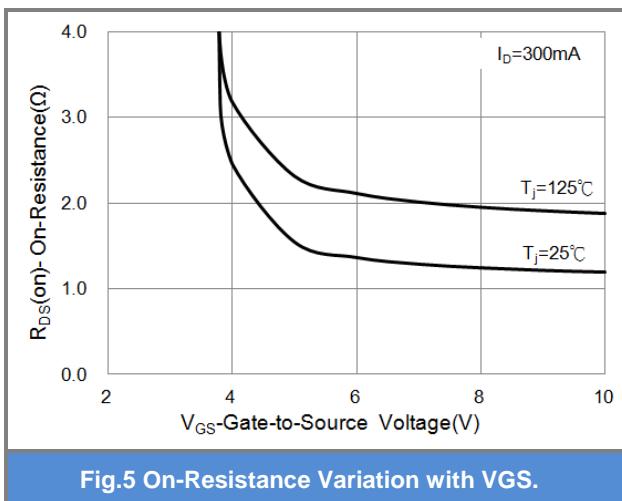
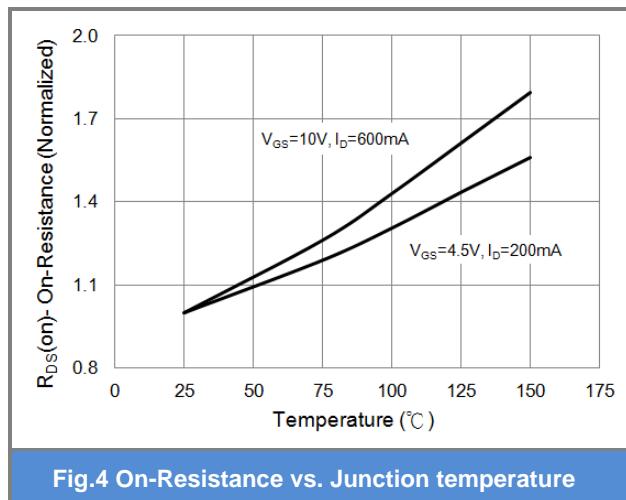
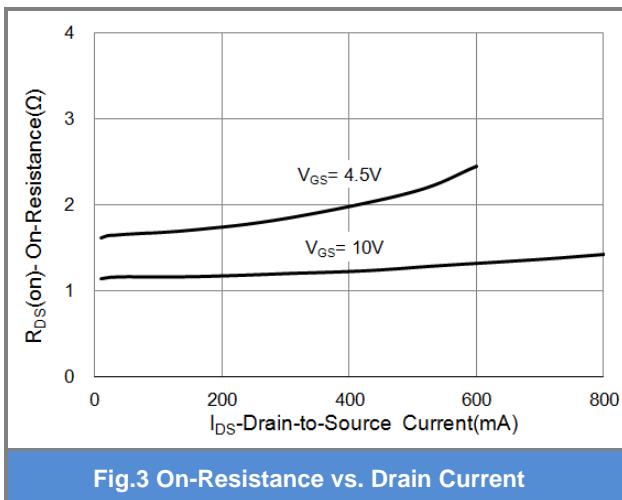
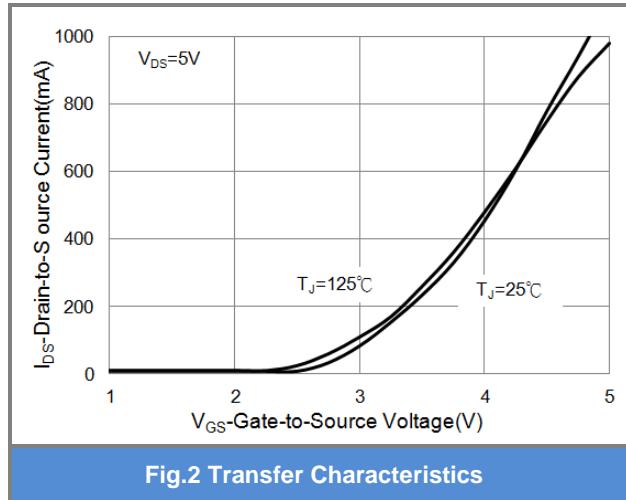
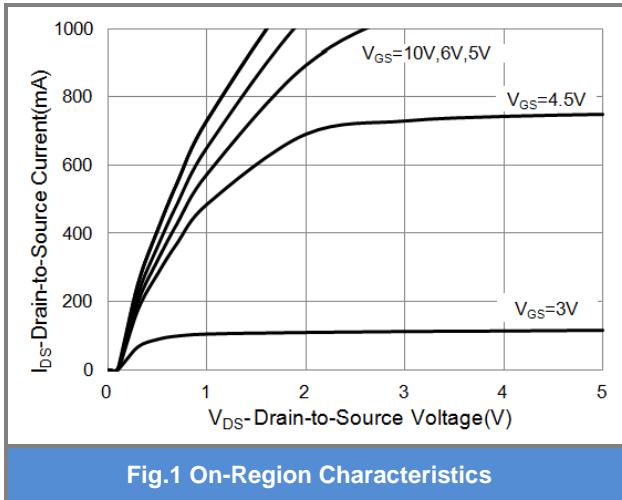
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	60	-	-	V
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1.0	1.8	2.5	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=600\text{mA}$	-	1.3	3	Ω
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=200\text{mA}$	-	1.7	4	
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=60\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Source Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 30\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Dynamic (Note 4)						
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=15\text{V}, \text{I}_D=600\text{mA}, \text{V}_{\text{GS}}=4.5\text{V}$	-	0.82	-	nC
Gate-Source Charge	Q_{gs}		-	0.53	-	
Gate-Drain Charge	Q_{gd}		-	0.22	-	
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$	-	34	-	pF
Output Capacitance	C_{oss}		-	11	-	
Reverse Transfer Capacitance	Crss		-	3.0	-	
Turn-On Delay Time	$\text{td}_{(\text{on})}$	$\text{V}_{\text{DD}}=10\text{V}, \text{I}_D=600\text{mA}, \text{V}_{\text{GS}}=10\text{V}, \text{R}_G=6\Omega$ (Note 1,2)	-	2.7	-	ns
Turn-On Rise Time	tr		-	21	-	
Turn-Off Delay Time	$\text{td}_{(\text{off})}$		-	3.8	-	
Turn-Off Fall Time	tf		-	18	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_s	---	-	-	500	mA
Diode Forward Voltage	V_{SD}	$\text{I}_s=500\text{mA}, \text{V}_{\text{GS}}=0\text{V}$	-	0.9	1.5	V

NOTES :

1. Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. R_{eJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
4. Guaranteed by design, not subject to production testing

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TYPICAL CHARACTERISTIC CURVES



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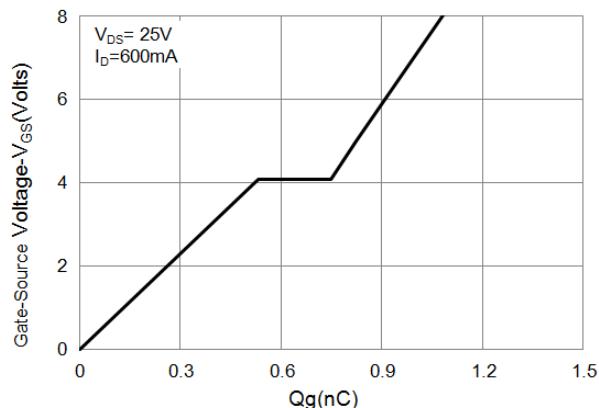


Fig.7 Gate-Charge Characteristics

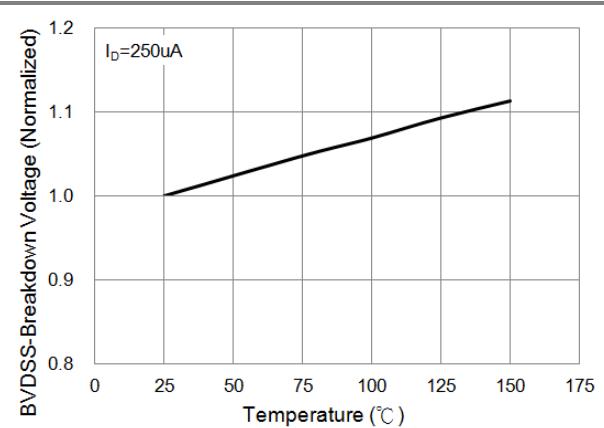


Fig.8 Breakdown Voltage Variation vs. Temperature

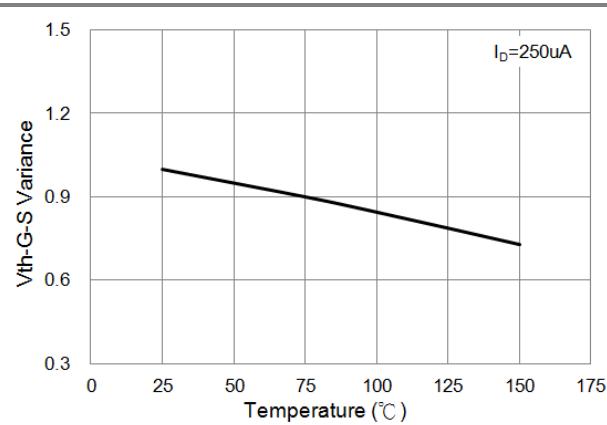


Fig.9 Threshold Voltage Variation with Temperature.

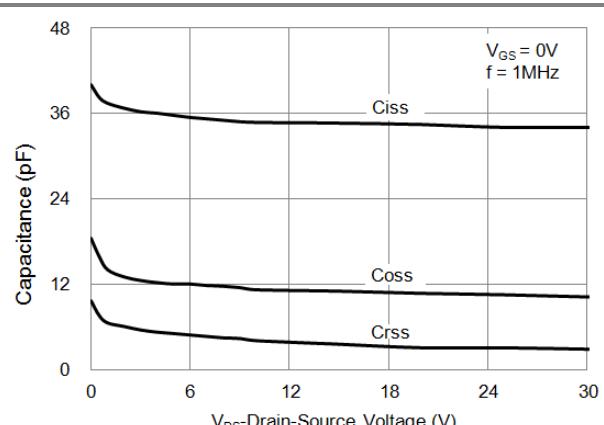


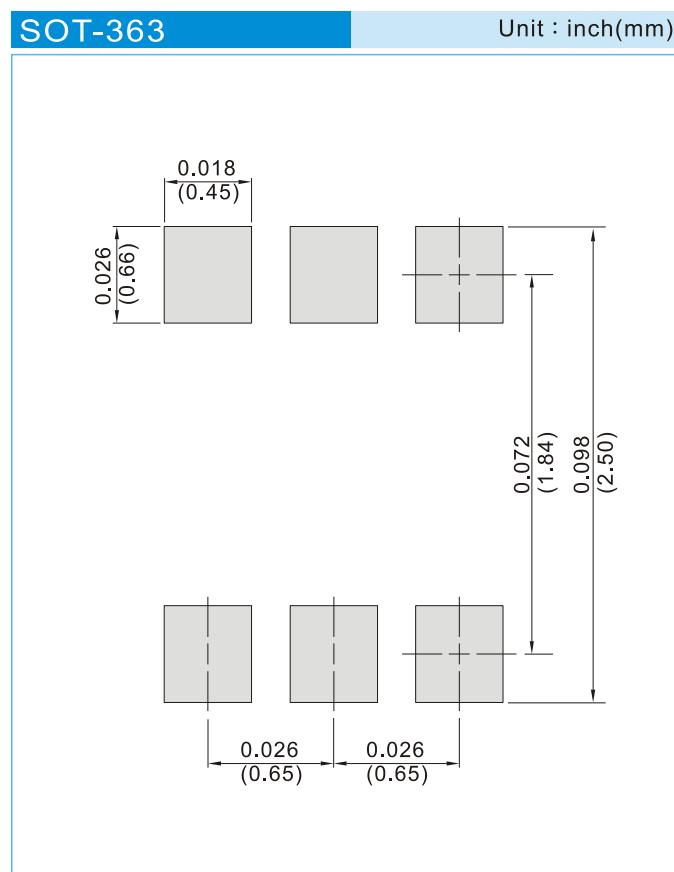
Fig.10 Capacitance vs. Drain-Source Voltage.

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Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJT7872B	SOT-363	3K pcs / 7" reel	T2B
PJT7872B	SOT-363	10K pcs / 13" reel	T2B

Mounting Pad Layout



PJT7872B

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