

# PJT7839

## 60V P-Channel Enhancement Mode MOSFET

**Voltage**

**-60 V**

**Current**

**-250mA**

### Features

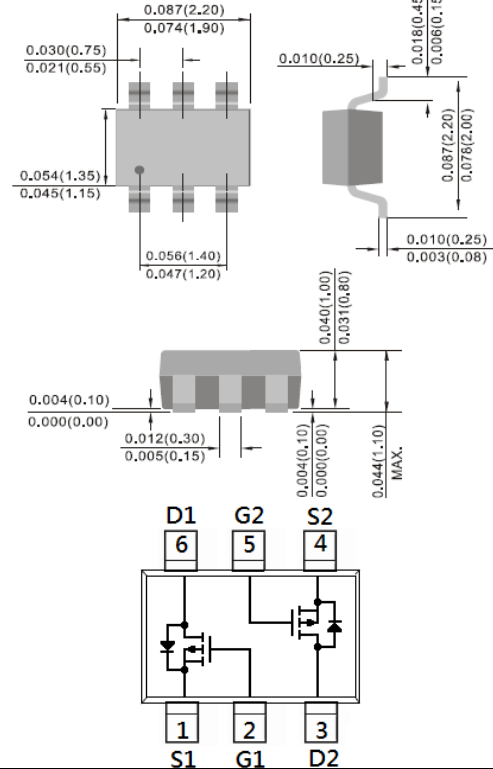
- RDS(ON) , VGS@-10V, ID@-500mA<4Ω
- RDS(ON) , VGS@-4.5V, ID@-200mA<6Ω
- RDS(ON) , VGS@-2.5V, ID@-50mA<13Ω
- Advanced Trench Process Technology
- Specially Designed for Relay driver, Speed line drive, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

### Mechanical Data

- Case: SOT-363 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0002 ounces, 0.006 grams
- Marking: T39

**SOT-363**

**Unit: inch(mm)**



### Maximum Ratings and Thermal Characteristics (TA=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	-60	V
Gate-Source Voltage		V <sub>GS</sub>	±20	V
Continuous Drain Current		I <sub>D</sub>	-250	mA
Pulsed Drain Current		I <sub>DM</sub>	-1000	mA
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	350	mW
	Derate above 25°C		2.8	mW/°C
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C
Typical Thermal resistance		R <sub>θJA</sub>	357	°C/W
- Junction to Ambient (Note 3)				

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## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

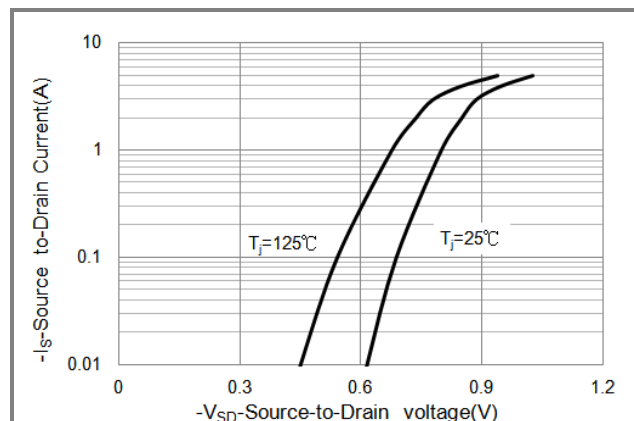
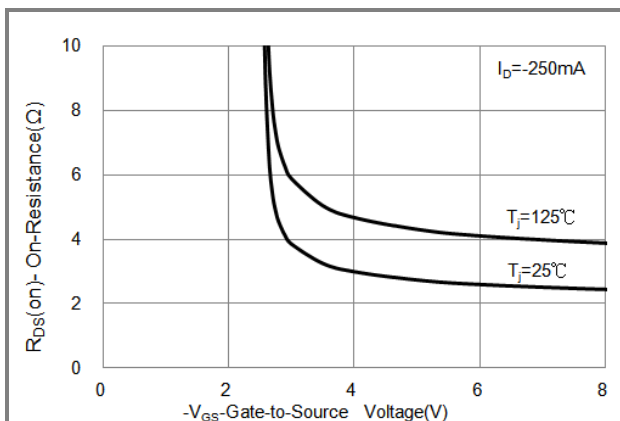
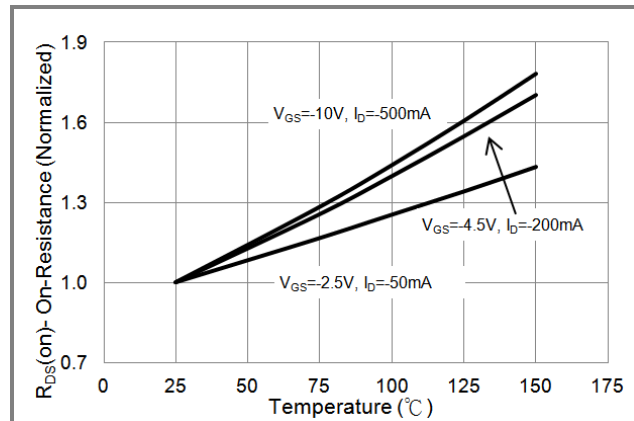
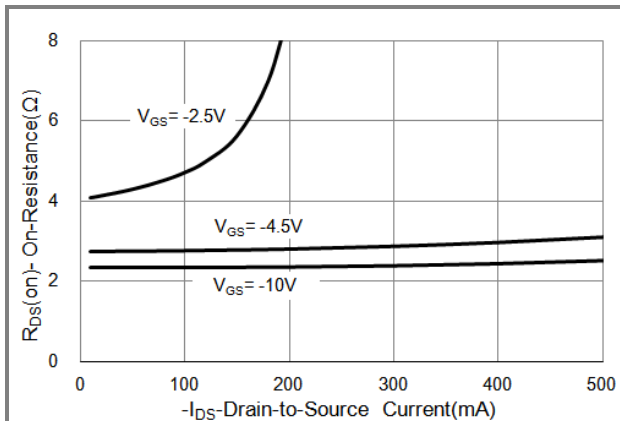
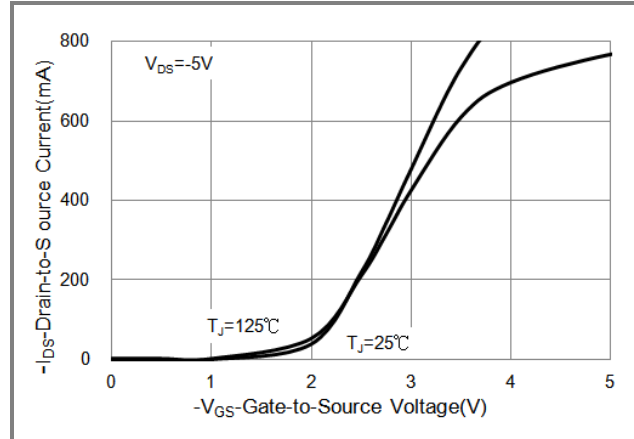
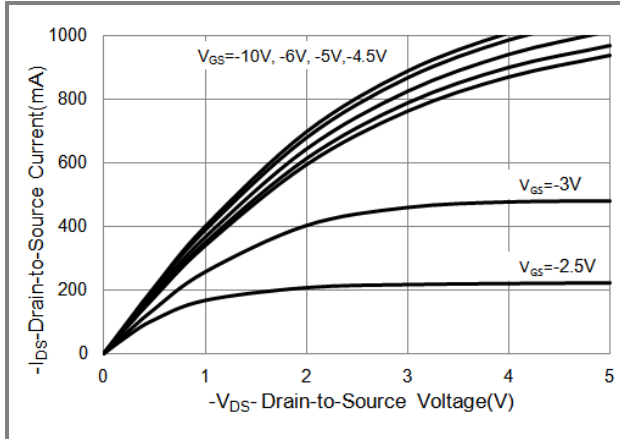
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1.0	-1.5	-2.5	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-500mA	-	2.4	4	Ω
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-200mA	-	2.65	6	
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-50mA	-	4.5	13	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-48V, V <sub>GS</sub> =0V	-	-	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Dynamic (Note 4)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-25V, I <sub>D</sub> =-100mA, V <sub>GS</sub> =-4.5V	-	1.1	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	0.3	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	0.2	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-25V, V <sub>GS</sub> =0V, f=1.0MHZ	-	51	-	pF
Output Capacitance	C <sub>oss</sub>		-	15	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	2.2	-	
Turn-On Delay Time	td <sub>(on)</sub>	V <sub>DD</sub> =-25V, I <sub>D</sub> =-100mA, V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω (Note 1,2)	-	4.8	-	ns
Turn-On Rise Time	tr		-	19	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	52	-	
Turn-Off Fall Time	tf		-	32	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>s</sub>	---	-	-	-250	mA
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-500mA, V <sub>GS</sub> =0V	-	-0.95	-1.3	V

### NOTES :

1. Pulse width ≤ 300us, Duty cycle ≤ 2%
2. Essentially independent of operating temperature typical characteristics.
3. R<sub>ΘJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
4. Guaranteed by design, not subject to production testing

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## TYPICAL CHARACTERISTIC CURVES



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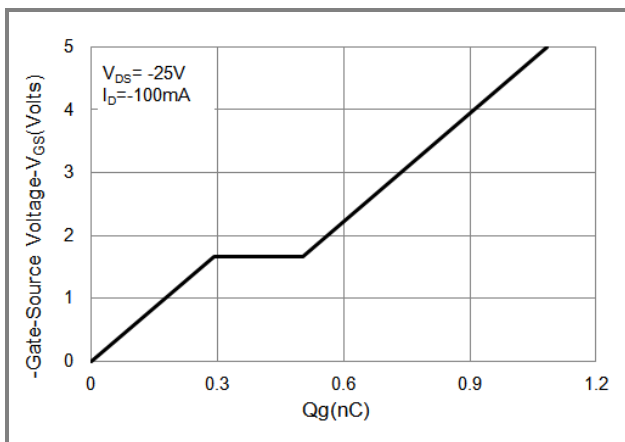


Fig.7 Gate-Charge Characteristics

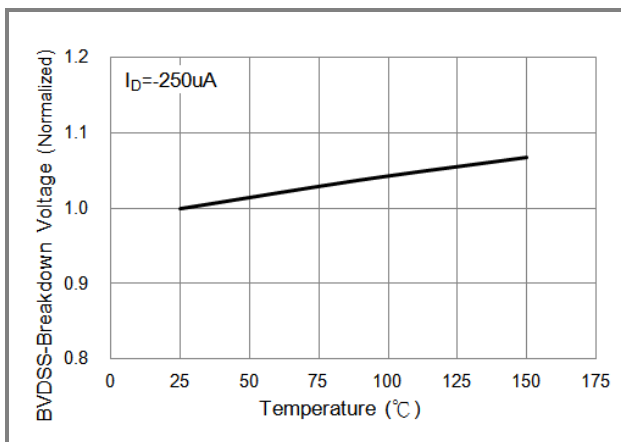


Fig.8 Breakdown Voltage Variation vs. Temperature

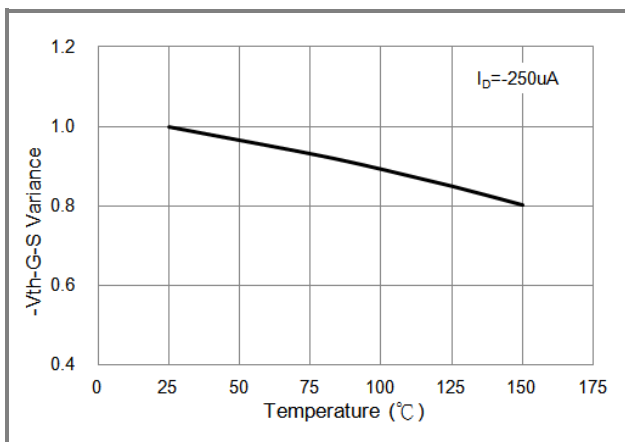


Fig.9 Threshold Voltage Variation with Temperature.

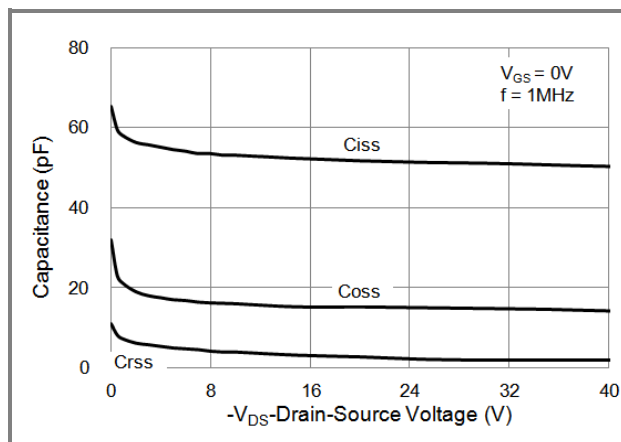


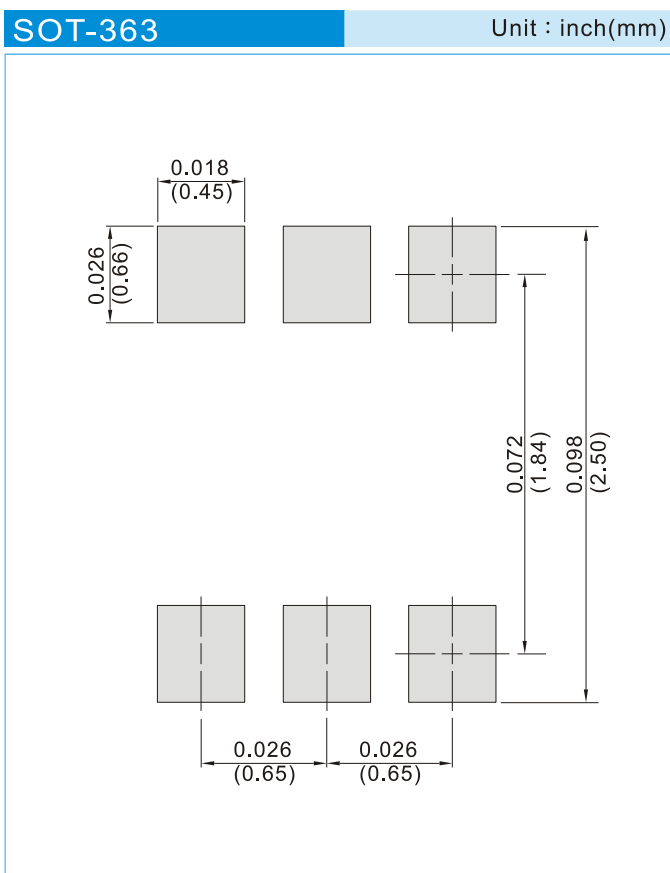
Fig.10 Capacitance vs. Drain-Source Voltage.

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## Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJT7839	SOT-363	3K pcs / 7" reel	T39
PJT7839	SOT-363	10K pcs / 13" reel	T39

## Mounting Pad Layout



## PJT7839

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