

# PJQ5423-AU

## 30V P-Channel Enhancement Mode MOSFET

Voltage

-30 V

Current

-60 A

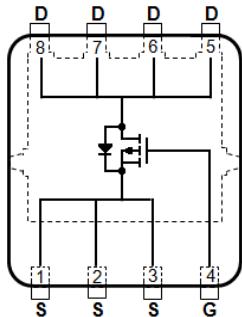
### Features

- $R_{DS(ON)}$ ,  $V_{GS} @ -10V, I_D @ -10A < 8.5m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS} @ -4.5V, I_D @ -8A < 14m\Omega$
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : DFN5060-8L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.08 grams

DFN5060-8L



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>(Note 4)</sup>	$I_D$	-60	A
$T_c=100^\circ C$		-38	
Pulsed Drain Current <sup>(Note 1)</sup>	$I_{DM}$	-240	
Power Dissipation	$P_D$	63	W
$T_c=100^\circ C$		25	
Continuous Drain Current <sup>(Note 4)</sup>	$I_D$	-11	A
$T_A=70^\circ C$		-8.8	A
Power Dissipation	$P_D$	2.0	W
$T_A=70^\circ C$		1.3	
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	°C
Typical Thermal Resistance <sup>(Note 4,5)</sup>	Junction to Case	$R_{\theta JC}$	2.0
	Junction to Ambient	$R_{\theta JA}$	62.5

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### Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30	-	-	V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.0	-1.5	-2.5	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-10\text{A}$	-	7.1	8.5	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-8\text{A}$	-	10	14	
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1.0	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
<b>Dynamic</b> <sup>(Note 6)</sup>						
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-10\text{A}, V_{\text{GS}}=-4.5\text{V}$ <sup>(Note 1,2)</sup>	-	27	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	8.4	-	
Gate-Drain Charge	$Q_{\text{gd}}$		-	8.7	-	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHZ}$	-	3228	-	pF
Output Capacitance	$C_{\text{oss}}$		-	396	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	254	-	
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-1\text{A}, V_{\text{GS}}=-10\text{V}, R_{\text{G}}=6\Omega$ <sup>(Note 1,2)</sup>	-	10	-	ns
Turn-On Rise Time	$t_r$		-	13	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	111	-	
Turn-Off Fall Time	$t_f$		-	51	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_s$	---	-	-	-60	A
Diode Forward Voltage	$V_{\text{SD}}$	$I_s=-1\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.7	-1	V

NOTES :

1. Pulse width $\leq 300\mu\text{s}$ , Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics
3. Repetitive rating, pulse width limited by junction temperature  $T_{\text{J}(\text{MAX})}=150^\circ\text{C}$ . Ratings are based on low frequency and duty cycles to keep initial  $T_{\text{J}}=25^\circ\text{C}$ .
4. The maximum current rating is package limited
5.  $R_{\text{OJA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing

# PJQ5423-AU

## TYPICAL CHARACTERISTIC CURVES

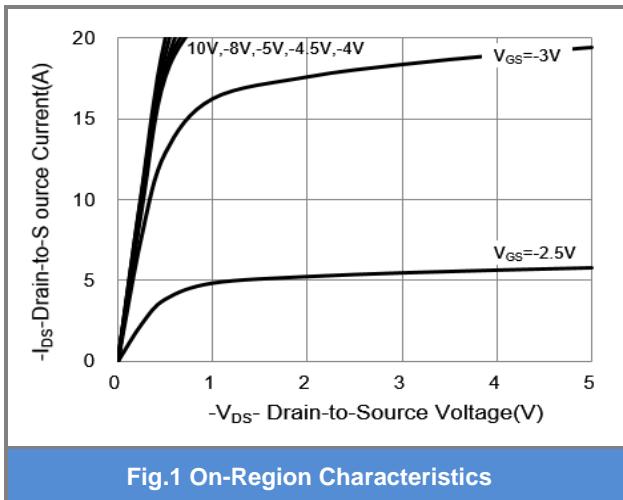


Fig.1 On-Region Characteristics

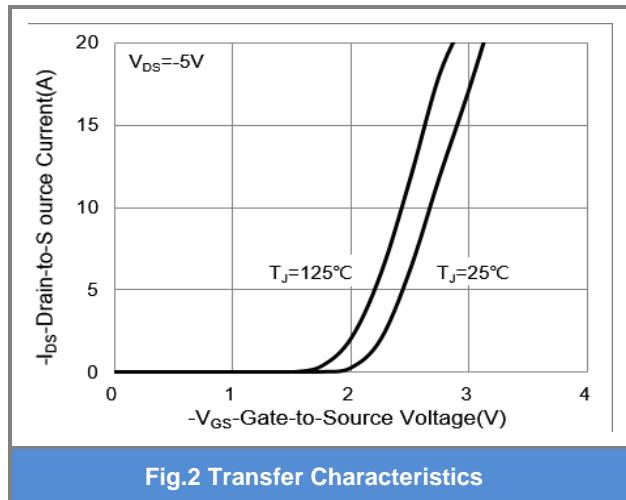


Fig.2 Transfer Characteristics

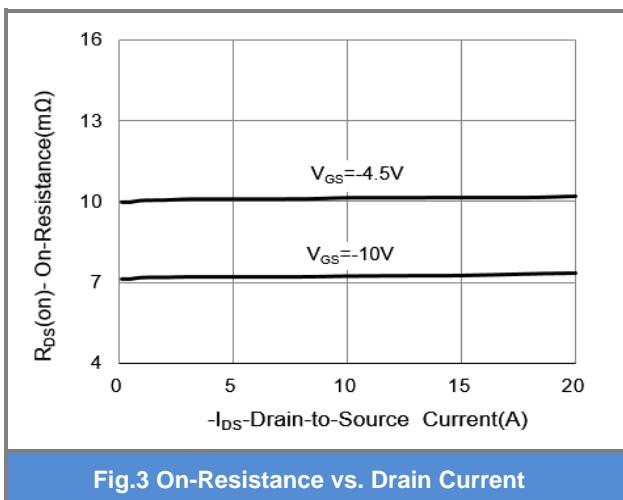


Fig.3 On-Resistance vs. Drain Current

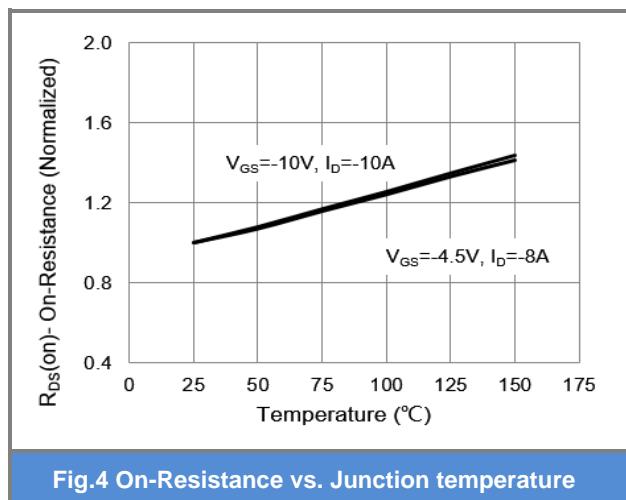


Fig.4 On-Resistance vs. Junction temperature

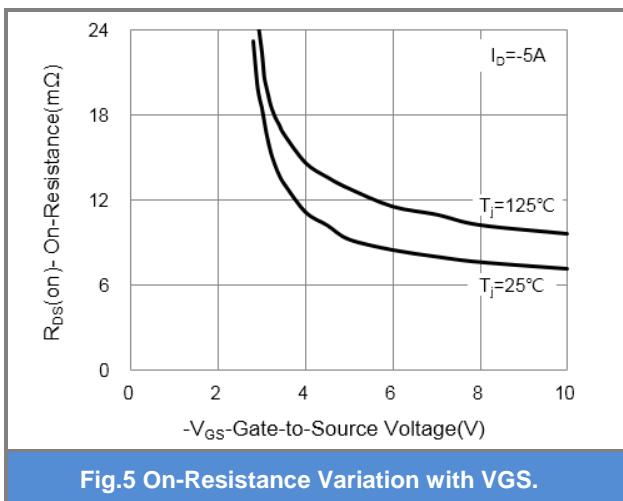


Fig.5 On-Resistance Variation with VGS.

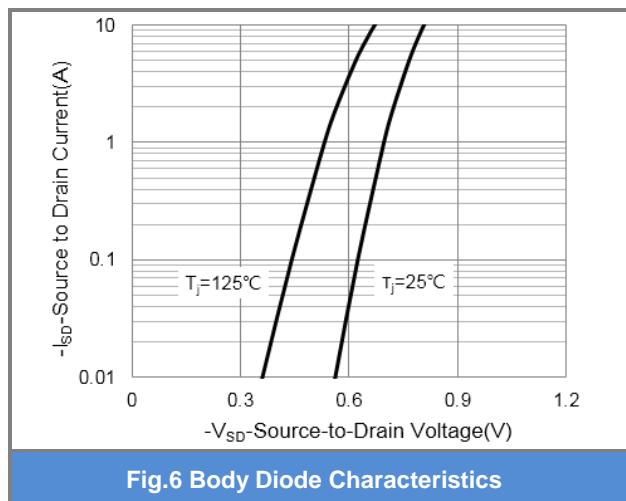


Fig.6 Body Diode Characteristics

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## TYPICAL CHARACTERISTIC CURVES

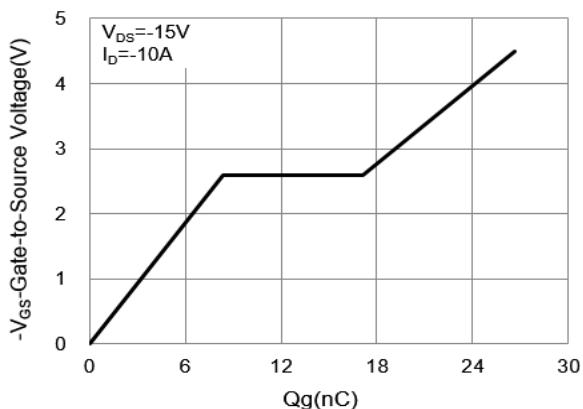


Fig.7 Gate-Charge Characteristics

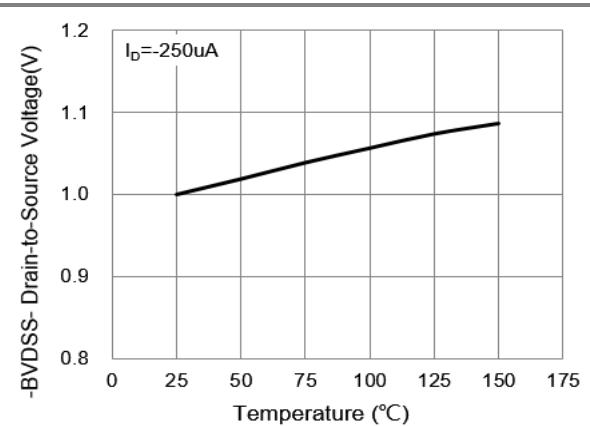


Fig.8 Breakdown Voltage Variation vs. Temperature

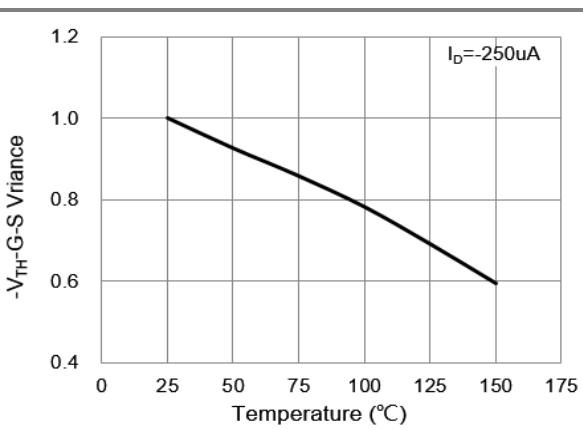


Fig.9 Threshold Voltage Variation with Temperature.

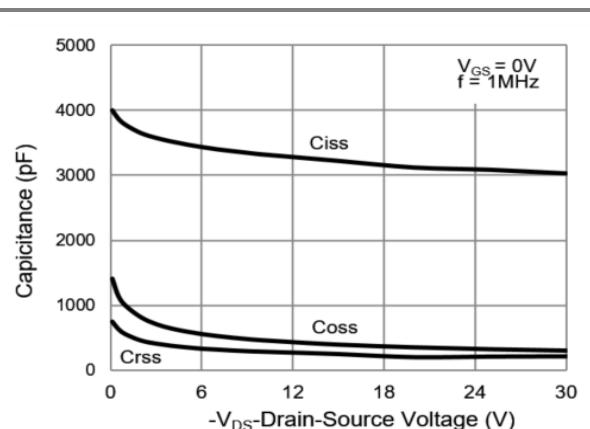


Fig.10 Capacitance vs. Drain-Source Voltage.

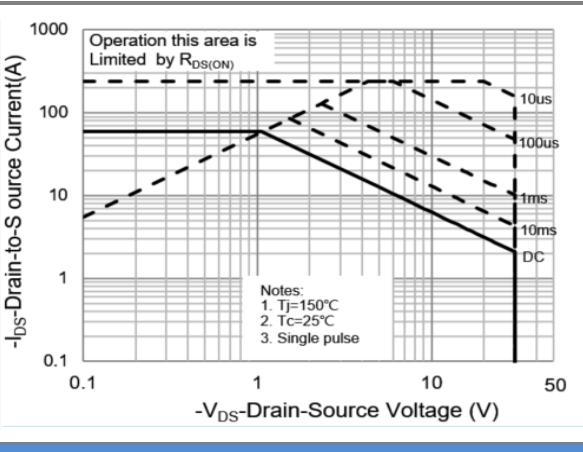


Fig.11 Maximum Safe Operating Area

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### TYPICAL CHARACTERISTIC CURVES

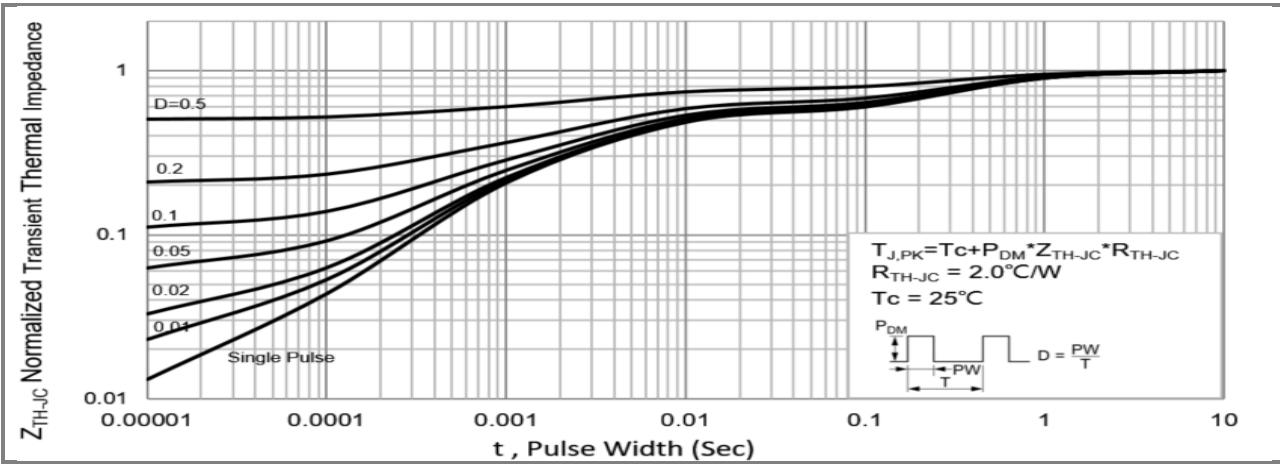


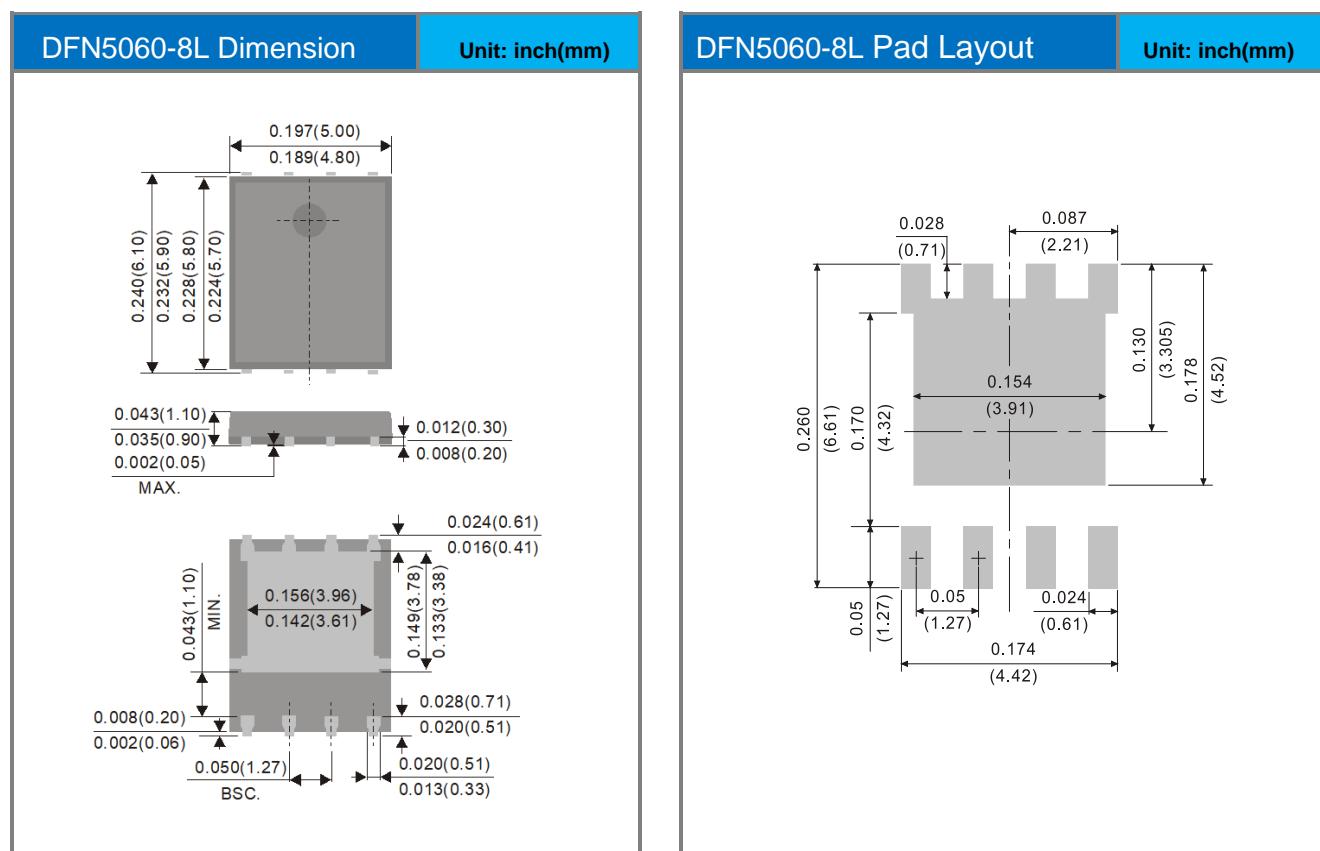
Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

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## Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJQ5423-AU	DFN5060-8L	3000pcs / 13" reel	Q5423

## Packaging Information & Mounting Pad Layout



## **PJQ5423-AU**

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