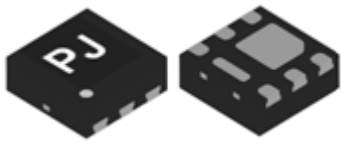
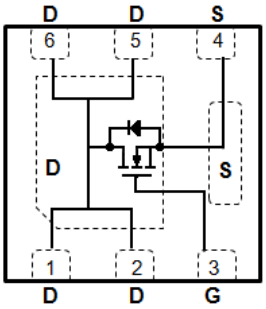
PJQ2460

60V N-Channel Enhancement Mode MOSFET

Voltage	60 V	Current	3.2A

DFN2020B-6L





Features

- $R_{DS(ON)}$, V_{GS}@10V, I_D@3.2A<75mΩ
- $R_{DS(ON)}$, V_{GS}@4.5V, I_D@2.0A<90mΩ
- Advanced Trench Process Technology
- High density cell design for ultra low on-resistance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : DFN2020B-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0086 grams

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current		I_D	3.2	A
Pulsed Drain Current		I_{DM}	12.8	A
Power Dissipation	T_a=25°C	P_D	2.0	W
	Derate above 25°C		16	mW/°C
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient, t<10s (Note 3)		$R_{\theta JA}$	62.5	°C/W

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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250uA	60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	1.0	1.8	2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=10V, I_D=3.2A	-	53	75	mΩ
		V_{GS}=4.5V, I_D=2.0A	-	61	90	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=48V, V_{GS}=0V	-	-	1	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
Dynamic (Note 6)						
Total Gate Charge	Q_g	V_{DS}=48V, I_D=3.0A, V_{GS}=10V (Note 1,2)	-	9.3	-	nC
Gate-Source Charge	Q_{gs}		-	2.2	-	
Gate-Drain Charge	Q_{gd}		-	1.9	-	
Input Capacitance	Ciss	V_{DS}=15V, V_{GS}=0V, f=1.0MHZ	-	509	-	pF
Output Capacitance	Coss		-	47	-	
Reverse Transfer Capacitance	Crss		-	23	-	
Turn-On Delay Time	$td_{(on)}$	V_{DD}=30V, I_D=3.0A, V_{GS}=10V, R_G=3.3Ω (Note 1,2)	-	3.2	-	ns
Turn-On Rise Time	tr		-	9.7	-	
Turn-Off Delay Time	$td_{(off)}$		-	18.5	-	
Turn-Off Fall Time	tf		-	6.4	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	3.2	A
Diode Forward Voltage	V_{SD}	I_S=1A, V_{GS}=0V	-	0.75	1.2	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%
2. Essentially independent of operating temperature typical characteristics.
3. The maximum current rating is package limited.
4. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
5. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 $inch^2$ with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES

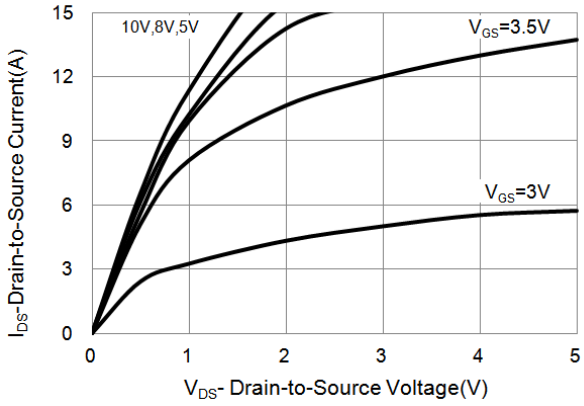

Fig.1 On-Region Characteristics

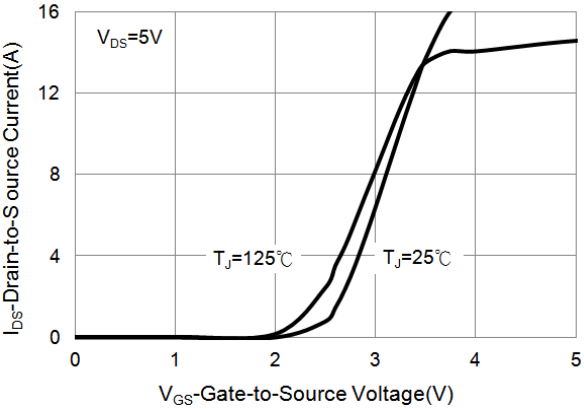

Fig.2 Transfer Characteristics

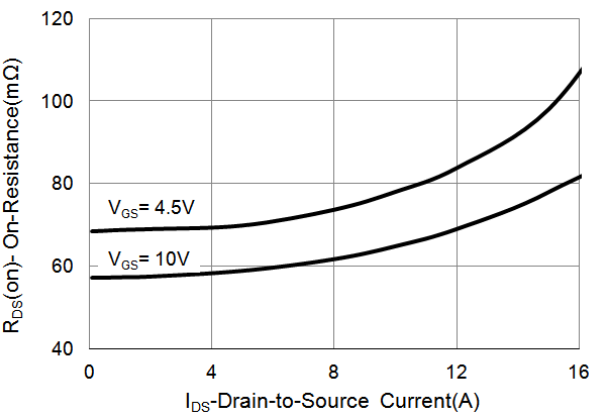

Fig.3 On-Resistance vs. Drain Current

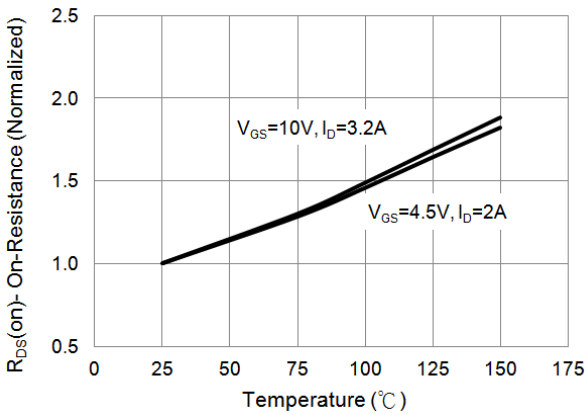

Fig.4 On-Resistance vs. Junction temperature

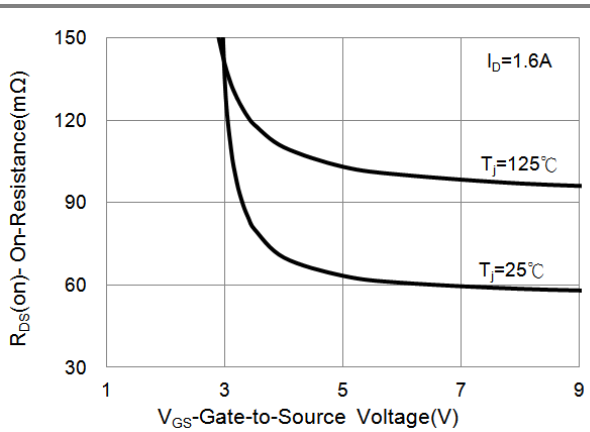

Fig.5 On-Resistance Variation with VGS.

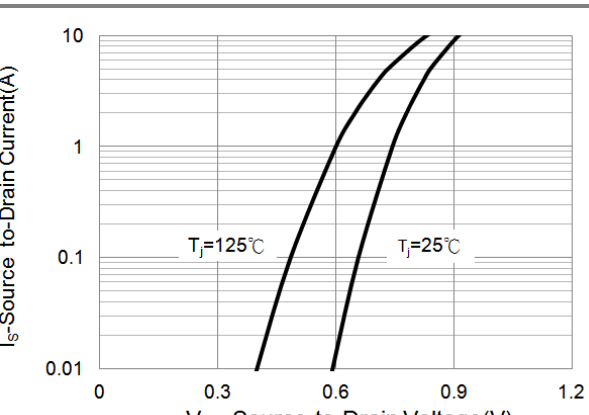

Fig.6 Body Diode Characteristics

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TYPICAL CHARACTERISTIC CURVES

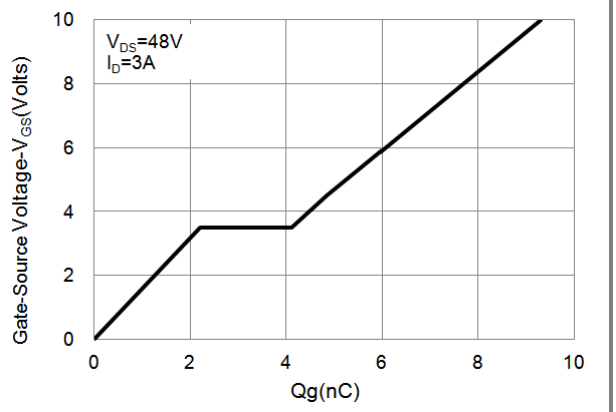


Fig.7 Gate-Charge Characteristics

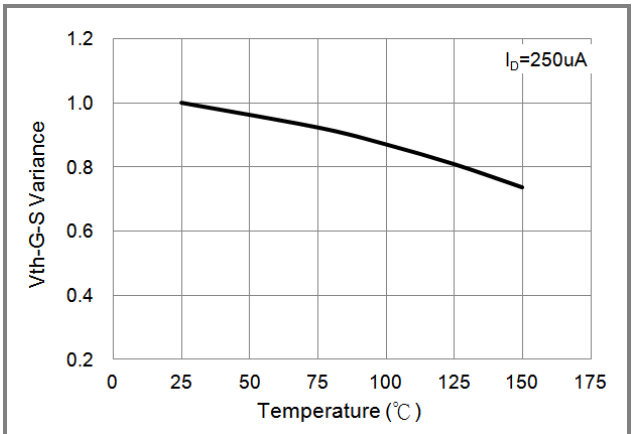


Fig.8 Threshold Voltage Variation with Temperature.

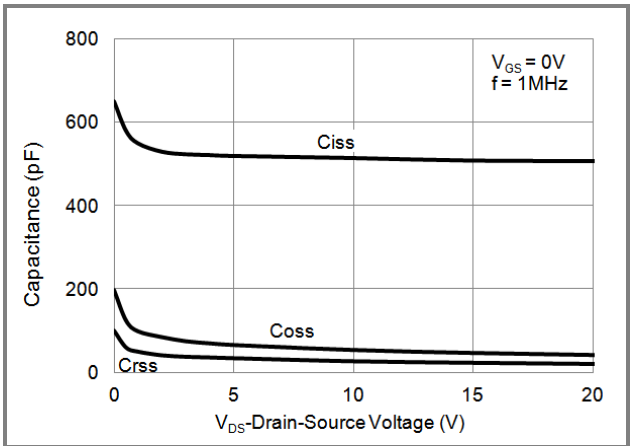


Fig.9 Capacitance vs. Drain-Source Voltage.

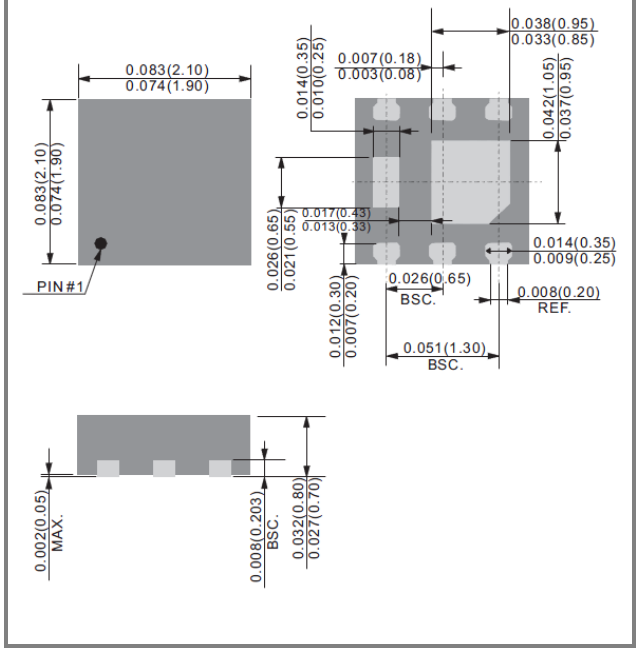
PJQ2460

Part No. Packing Code Version

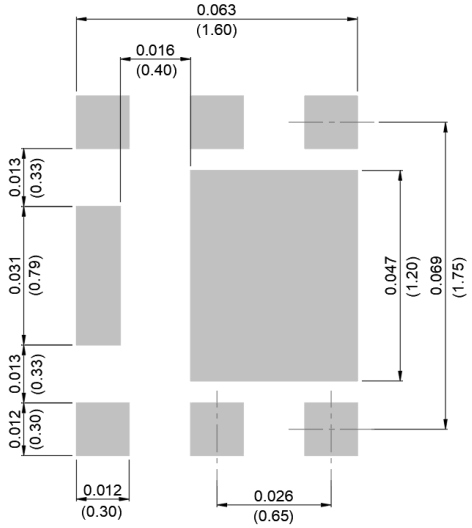
Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ2460_R1_00001	DFN2020B-6L	3K pcs / 7” reel	460	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout

DFN2020B-6L Dimension — Unit: inch(mm)



DFN2020B-6L Pad Layout — Unit: inch(mm)



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