

PJE8408-AU

20V N-Channel Enhancement Mode MOSFET

Voltage 20 V **Current** 500mA

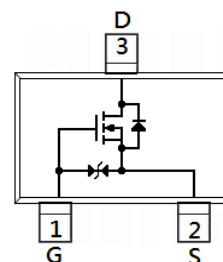
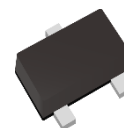
Features

- Low Voltage Drive (1.2V).
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC61249 standard

Mechanical Data

- Case : SOT-523 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.002 grams

SOT-523



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	±10	
Continuous Drain Current ^(Note 4)		I _D	500	mA
Pulsed Drain Current ^(Note 1)		I _{DM}	1000	
Power Dissipation	T _a =25°C	P _D	300	mW
	Derate above 25°C		2.4	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance		R _{θJA}	417	°C/W
- Junction to Ambient ^(Note 3,4)				

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Electrical Characteristics (T_A=25°C unless otherwise noted)

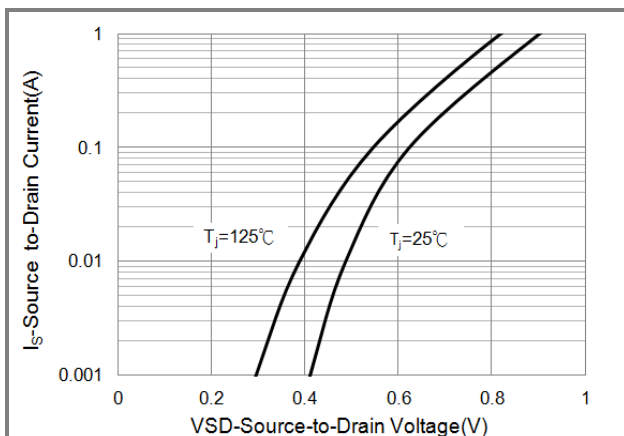
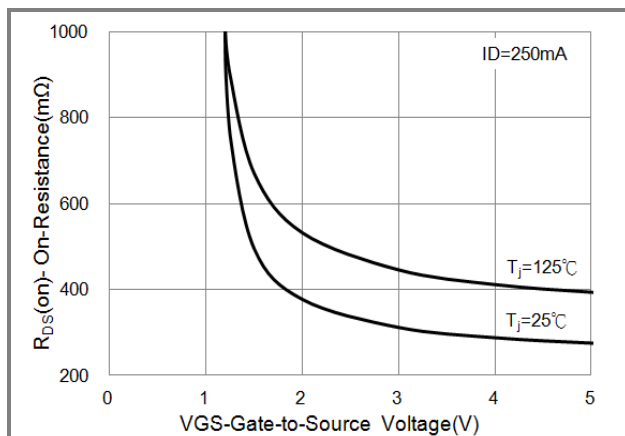
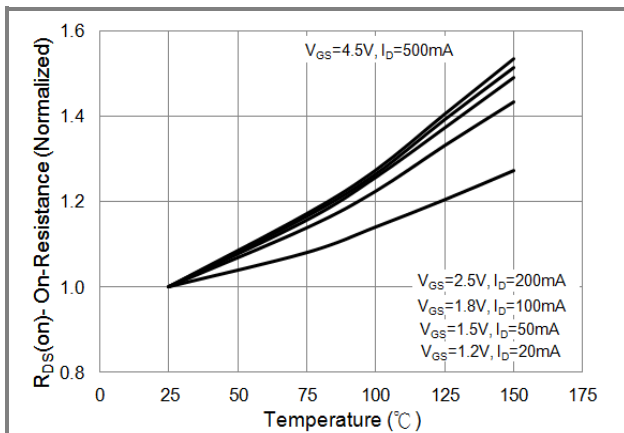
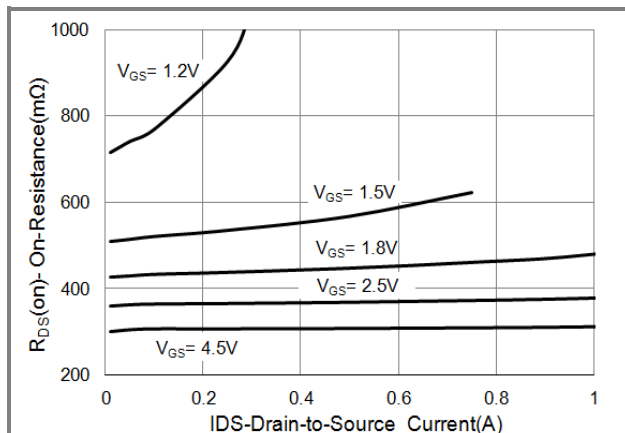
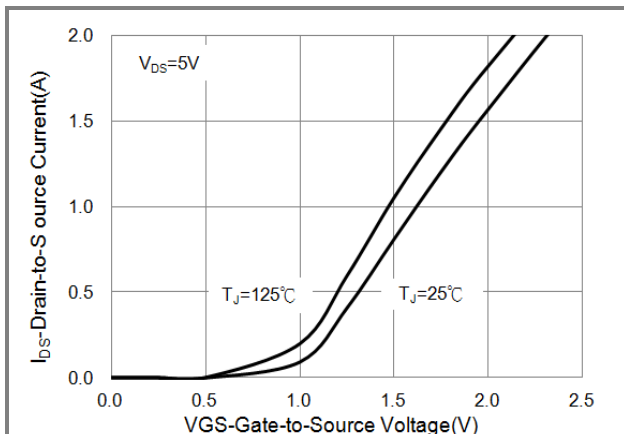
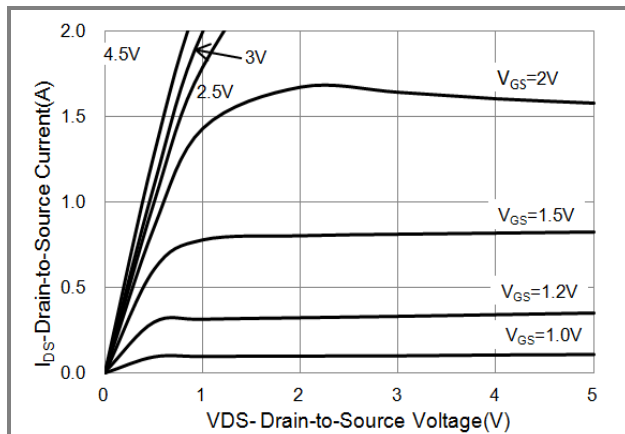
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	0.3	0.64	0.9	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =500mA	-	310	400	mΩ
		V _{GS} =2.5V, I _D =200mA	-	360	650	
		V _{GS} =1.8V, I _D =100mA	-	430	800	
		V _{GS} =1.5V, I _D =50mA	-	510	1200	
		V _{GS} =1.2V, I _D =20mA	-	710	3000	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =16V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>±</u> 8V, V _{DS} =0V	-	-	<u>±</u> 10	
Dynamic ^(Note 5)						
Total Gate Charge	Q _g	V _{DS} =10V, I _D =500mA, V _{GS} =4.5V ^(Note 1,2)	-	1.4	-	nC
Gate-Source Charge	Q _{gs}		-	0.22	-	
Gate-Drain Charge	Q _{gd}		-	0.21	-	
Input Capacitance	C _{iss}	V _{DS} =10V, V _{GS} =0V, f=1MHZ	-	67	-	pF
Output Capacitance	C _{oss}		-	19	-	
Reverse Transfer Capacitance	C _{rss}		-	6	-	
Turn-On Delay Time	td _(on)	V _{DD} =10V, I _D =150mA, V _{GS} =4.0V, R _G =10Ω ^(Note 1,2)	-	2.8	-	ns
Turn-On Rise Time	tr		-	20	-	
Turn-Off Delay Time	td _(off)		-	23	-	
Turn-Off Fall Time	tf		-	23	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _s	---	-	-	500	mA
Diode Forward Voltage	V _{SD}	I _S =500mA, V _{GS} =0V	-	0.87	1.3	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%.
2. Essentially independent of operating temperature typical characteristics.
3. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES



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TYPICAL CHARACTERISTIC CURVES

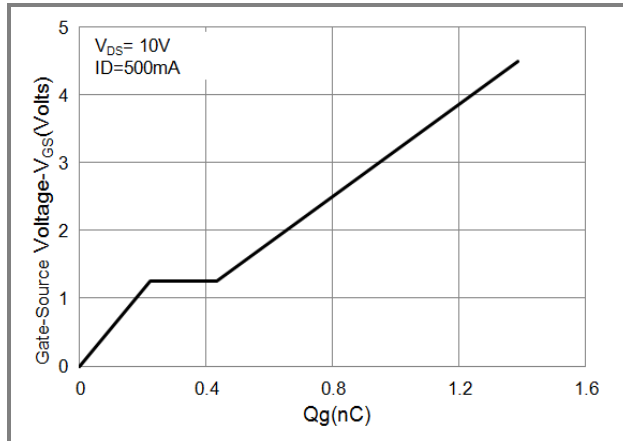


Fig.7 Gate-Charge Characteristics

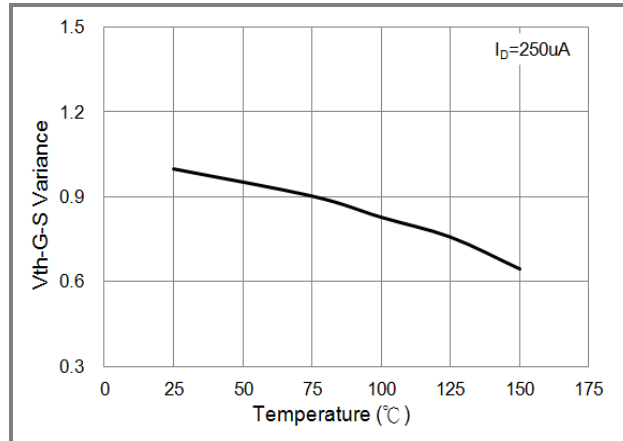


Fig.8 Threshold Voltage Variation with Temperature

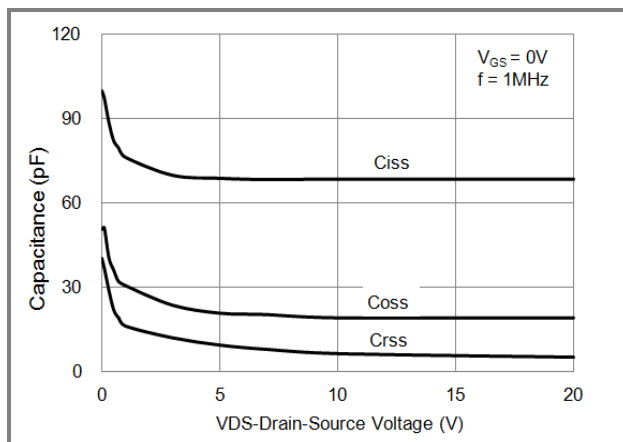


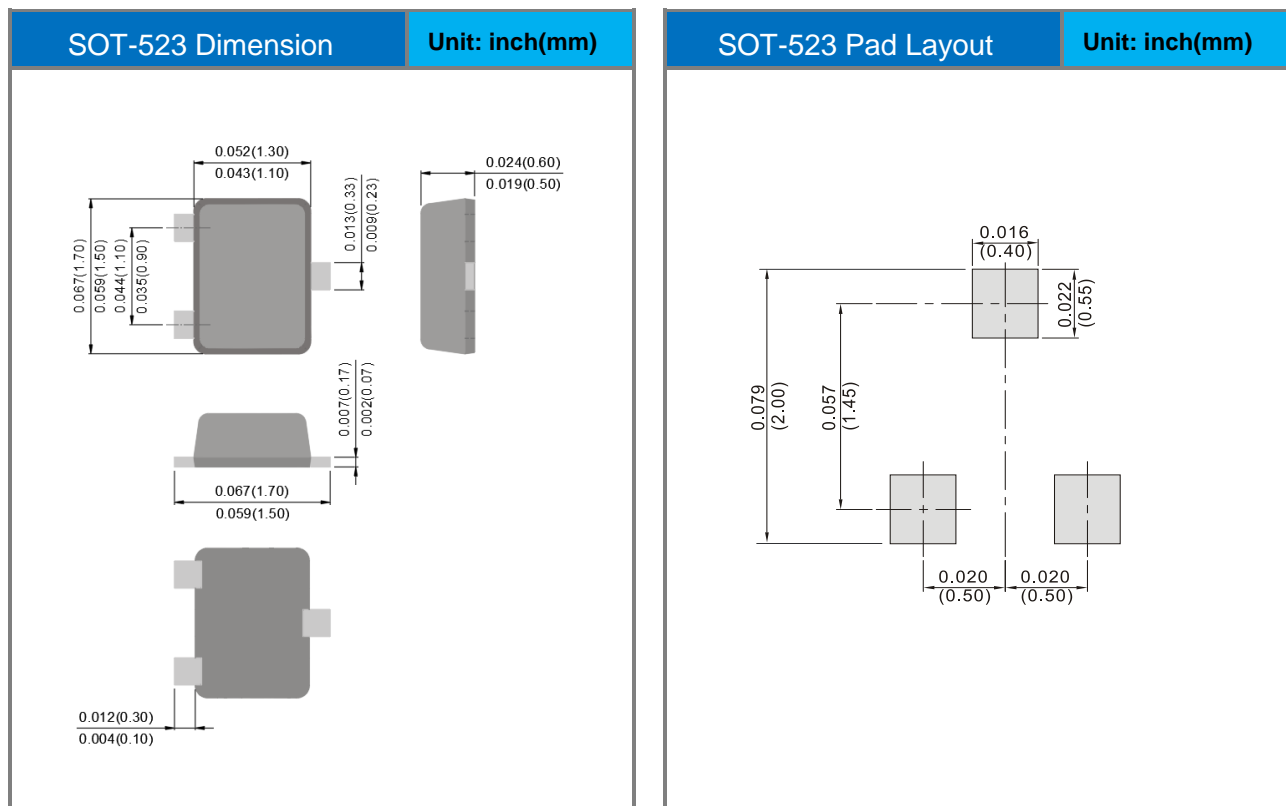
Fig.9 Capacitance vs. Drain-Source Voltage

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Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJE8408-AU	SOT-523	4K pcs / 7" reel	E08

Packaging Information & Mounting Pad Layout



PJE8408-AU

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