

PJC7403

20V P-Channel Enhancement Mode MOSFET – ESD Protected

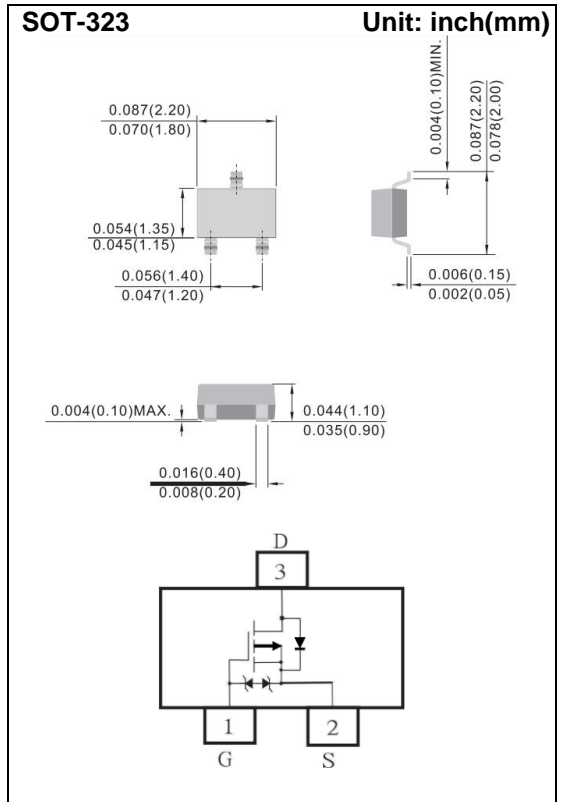
Voltage **-20 V** **Current** **-0.7A**

Features

- RDS(ON) , VGS@-4.5V, ID@-0.7A<325mΩ
- RDS(ON) , VGS@-2.5V, ID@-0.6A<420mΩ
- RDS(ON) , VGS@-1.8V, ID@-0.5A<600mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected 2KV HBM
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std.
(Halogen Free)

Mechanical Data

- Case: SOT-323 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0002 ounces, 0.005 grams
- Marking : C03



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DS}	-20	V
Gate-Source Voltage	V _{GS}	±8	V
Continuous Drain Current	I _D	-0.7	A
Pulsed Drain Current (Note 4)	I _{DM}	-2.8	A
Power Dissipation	P _D	T _a =25°C	350
		Derate above 25°C	2.8
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55~150	°C
Typical Thermal resistance	R _{θJA}	357	°C/W
- Junction to Ambient (Note 3)			

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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-0.5	-0.64	-1.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-0.7A	-	260	325	mΩ
		V _{GS} =-2.5V, I _D =-0.6A	-	310	420	
		V _{GS} =-1.8V, I _D =-0.5A	-	400	600	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±8V, V _{DS} =0V	-	±3.5	±10	uA
Dynamic						
Total Gate Charge	Q _g	V _{DS} =-10V, I _D =-0.7A, V _{GS} =-4.5V (Note 1,2)	-	2.2	-	nC
Gate-Source Charge	Q _{gs}		-	0.4	-	
Gate-Drain Charge	Q _{gd}		-	0.5	-	
Input Capacitance	C _{iss}	V _{DS} =-10V, V _{GS} =0V, f=1.0MHZ	-	165	-	pF
Output Capacitance	C _{oss}		-	25	-	
Reverse Transfer Capacitance	C _{rss}		-	14.7	-	
Switching						
Turn-On Delay Time	t _{d(on)}	V _{DD} =-10V, I _D =-0.7A, V _{GS} =-4.5V, R _G =6Ω (Note 1,2)	-	8.9	-	ns
Turn-On Rise Time	t _r		-	37	-	
Turn-Off Delay Time	t _{d(off)}		-	127	-	
Turn-Off Fall Time	t _f		-	70	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S	---	-	-	-1	A
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V	-	-0.97	-1.2	V

NOTES :

1. Pulse width ≤ 300us, Duty cycle ≤ 2%
2. Essentially independent of operating temperature typical characteristics.
3. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.

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TYPICAL CHARACTERISTIC CURVES

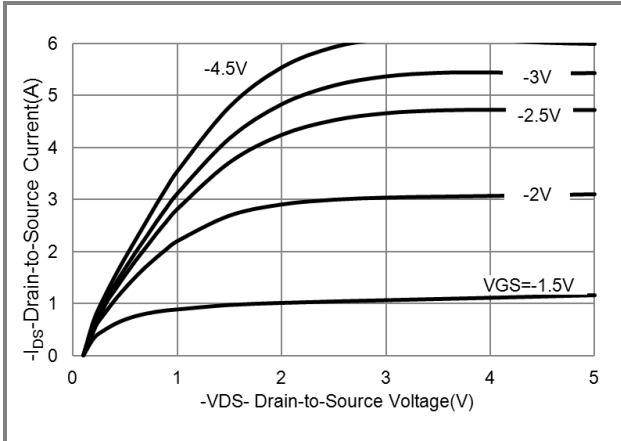


Fig.1 On-Region Characteristics

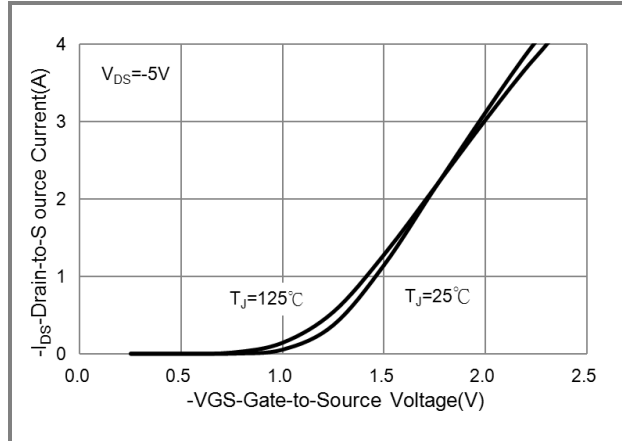


Fig.2 Transfer Characteristics

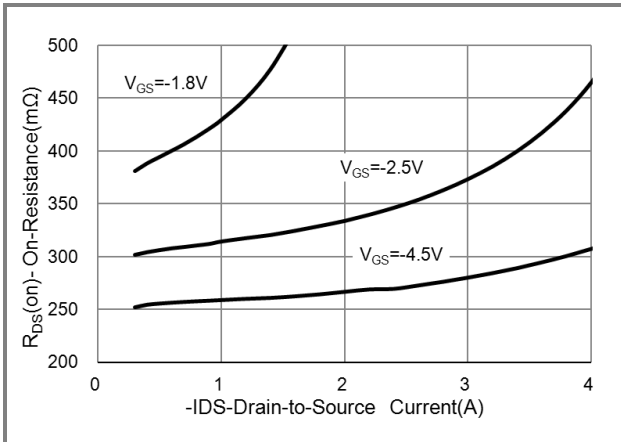


Fig.3 On-Resistance vs. Drain Current

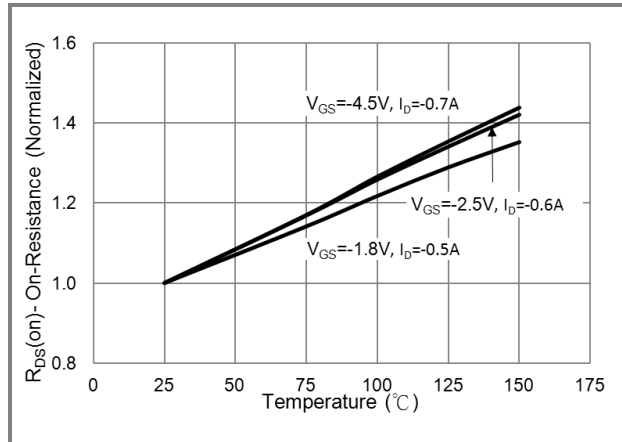


Fig.4 On-Resistance vs. Junction temperature

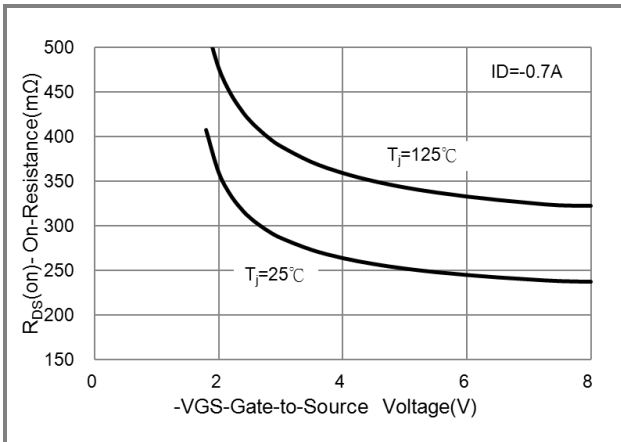


Fig.5 On-Resistance Variation with VGS.

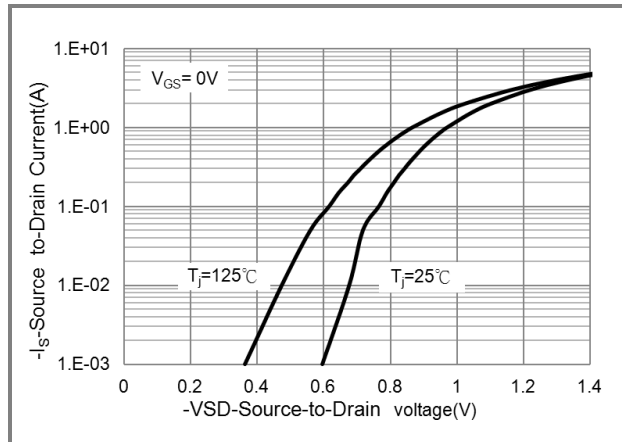


Fig.6 Body Diode Characteristics

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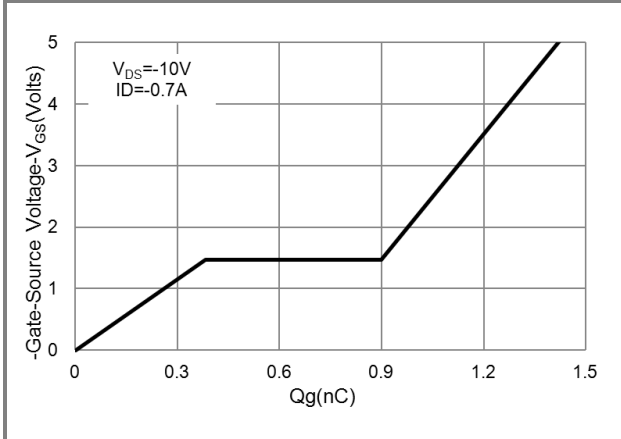


Fig.7 Gate-Charge Characteristics

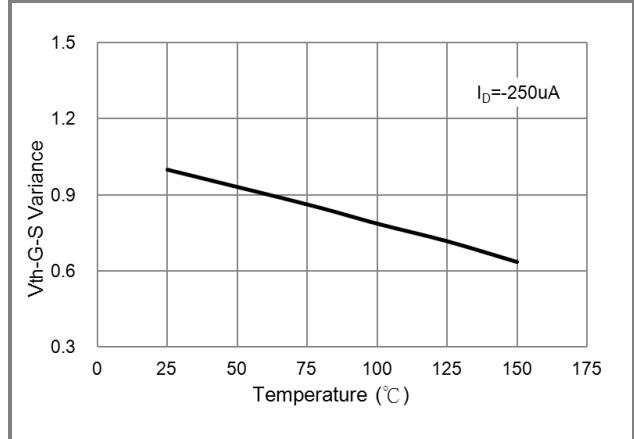


Fig.8 Threshold Voltage Variation with Temperature.

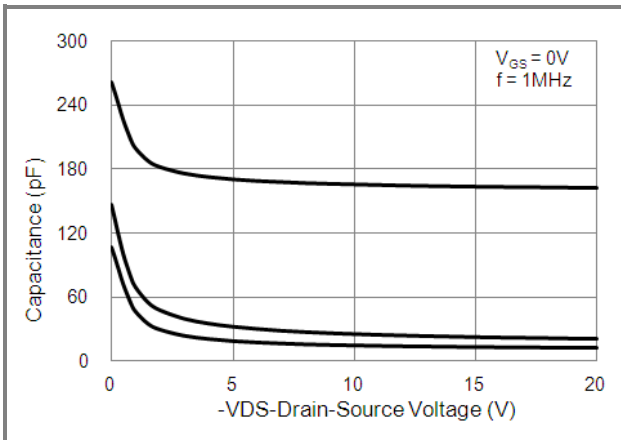


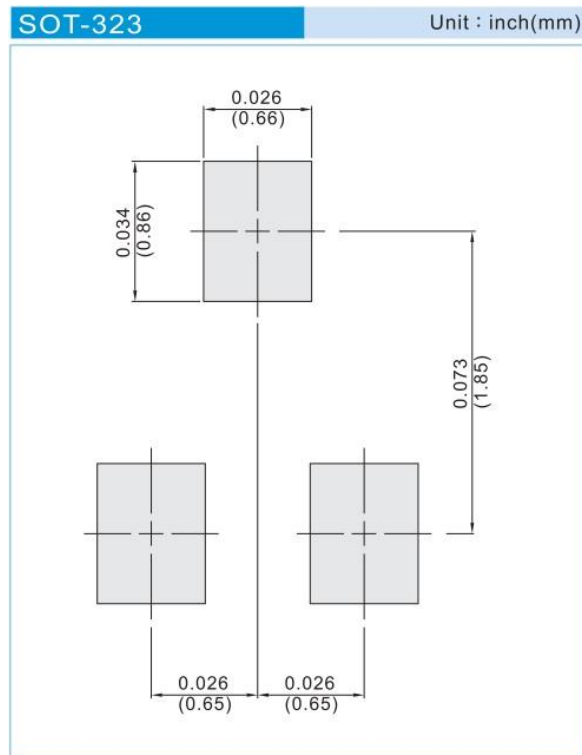
Fig.9 Threshold Voltage Variation with Temperature.

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Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJC7403	SOT-323	3K pcs / 7" reel	C03
PJC7403	SOT-323	12K pcs / 13" reel	C03

Mounting Pad Layout



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