



PJA3476

100V N-Channel Enhancement Mode MOSFET – ESD Protected

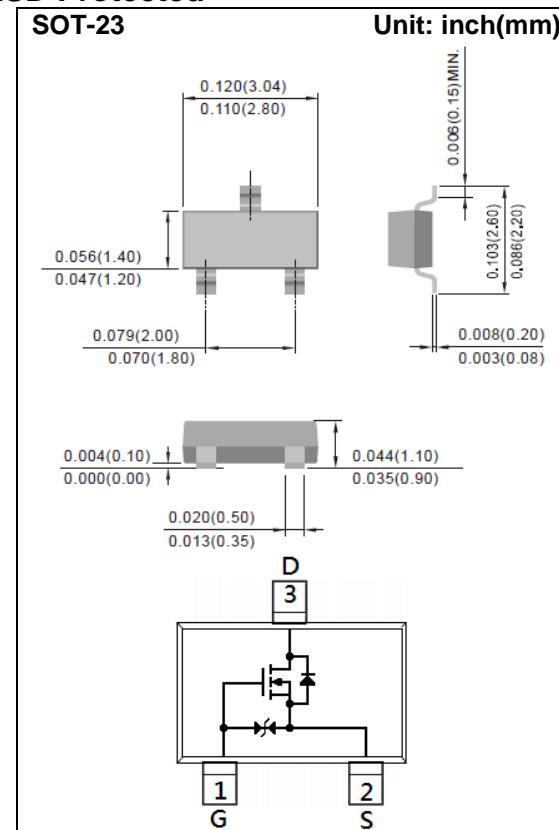
Voltage 100 V Current 300mA

Features

- RDS(ON) , V_{GS}@10V, I_D@300mA<6Ω
- RDS(ON) , V_{GS}@4.5V, I_D@200mA<9Ω
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- ESD Protected 2KV HBM
- Green molding compound as per IEC61249 Std.
(Halogen Free)

Mechanical Data

- Case: SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams
- Marking: A76



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	300	mA
Pulsed Drain Current ^(Note 4)	I_{DM}	800	mA
Power Dissipation	P_D	500	mW
		4	$\text{mW}/^\circ\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ\text{C}$
Typical Thermal resistance - Junction to Ambient ^(Note 3)	$R_{\theta JA}$	250	$^\circ\text{C}/\text{W}$



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Electrical Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.5	1.77	2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=300mA$	-	4	6	Ω
		$V_{GS}=4.5V, I_D=200mA$	-	4.2	9	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=80V, V_{GS}=0V$	-	-	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 10	μA
Dynamic <small>(Note 5)</small>						
Total Gate Charge	Q_g	$V_{DS}=30V, I_D=200mA,$ $V_{GS}=10V$ <small>(Note 1,2)</small>	-	1.8	-	nC
Gate-Source Charge	Q_{gs}		-	0.4	-	
Gate-Drain Charge	Q_{gd}		-	0.3	-	
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0MHz$	-	45	-	pF
Output Capacitance	C_{oss}		-	14	-	
Reverse Transfer Capacitance	C_{rss}		-	7.8	-	
Turn-On Delay Time	$td_{(on)}$	$V_{DD}=30V, I_D=200mA,$ $V_{GS}=10V,$ $R_G=6\Omega$ <small>(Note 1,2)</small>	-	3.4	-	ns
Turn-On Rise Time	tr		-	19	-	
Turn-Off Delay Time	$td_{(off)}$		-	8.2	-	
Turn-Off Fall Time	tf		-	20	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	400	mA
Diode Forward Voltage	V_{SD}	$I_S=400mA, V_{GS}=0V$	-	0.9	1.3	V

NOTES :

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. R_{QJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTIC CURVES

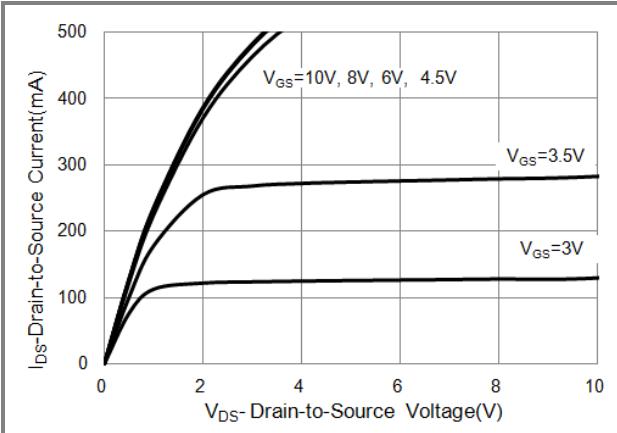


Fig.1 On-Region Characteristics

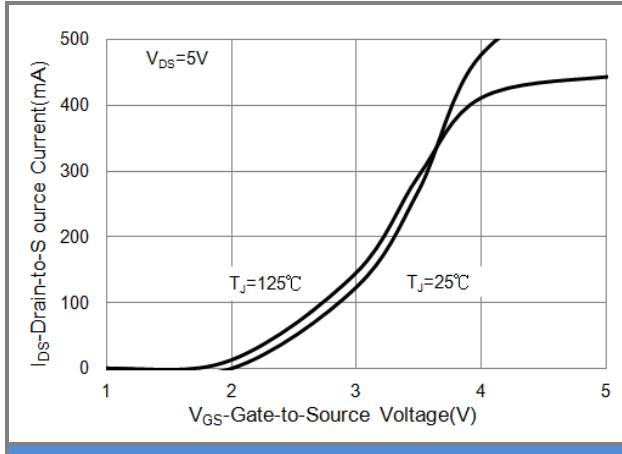


Fig.2 Transfer Characteristics

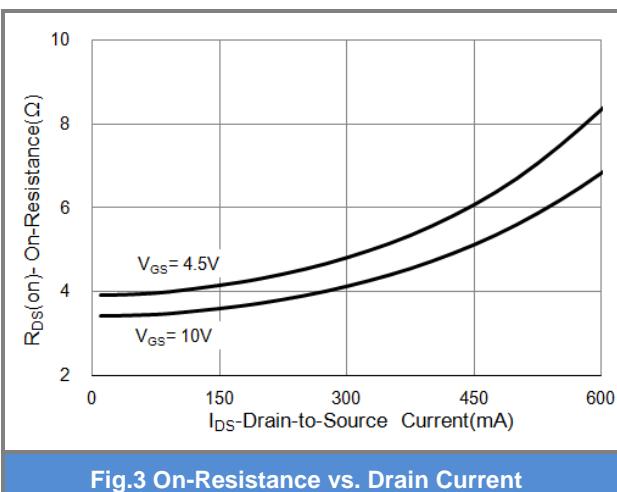


Fig.3 On-Resistance vs. Drain Current

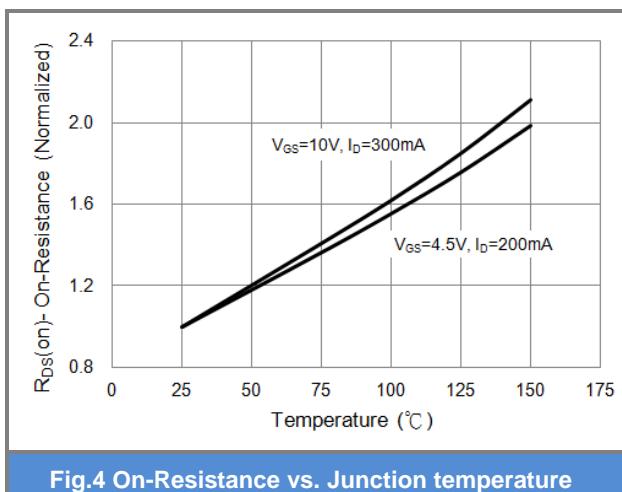


Fig.4 On-Resistance vs. Junction temperature

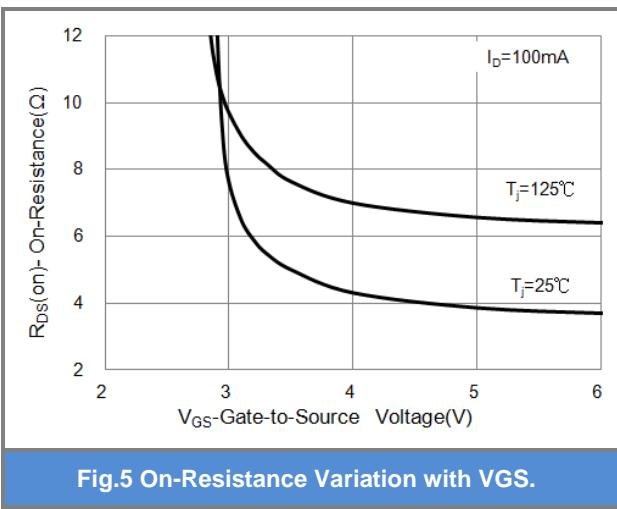


Fig.5 On-Resistance Variation with VGS.

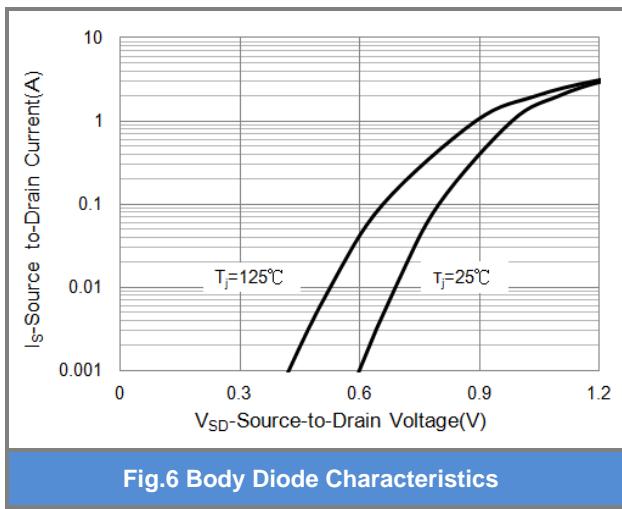
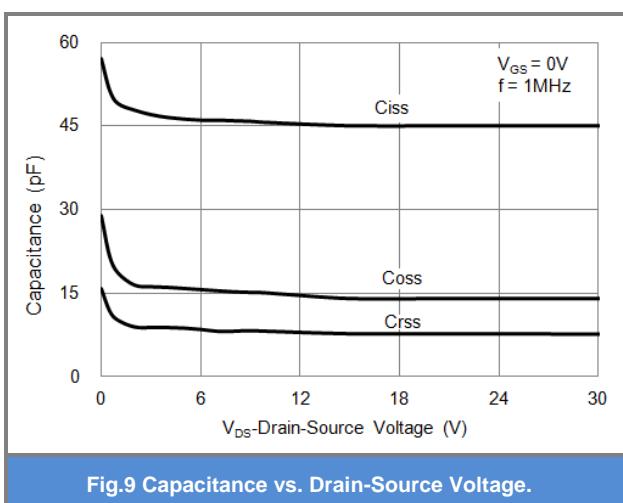
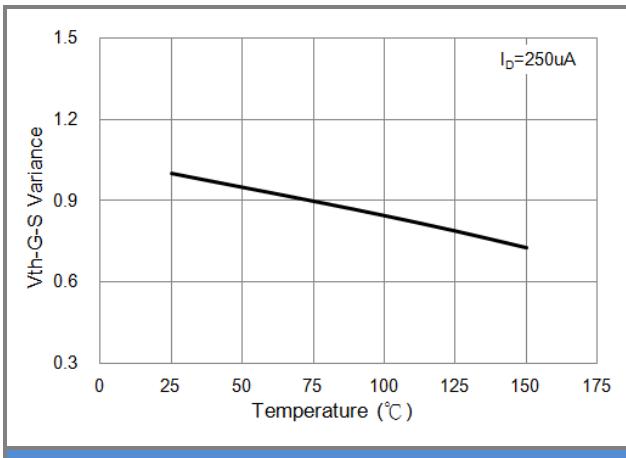
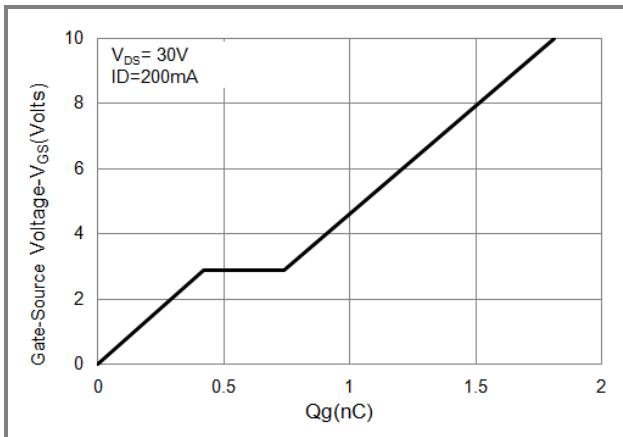


Fig.6 Body Diode Characteristics



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TYPICAL CHARACTERISTIC CURVES



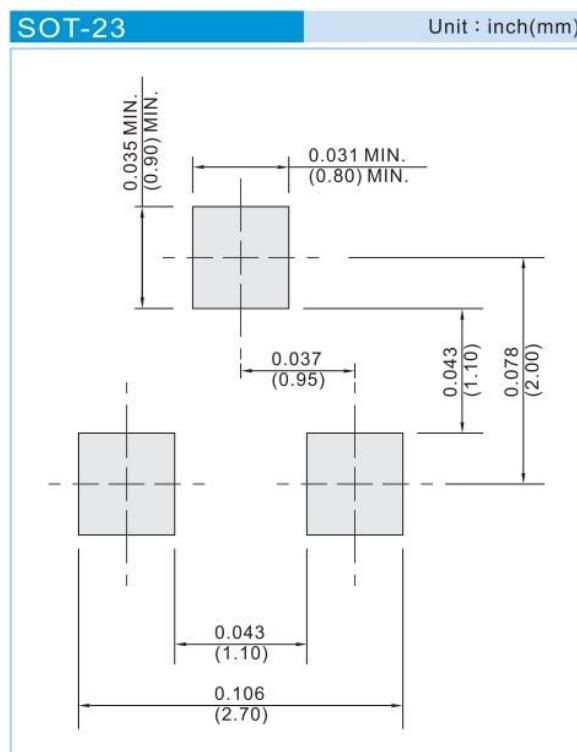


PJA3476

PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing type	Marking	Version
PJA3476_R1_00001	SOT-23	3K pcs / 7" reel	A76	Halogen free
PJA3476_R2_00001	SOT-23	12K pcs / 13" reel	A76	Halogen free

MOUNTING PAD LAYOUT





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