

# PJA3460

## 60V N-Channel Enhancement Mode MOSFET

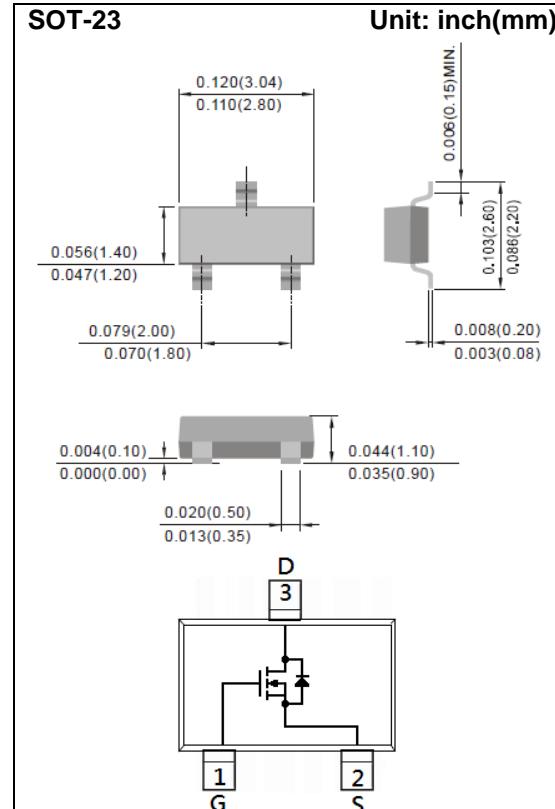
Voltage      60 V      Current      2.5 A

### Features

- R<sub>DS(ON)</sub> , V<sub>GS</sub>@10V, I<sub>D</sub>@2.0A<75mΩ
- R<sub>DS(ON)</sub> , V<sub>GS</sub>@4.5V, I<sub>D</sub>@1.0A<90mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

### Mechanical Data

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0084 grams
- Marking : A60



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>GS</sub>	+20	V
Continuous Drain Current	I <sub>D</sub>	2.5	A
Pulsed Drain Current <sup>(Note 4)</sup>	I <sub>DM</sub>	10	A
Power Dissipation	T <sub>A</sub> =25°C	1.25	W
	Derate above 25°C	10	mW/°C
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55~150	°C
Typical Thermal Resistance - Junction to Ambient <sup>(Note 3)</sup>	R <sub>θJA</sub>	100	°C/W

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## Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{D}}=250\mu\text{A}$	60	-	-	V
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_{\text{D}}=250\mu\text{A}$	1.0	1.75	2.5	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_{\text{D}}=2.0\text{A}$	-	55	75	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_{\text{D}}=1.0\text{A}$	-	63	90	
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=48\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate-Source Leakage Current	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
<b>Dynamic</b> <sup>(Note 5)</sup>						
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=48\text{V}, \text{I}_{\text{D}}=2.0\text{A}, \text{V}_{\text{GS}}=10\text{V}^{(\text{Note 1,2})}$	-	9.3	-	nC
Gate-Source Charge	$\text{Q}_{\text{gs}}$		-	2.2	-	
Gate-Drain Charge	$\text{Q}_{\text{gd}}$		-	1.9	-	
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=15\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{f}=1.0\text{MHZ}$	-	509	-	pF
Output Capacitance	$\text{C}_{\text{oss}}$		-	47	-	
Reverse Transfer Capacitance	$\text{Crss}$		-	23	-	
Turn-On Delay Time	$\text{td}_{(\text{on})}$	$\text{V}_{\text{DD}}=30\text{V}, \text{I}_{\text{D}}=2.0\text{A}, \text{V}_{\text{GS}}=10\text{V}, \text{R}_g=3.3\Omega^{(\text{Note 1,2})}$	-	3.2	-	ns
Turn-On Rise Time	$\text{tr}$		-	9.7	-	
Turn-Off Delay Time	$\text{td}_{(\text{off})}$		-	18.5	-	
Turn-Off Fall Time	$\text{tf}$		-	6.4	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$\text{I}_{\text{s}}$	---	-	-	2.5	A
Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{I}_{\text{s}}=1\text{A}, \text{V}_{\text{GS}}=0\text{V}$	-	0.77	1.2	V

### NOTES :

1. Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .
2. Essentially independent of operating temperature typical characteristics.
3.  $\text{R}_{\text{eJA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing.

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## TYPICAL CHARACTERISTIC CURVES

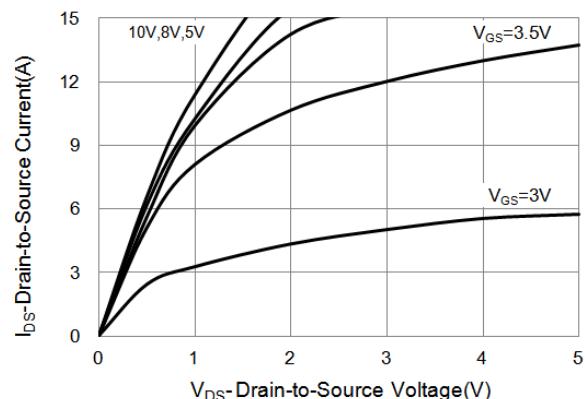


Fig.1 On-Region Characteristics

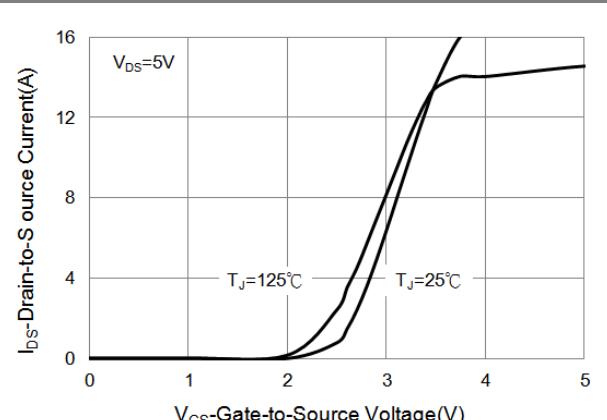


Fig.2 Transfer Characteristics

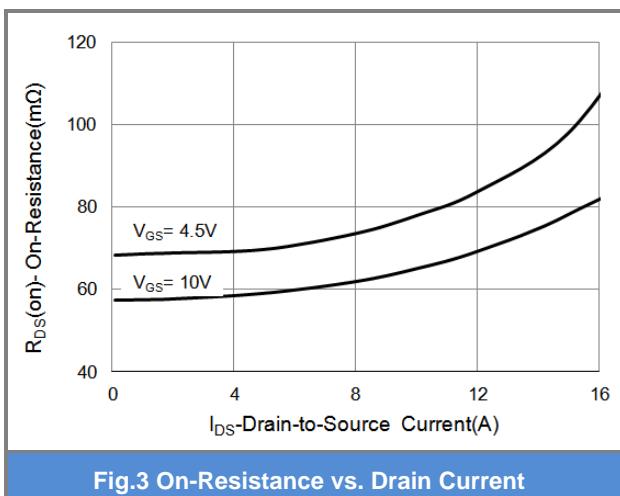


Fig.3 On-Resistance vs. Drain Current

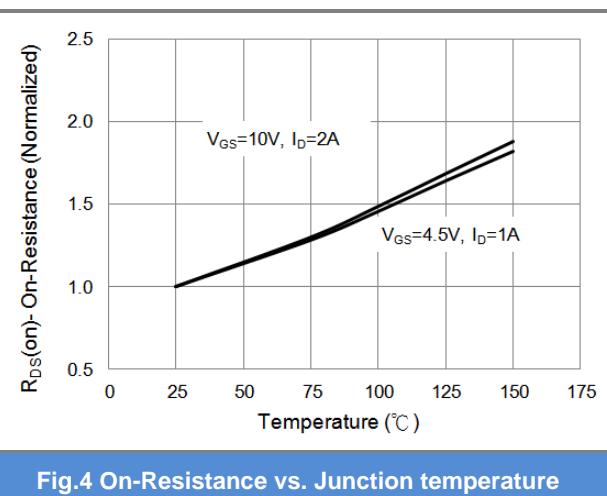


Fig.4 On-Resistance vs. Junction temperature

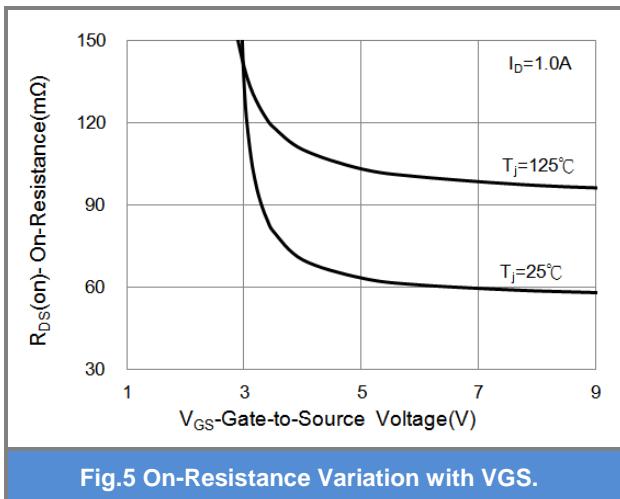


Fig.5 On-Resistance Variation with VGS.

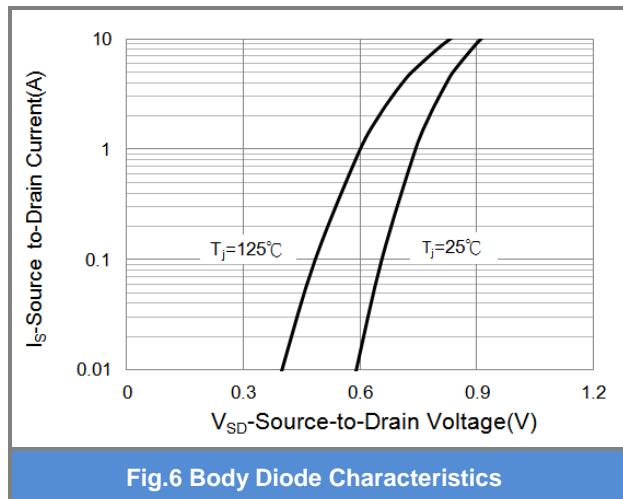


Fig.6 Body Diode Characteristics

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## TYPICAL CHARACTERISTIC CURVES

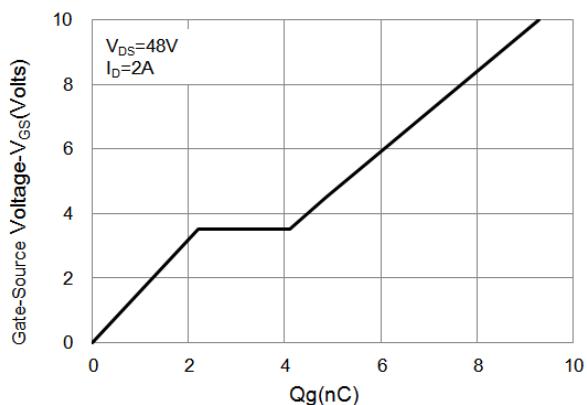


Fig.7 Gate-Charge Characteristics

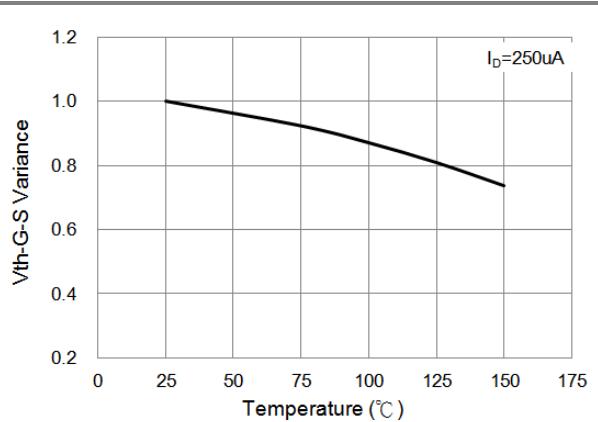


Fig.8 Threshold Voltage Variation with Temperature.

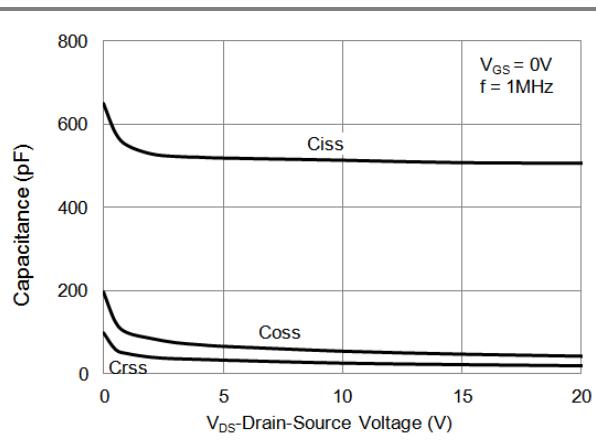


Fig.9 Capacitance vs. Drain-Source Voltage.

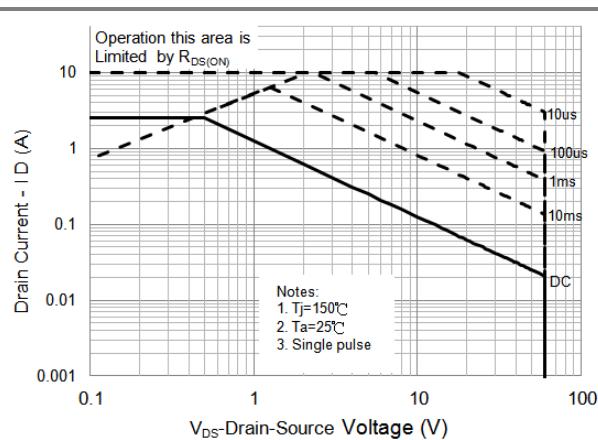


Fig.10 Maximum Safe Operating Area.

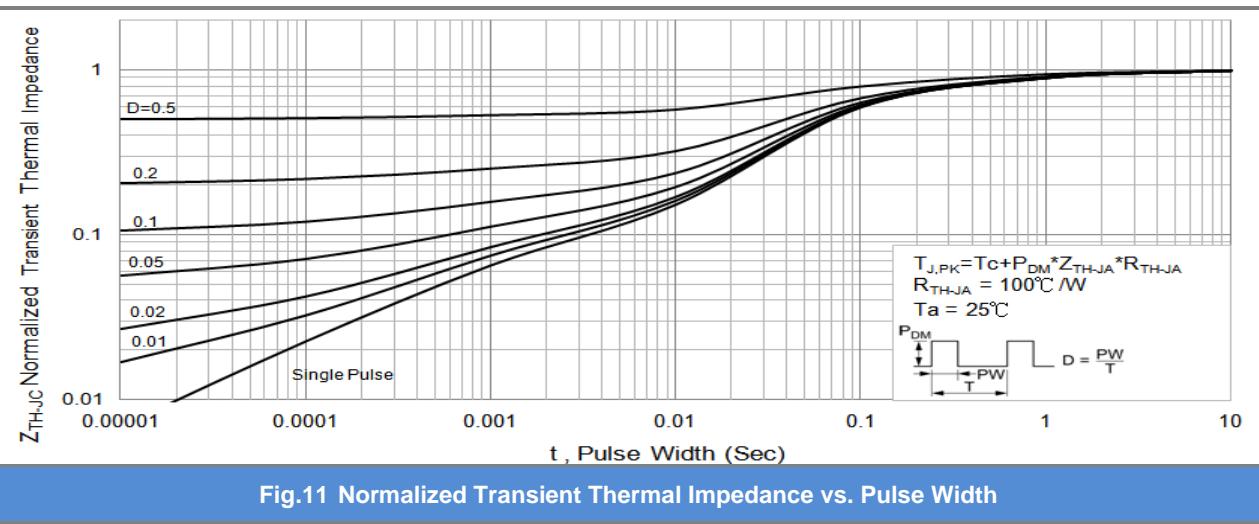


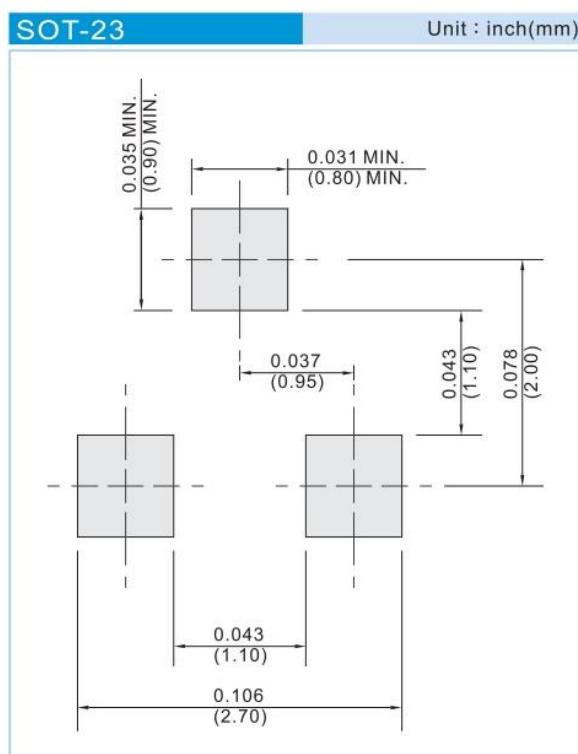
Fig.11 Normalized Transient Thermal Impedance vs. Pulse Width

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## Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJA3460	SOT-23	3K pcs / 7" reel	A60

## Mounting Pad Layout



## PJA3460

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