

PJA3441

40V P-Channel Enhancement Mode MOSFET

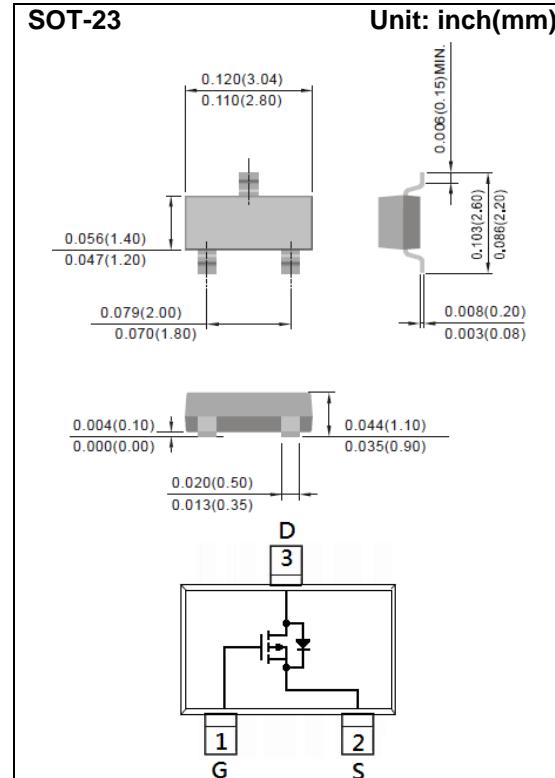
Voltage -40 V Current -3.1A

Features

- R_{DS(ON)} , V_{GS}@-10V, I_D@-3.1A<88mΩ
- R_{DS(ON)} , V_{GS}@-4.5V, I_D@-2.6A<108mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std.
(Halogen Free)

Mechanical Data

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0084 grams
- Marking : A41



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V _{DS}	-40	V
Gate-Source Voltage	V _{GS}	<u>±</u> 20	V
Continuous Drain Current	I _D	-3.1	A
Pulsed Drain Current ^(Note 4)	I _{DM}	-12.4	A
Power Dissipation	T _a =25°C	1.25	W
	Derate above 25°C	10	mW/°C
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)	R _{θJA}	100	°C/W

PJA3441

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=-250\mu\text{A}$	-40	-	-	V
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=-250\mu\text{A}$	-1.0	-1.5	-2.5	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=-10\text{V}, \text{I}_D=-3.1\text{A}$	-	74	88	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_D=-2.6\text{A}$	-	88	108	
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=-40\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-0.01	-1	μA
Gate-Source Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	± 10	± 100	nA
Dynamic ^(Note 5)						
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=-20\text{V}, \text{I}_D=-3.1\text{A}, \text{V}_{\text{GS}}=-4.5\text{V}^{(\text{Note 1,2})}$	-	6	-	nC
Gate-Source Charge	Q_{gs}		-	1.6	-	
Gate-Drain Charge	Q_{gd}		-	2.3	-	
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=-20\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{f}=1.0\text{MHZ}$	-	505	-	pF
Output Capacitance	C_{oss}		-	48	-	
Reverse Transfer Capacitance	Crss		-	33	-	
Turn-On Delay Time	$\text{td}_{(\text{on})}$	$\text{V}_{\text{DD}}=-20\text{V}, \text{I}_D=-2.5\text{A}, \text{V}_{\text{GS}}=-10\text{V}, \text{R}_G=1\Omega^{(\text{Note 1,2})}$	-	6	-	ns
Turn-On Rise Time	tr		-	35	-	
Turn-Off Delay Time	$\text{td}_{(\text{off})}$		-	18	-	
Turn-Off Fall Time	tf		-	10	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_s	---	-	-	-1.0	A
Diode Forward Voltage	V_{SD}	$\text{I}_s=-1.0\text{A}, \text{V}_{\text{GS}}=0\text{V}$	-	-0.82	-1.2	V
Reverse Recovery Time	trr	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=-2.5\text{A}$ $d\text{I}_F/dt=100\text{A}/\mu\text{s}$	-	13	-	ns
Reverse Recovery Charge	Qrr		-	8.7	-	nC

NOTES :

1. Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.
2. Essentially independent of operating temperature typical characteristics.
3. R_{eJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing.

PJA3441

TYPICAL CHARACTERISTIC CURVES

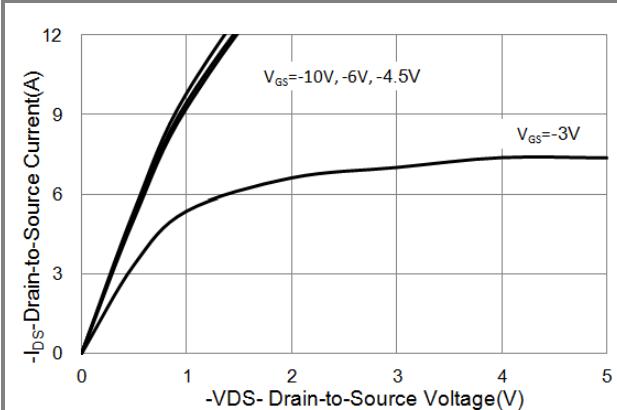


Fig.1 On-Region Characteristics

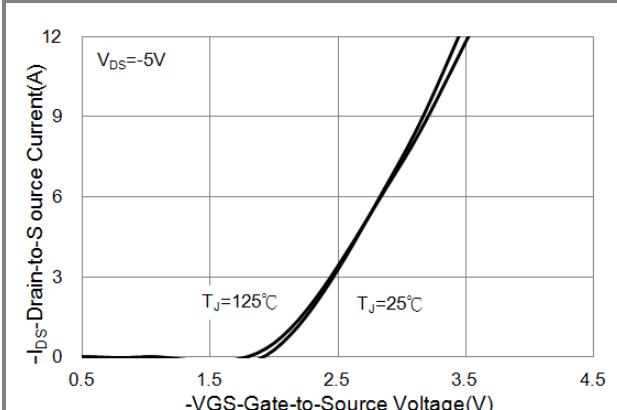


Fig.2 Transfer Characteristics

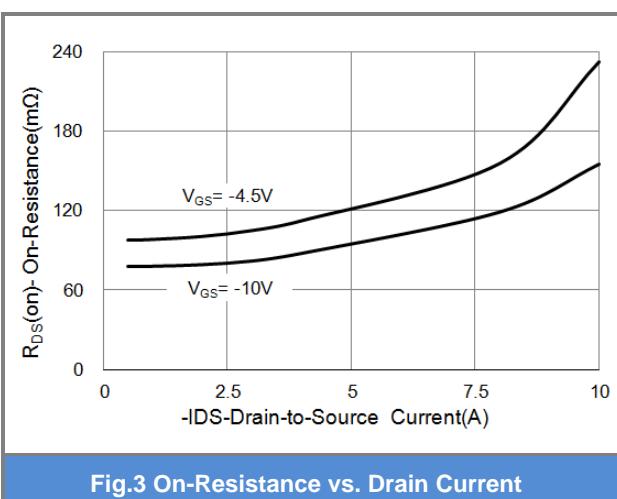


Fig.3 On-Resistance vs. Drain Current

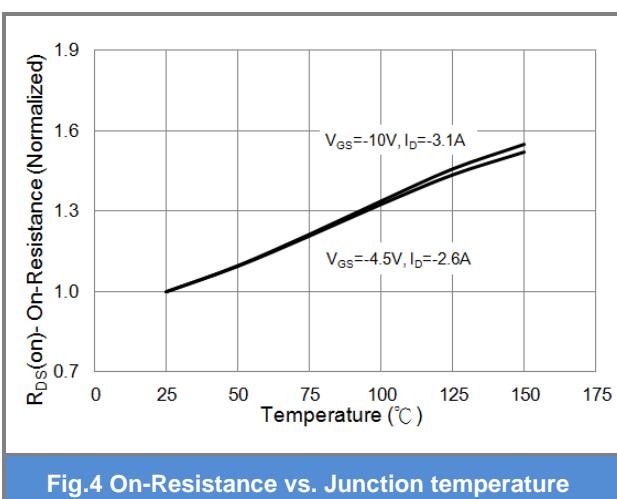


Fig.4 On-Resistance vs. Junction temperature

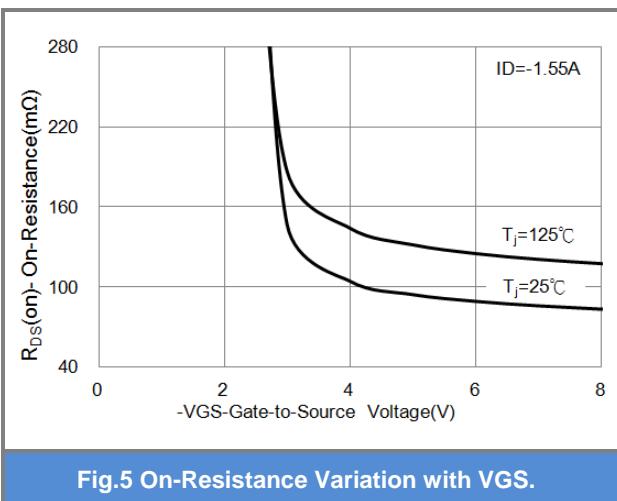


Fig.5 On-Resistance Variation with VGS.

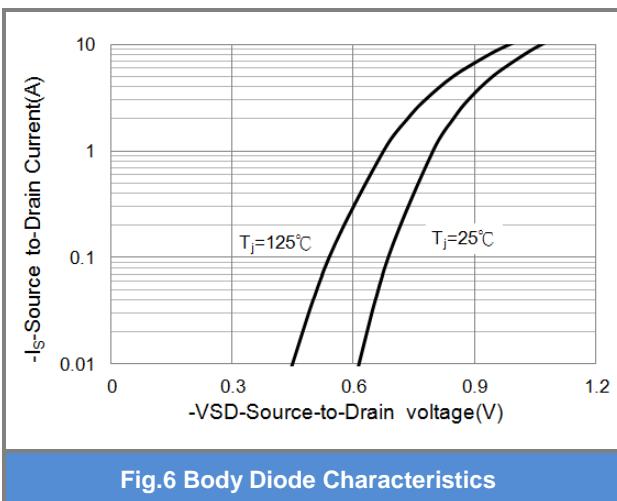


Fig.6 Body Diode Characteristics

PJA3441

TYPICAL CHARACTERISTIC CURVES

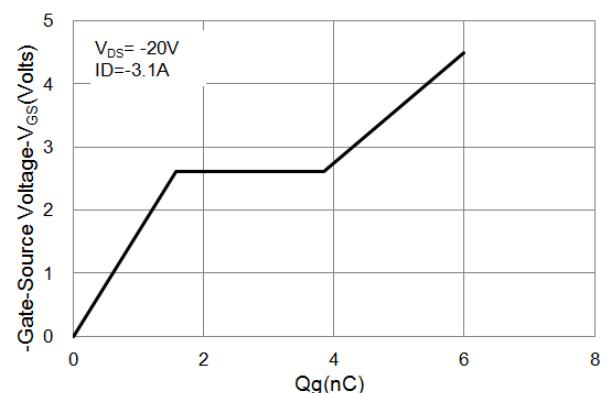


Fig.7 Gate-Charge Characteristics

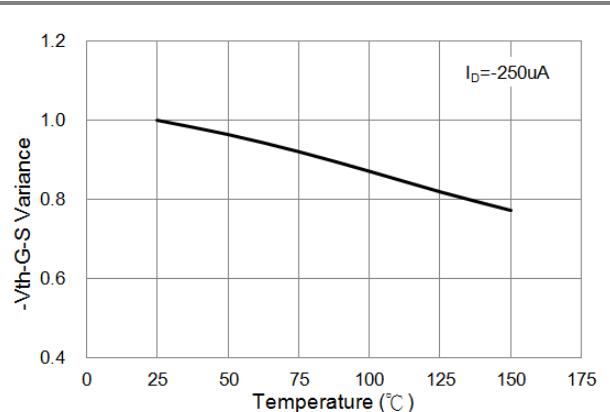


Fig.8 Threshold Voltage Variation with Temperature.

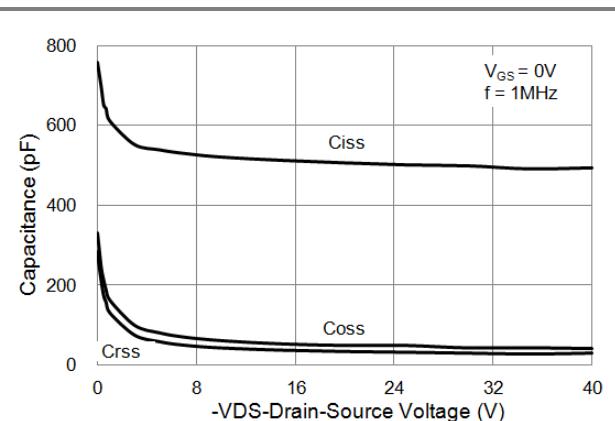


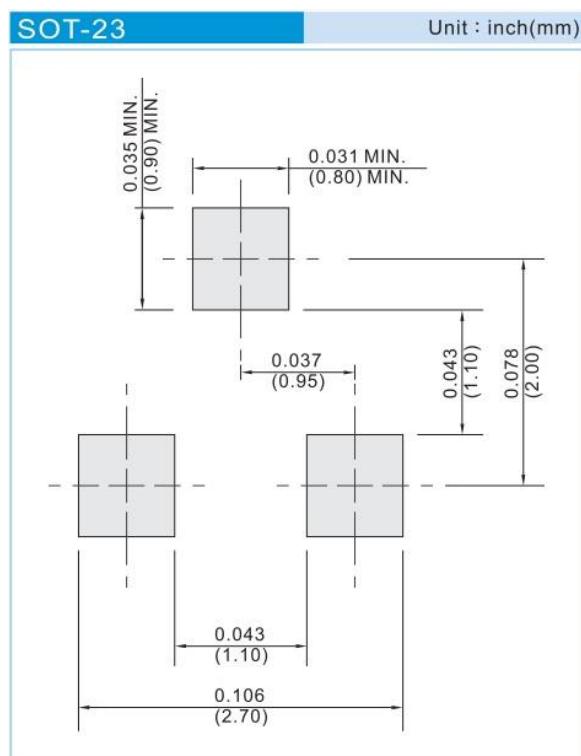
Fig.9 Capacitance vs. Drain-Source Voltage.

PJA3441

Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJA3441	SOT-23	3K pcs / 7" reel	A41

Mounting Pad Layout



PJA3441

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