

# PJA3435

## 20V P-Channel Enhancement Mode MOSFET – ESD Protected

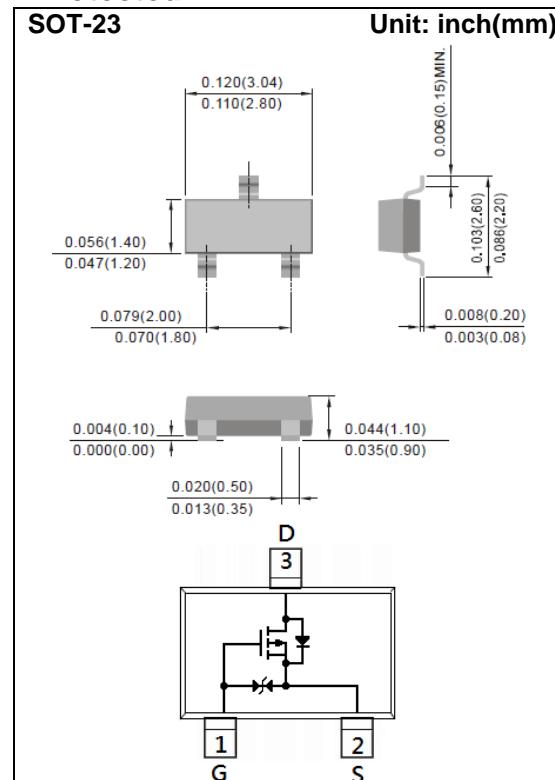
**Voltage**    **-20 V**    **Current**    **-0.5A**

### Features

- Low Voltage Drive (1.2V).
- Advanced Trench Process Technology
- Specially Designed for Load switch, PWM Application, etc.
- ESD Protected
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0084 grams
- Marking : A35



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	$V_{DS}$	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 10$	V
Continuous Drain Current	$I_D$	-0.5	A
Pulsed Drain Current <sup>(Note 4)</sup>	$I_{DM}$	-1.0	A
Power Dissipation	$T_a=25^\circ\text{C}$	500	mW
	Derate above $25^\circ\text{C}$	4	$\text{mW}/^\circ\text{C}$
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	$^\circ\text{C}$
Typical Thermal Resistance - Junction to Ambient <sup>(Note 3)</sup>	$R_{\theta JA}$	250	$^\circ\text{C}/\text{W}$

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## Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-20	-	-	V
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-0.3	-0.59	-1.0	V
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-0.5\text{A}$	-	0.85	1.2	$\Omega$
		$V_{\text{GS}}=-2.5\text{V}, I_{\text{D}}=-0.2\text{A}$	-	0.98	1.5	
		$V_{\text{GS}}=-1.8\text{V}, I_{\text{D}}=-0.1\text{A}$	-	1.15	2.2	
		$V_{\text{GS}}=-1.5\text{V}, I_{\text{D}}=-0.05\text{A}$	-	1.33	3.6	
		$V_{\text{GS}}=-1.2\text{V}, I_{\text{D}}=-0.01\text{A}$	-	1.5	6.0	
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-16\text{V}, V_{\text{GS}}=0\text{V}$	-	-0.01	-1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 8\text{V}, V_{\text{DS}}=0\text{V}$	-	$\pm 2$	$\pm 10$	$\mu\text{A}$
<b>Dynamic</b> <sup>(Note 5)</sup>						
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-0.5\text{A}, V_{\text{GS}}=-4.5\text{V}^{(\text{Note 1,2})}$	-	1.4	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	0.19	-	
Gate-Drain Charge	$Q_{\text{gd}}$		-	0.2	-	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-10\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHZ}$	-	38	-	pF
Output Capacitance	$C_{\text{oss}}$		-	15	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	9	-	
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=-10\text{V}, I_{\text{D}}=-0.5\text{A}, V_{\text{GS}}=-4.5\text{V}, R_{\text{G}}=6\Omega^{(\text{Note 1,2})}$	-	7.2	-	ns
Turn-On Rise Time	$t_{\text{r}}$		-	21	-	
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	85	-	
Turn-Off Fall Time	$t_{\text{f}}$		-	116	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_{\text{s}}$	---	-	-	-0.5	A
Diode Forward Voltage	$V_{\text{sd}}$	$I_{\text{s}}=-0.5\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.93	-1.3	V

### NOTES :

1. Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .
2. Essentially independent of operating temperature typical characteristics.
3.  $R_{\text{OJA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing.

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## TYPICAL CHARACTERISTIC CURVES

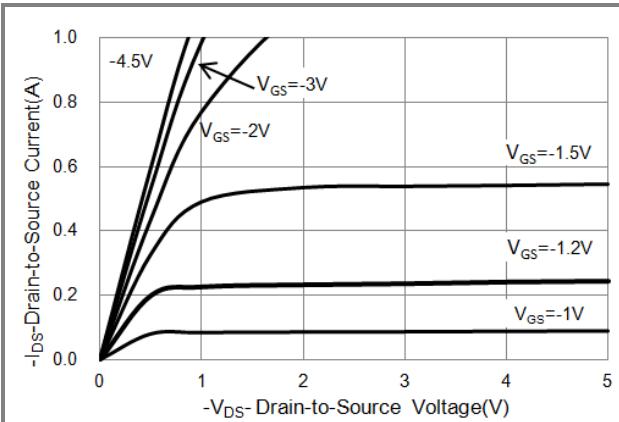


Fig.1 On-Region Characteristics

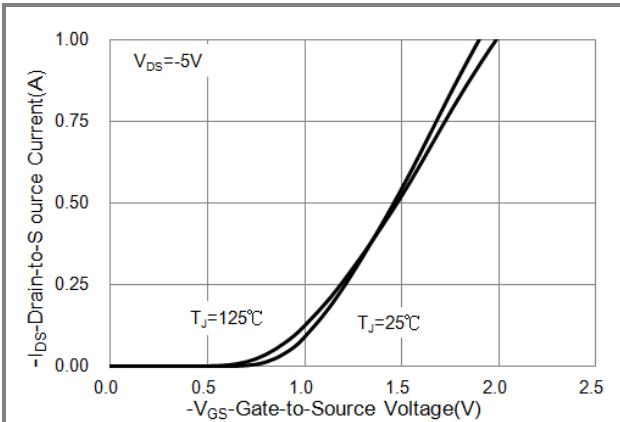


Fig.2 Transfer Characteristics

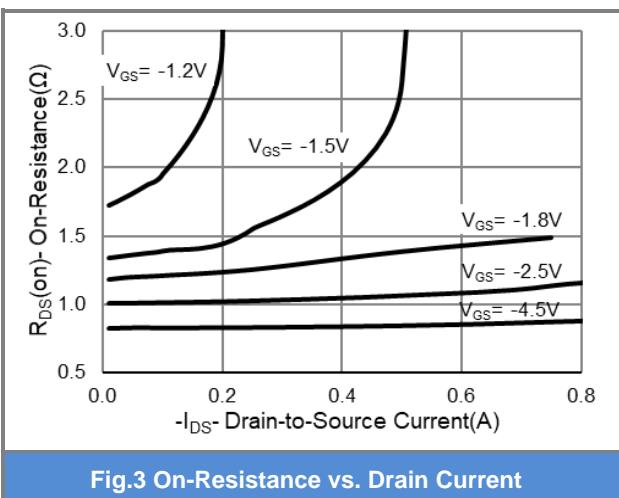


Fig.3 On-Resistance vs. Drain Current

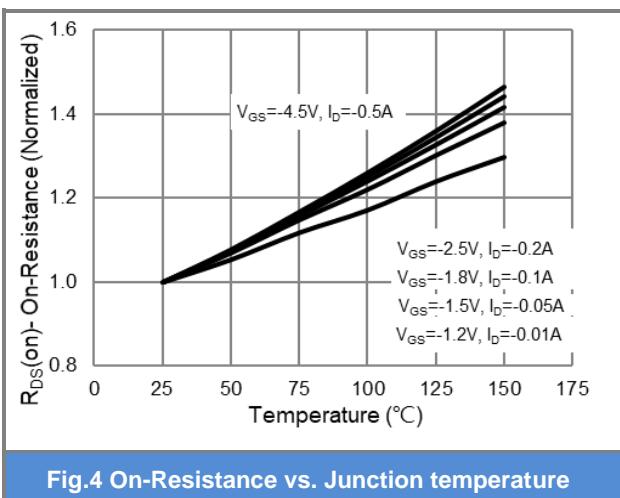


Fig.4 On-Resistance vs. Junction temperature

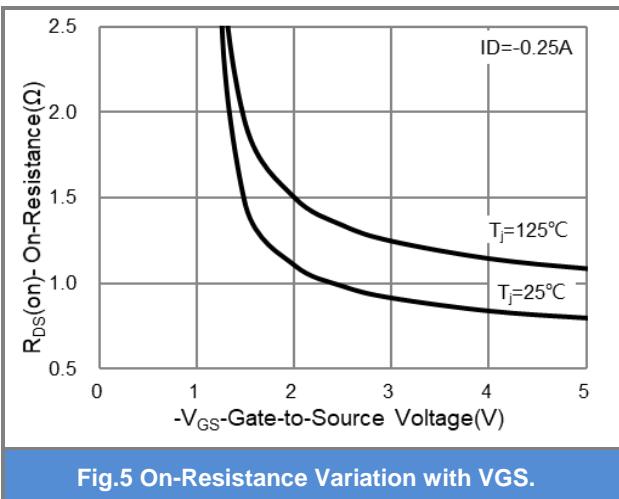


Fig.5 On-Resistance Variation with VGS.

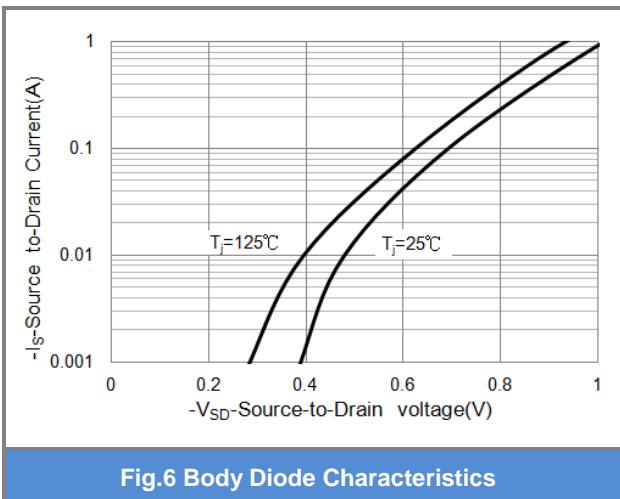


Fig.6 Body Diode Characteristics

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## TYPICAL CHARACTERISTIC CURVES

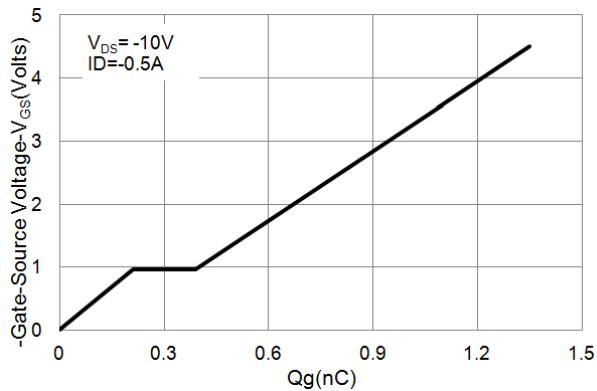


Fig.7 Gate-Charge Characteristics

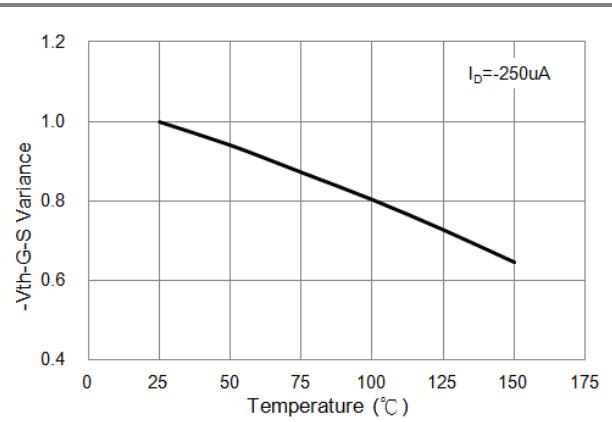


Fig.8 Threshold Voltage Variation with Temperature.

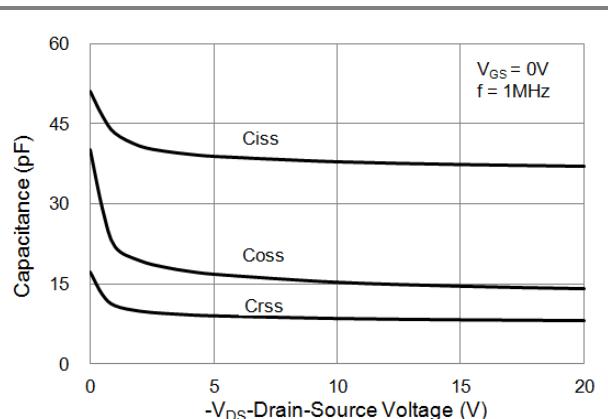


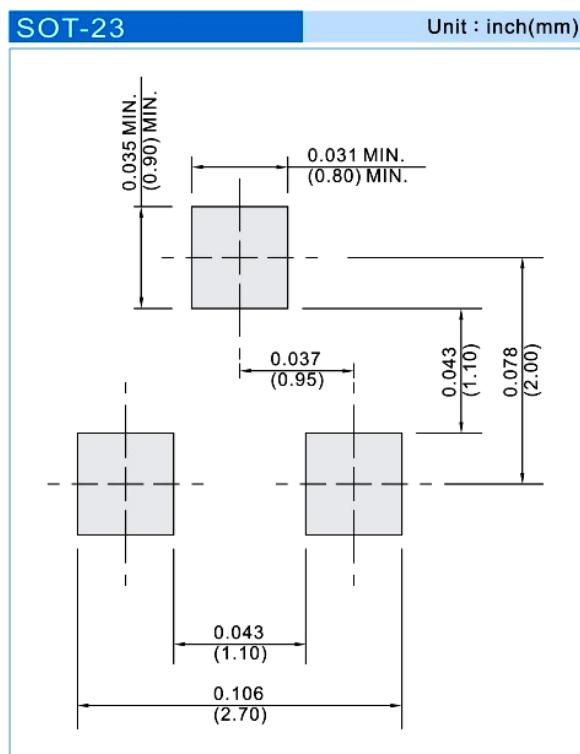
Fig.9 Capacitance vs. Drain-Source Voltage.

## PJA3435

### Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJA3435	SOT-23	3K pcs / 7" reel	A35
PJA3435	SOT-23	12K pcs / 13" reel	A35

### Mounting Pad Layout



## PJA3435

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