

PJA3415A

20V P-Channel Enhancement Mode MOSFET

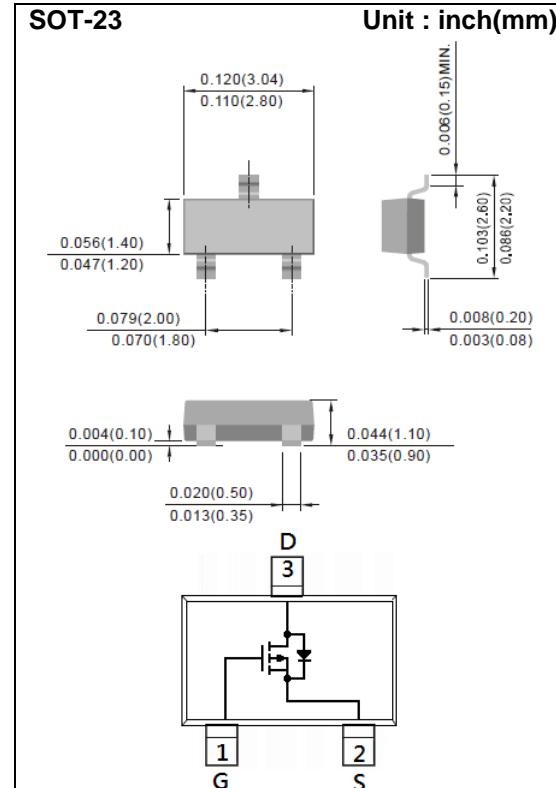
Voltage -20 V Current -4.5A

Features

- R_{DS(ON)} , V_{GS}@-4.5V, I_D@-4.5A<46mΩ
- R_{DS(ON)} , V_{GS}@-2.5V, I_D@-3.0A<56mΩ
- R_{DS(ON)} , V_{GS}@-1.8V, I_D@-1.5A<88mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.
(Halogen Free)

Mechanical Data

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams
- Marking: A5A



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V _{DS}	-20	V
Gate-Source Voltage	V _{GS}	±12	V
Continuous Drain Current	I _D	-4.5	A
Pulsed Drain Current	I _{DM}	-18	A
Power Dissipation	T _a =25°C	1.25	W
	Derate above 25°C	10	mW/°C
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient (Note 3)	R _{θJA}	100	°C/W

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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

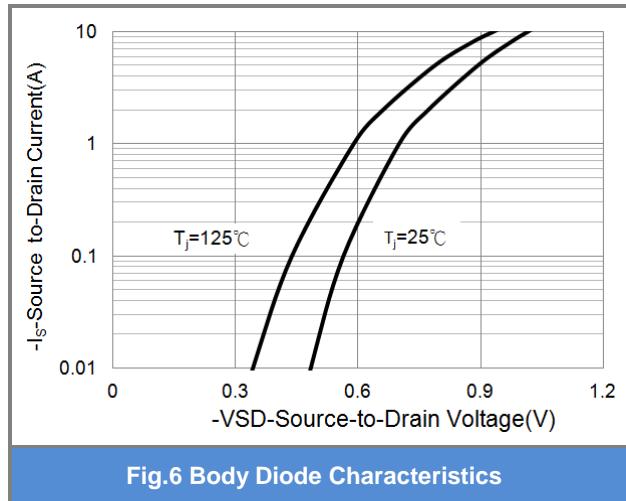
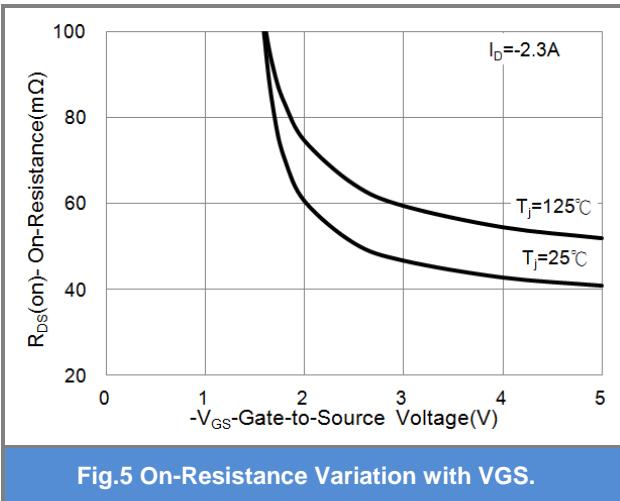
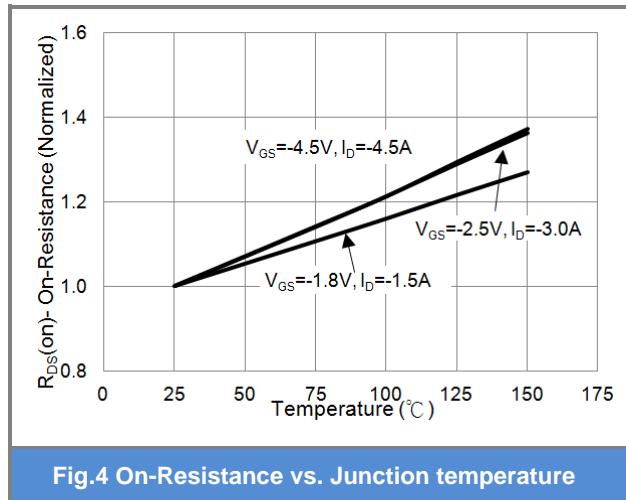
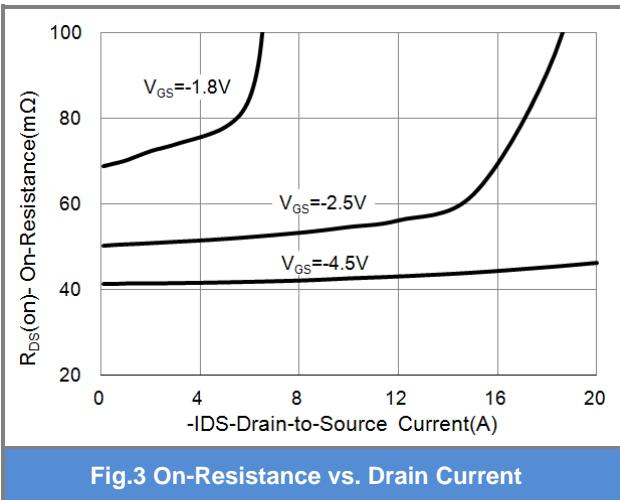
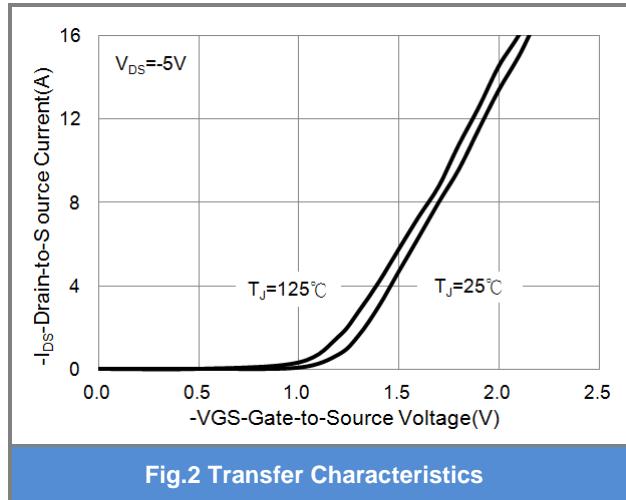
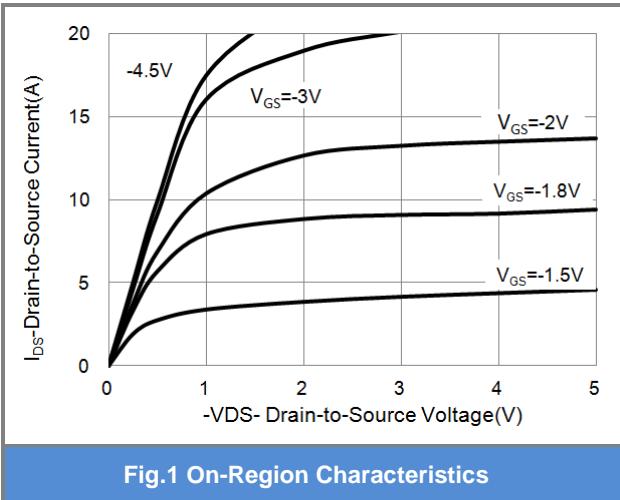
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-20	-	-	V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-0.5	-0.74	-1.3	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-4.5\text{A}$	-	38	46	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}, I_{\text{D}}=-3.0\text{A}$	-	47	56	
		$V_{\text{GS}}=-1.8\text{V}, I_{\text{D}}=-1.5\text{A}$	-	68	88	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=-16\text{V}, V_{\text{GS}}=0\text{V}$	-	-0.01	-1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$	-	± 10	± 100	nA
Dynamic						
Total Gate Charge	Q_g	$V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-4.5\text{A}, V_{\text{GS}}=-4.5\text{V}$ (Note 1,2)	-	10	-	nC
Gate-Source Charge	Q_{gs}		-	1.7	-	
Gate-Drain Charge	Q_{gd}		-	2.4	-	
Input Capacitance	C_{iss}	$V_{\text{DS}}=-10\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHZ}$	-	980	-	pF
Output Capacitance	C_{oss}		-	100	-	
Reverse Transfer Capacitance	C_{rss}		-	81	-	
Switching						
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-10\text{V}, I_{\text{D}}=-4.5\text{A}, V_{\text{GS}}=-4.5\text{V}, R_{\text{G}}=6\Omega$ (Note 1,2)	-	9.8	-	ns
Turn-On Rise Time	t_r		-	54	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	44	-	
Turn-Off Fall Time	t_f		-	31	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_s	---	-	-	-1.5	A
Diode Forward Voltage	V_{SD}	$I_s=-1.0\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.78	-1.2	V

NOTES :

1. Pulse width $\leq 300\text{us}$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. R_{eJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited

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TYPICAL CHARACTERISTIC CURVES



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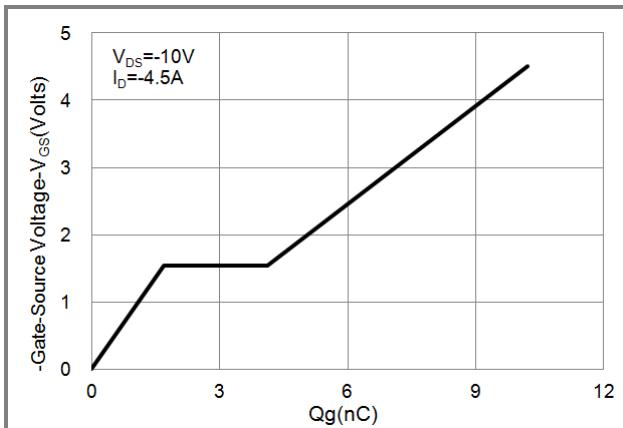


Fig.7 Gate-Charge Characteristics

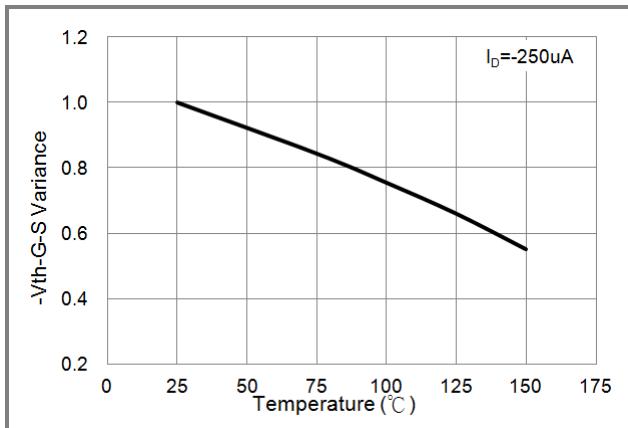


Fig.8 Threshold Voltage Variation with Temperature.

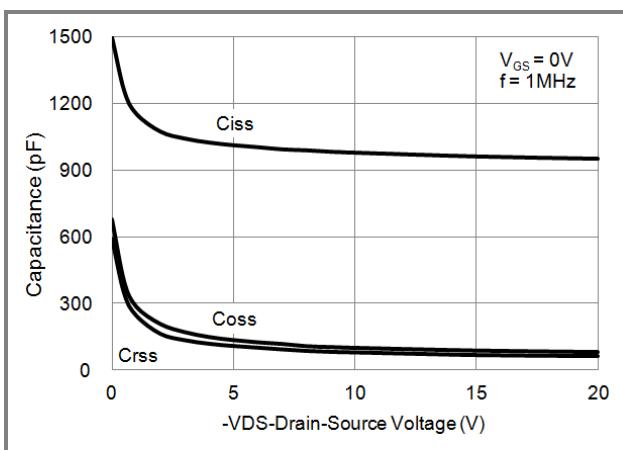


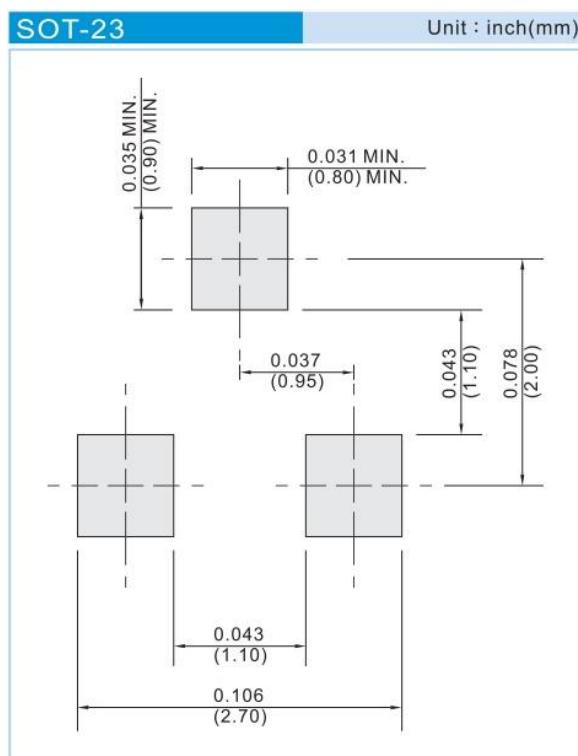
Fig.9 Capacitance vs. Drain-Source Voltage.

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Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJA3415A	SOT-23	3K pcs / 7" reel	A5A
PJA3415A	SOT-23	12K pcs / 13" reel	A5A

MOUNTING PAD LAYOUT



PJA3415A

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