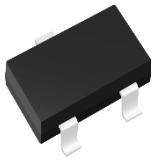
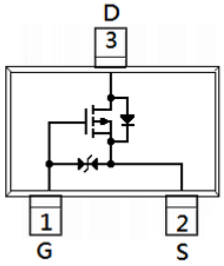
PJA3407E

30V P-Channel Enhancement Mode MOSFET– ESD Protected

Voltage -30 V **Current** -4.0 A

SOT-23





Features

- $R_{DS(ON)}$, V_{GS}@-10V, I_D@-3.7A<53mΩ
- $R_{DS(ON)}$, V_{GS}@-4.5V, I_D@-2.8A<80mΩ
- ESD Protected
- Advanced Trench Process Technology
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0084 grams

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current(Note 4)	T_A=25°C	I_D	-4.0	A
	T_A=70°C		-3.2	
Pulsed Drain Current(Note 1)	T_A=25°C	I_{DM}	-20	
Power Dissipation	T_A=25°C	P_D	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient(Note 5)		$R_{\theta JA}$	100	°C/W

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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=-250uA	-30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=-250uA	-1.3	-1.8	-2.3	
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=-10V, I_D=-3.7A	-	42	53	mΩ
		V_{GS}=-4.5V, I_D=-2.8A	-	60	80	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-24V, V_{GS}=0V	-	-	-1	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	-	-	±10	uA
Dynamic(Note 6)						
Total Gate Charge	Q_g	V_{DS}=-24V, I_D=-3.7A, V_{GS}=-10V(Note 2,3)	-	10.3	-	nC
Gate-Source Charge	Q_{gs}		-	1.3	-	
Gate-Drain Charge	Q_{gd}		-	2.7	-	
Input Capacitance	Ciss	V_{DS}=-24V, V_{GS}=0V, f=1MHZ	-	390	-	pF
Output Capacitance	Coss		-	60	-	
Reverse Transfer Capacitance	Crss		-	36	-	
Gate resistance	R_g	f=1.0MHZ	-	6	-	Ω
Turn-On Delay Time	$td_{(on)}$	V_{DS}=-24V, I_D=-3.7A, V_{GS}=-10V, R_G=3Ω (Note 2,3)	-	3	-	ns
Turn-On Rise Time	t_r		-	25	-	
Turn-Off Delay Time	$td_{(off)}$		-	15	-	
Turn-Off Fall Time	t_f		-	23	-	
Drain-Source Diode						
Diode Forward Current	I_S	T_A=25°C	-	-	-4	A
Diode Forward Voltage	V_{SD}	I_S=-1A,V_{GS}=0V	-	-0.8	-1	V
Reverse Recovery Time	Trr	V_{GS}=0V, I_S=-3.7A dIs/dt=100A/us(Note 2,3)	-	120	-	ns
Reverse Recovery Charge	Qrr		-	226	-	nC

Note：

1. Pulse width≤300us, Duty cycle≤2%.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=150℃.Ratings are based on low frequency and duty cycles to keep initial T_J =25℃.
4. The maximum current rating is package limited.
5. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 $inch^2$ with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES

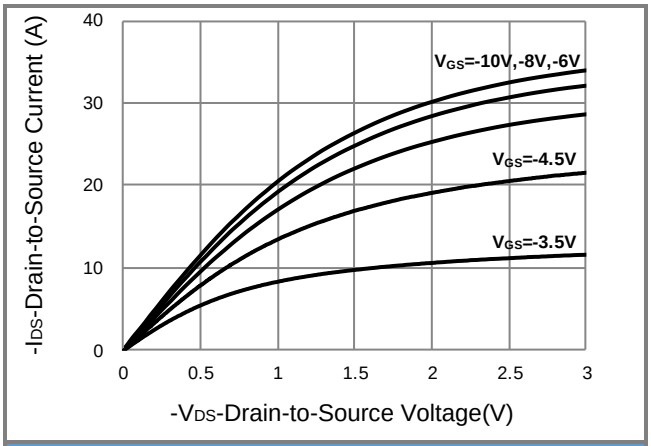

Fig.1 Output Characteristics

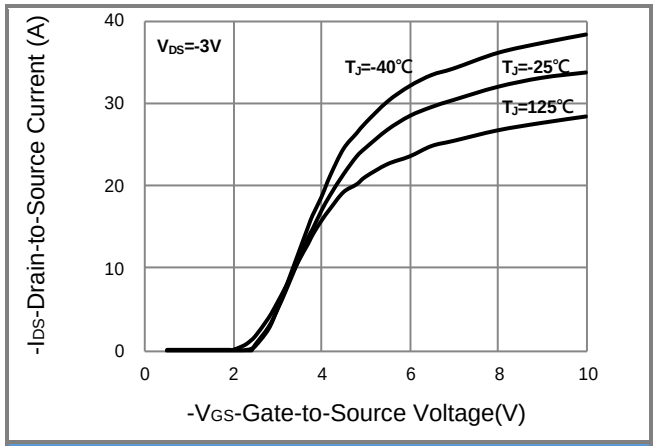

Fig.2 Transfer Characteristics

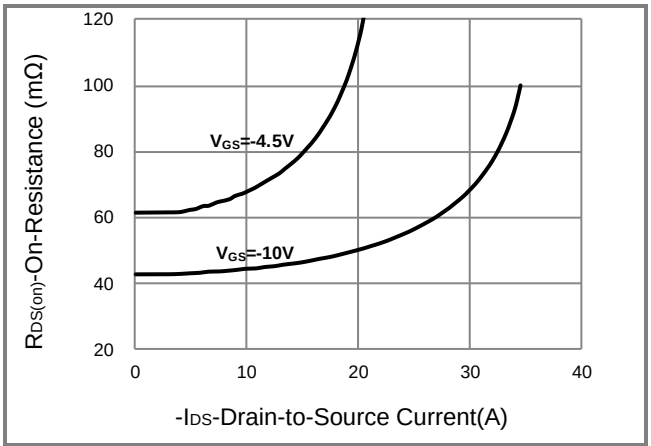

Fig.3 On-Resistance vs. Drain Current

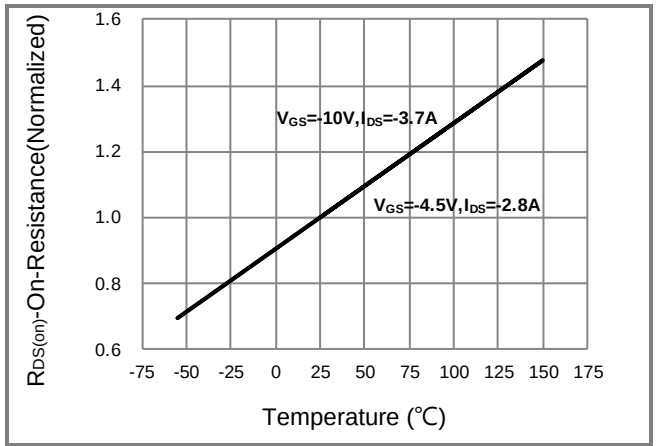

Fig.4 On-Resistance vs. Junction temperature

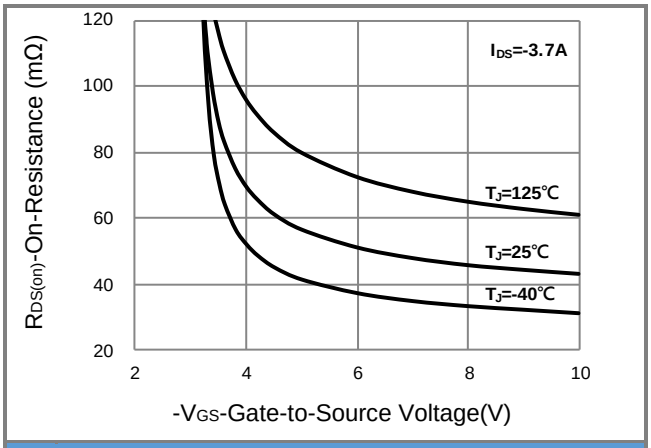

Fig.5 On-Resistance Variation with V_{GS}

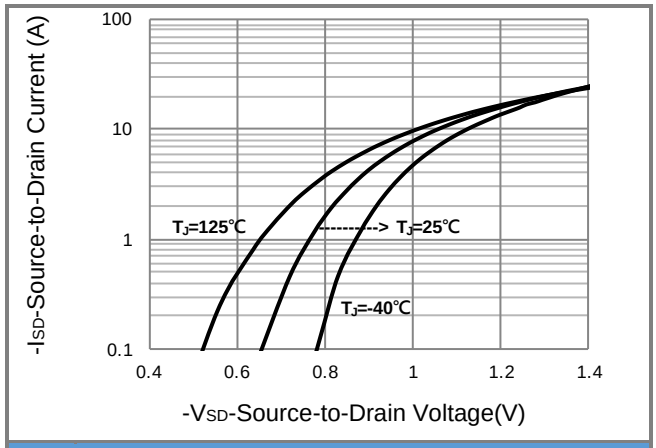

Fig.6 Source-Drain Diode Forward Voltage

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TYPICAL CHARACTERISTIC CURVES

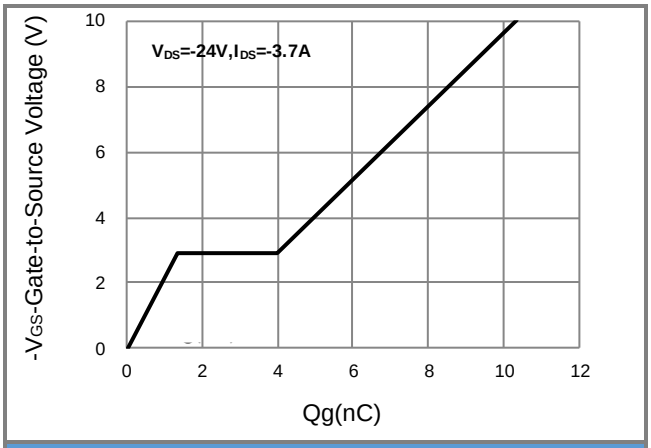


Fig.7 Gate-Charge Characteristics

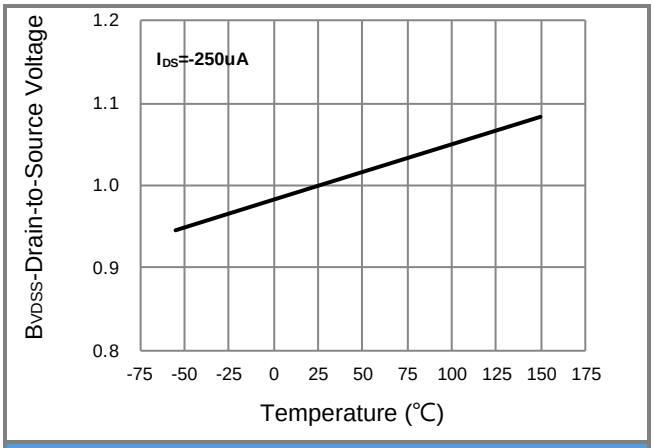


Fig.8 Breakdown Voltage Variation vs. Temperature

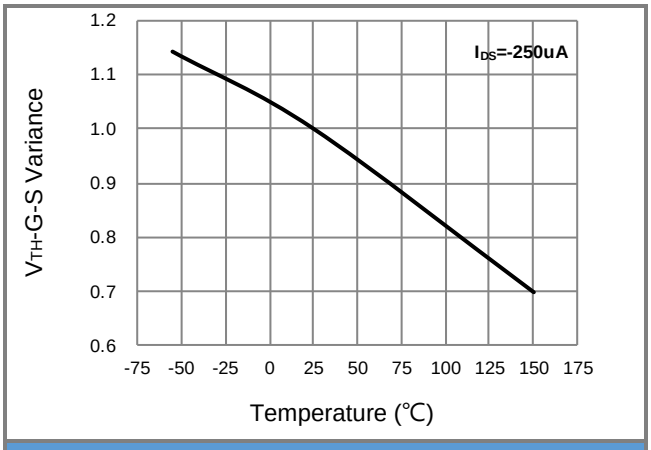


Fig.9 Threshold Voltage Variation with Temperature

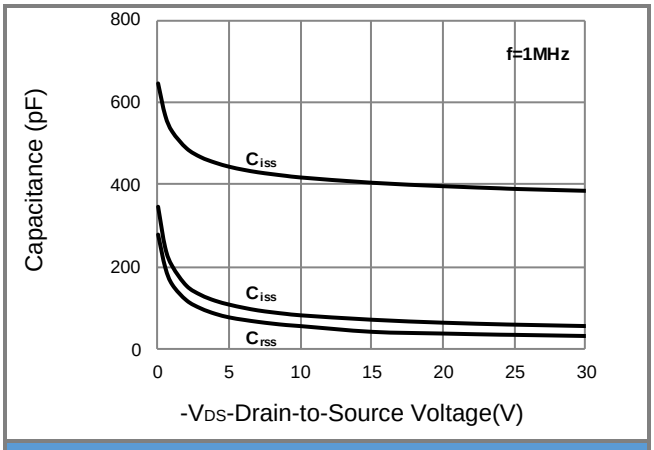


Fig.10 Capacitance vs. Drain-Source Voltage

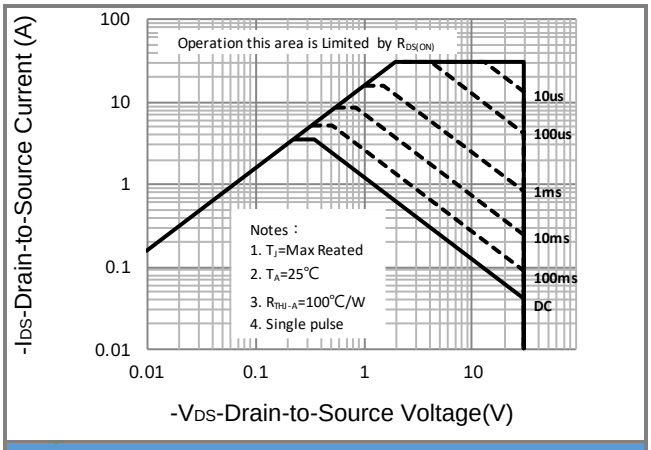


Fig.11 Maximum Safe Operating Area

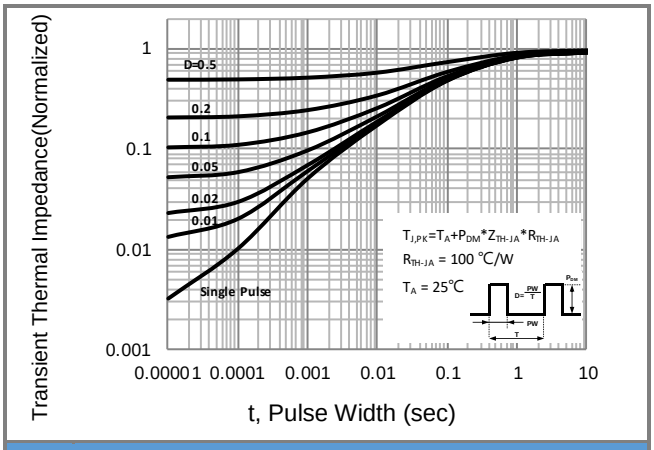


Fig.12 Normalized Transient Thermal Impedance

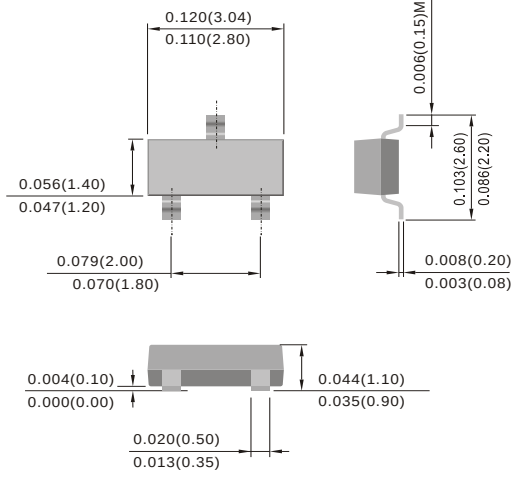
PJA3407E

Product and Packing Information

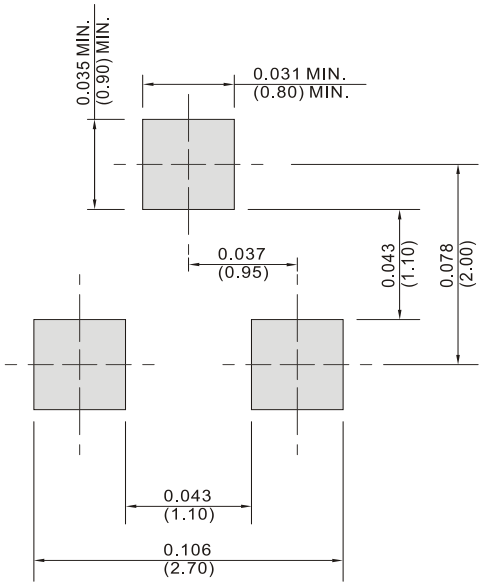
Part No.	Package Type	Packing Type	Marking
PJA3407E	SOT-23	3K pcs / 7” reel	07E

Packaging Information & Mounting Pad Layout

SOT-23 Dimension — Unit: inch(mm)



SOT-23 Pad Layout — Unit: inch(mm)



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