

PJA3404-AU

30V N-Channel Enhancement Mode MOSFET

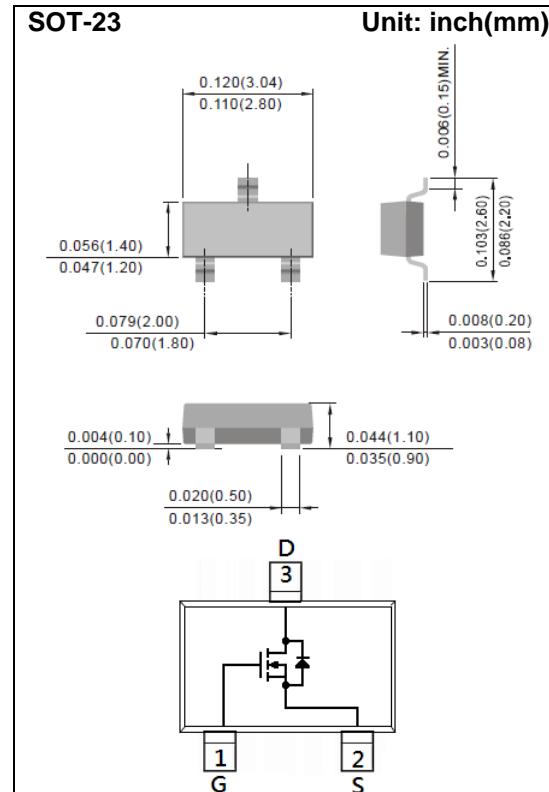
Voltage 30 V Current 5.6A

Features

- RDS(ON) , VGS@10V, ID@5.6A<30mΩ
- RDS(ON) , VGS@4.5V, ID@3.5A<45mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- Acquire quality system certificate : TS16949
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.
(Halogen Free)

Mechanical Data

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams
- Marking: A04



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	5.6	A
Pulsed Drain Current	I_{DM}	22	A
Power Dissipation	$T_a=25^\circ\text{C}$	1.25	W
		10	mW/ $^\circ\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ\text{C}$
Typical Thermal Resistance - Junction to Ambient (Note 3)	$R_{\theta JA}$	100	$^\circ\text{C/W}$

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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	30	-	-	V
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1.0	1.33	2.1	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=5.6\text{A}$	-	27	30	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=3.5\text{A}$	-	39	45	
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	0.01	1	μA
Gate-Source Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	± 10	± 100	nA
Dynamic						
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=15\text{V}, \text{I}_D=5.6\text{A}, \text{V}_{\text{GS}}=10\text{V}$ (Note 1,2)	-	7.8	-	nC
Gate-Source Charge	Q_{gs}		-	1.2	-	
Gate-Drain Charge	Q_{gd}		-	1.5	-	
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=15\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{f}=1.0\text{MHZ}$	-	343	-	pF
Output Capacitance	C_{oss}		-	48	-	
Reverse Transfer Capacitance	Crss		-	34	-	
Switching						
Turn-On Delay Time	$\text{td}_{(\text{on})}$	$\text{V}_{\text{DD}}=15\text{V}, \text{I}_D=5.6\text{A}, \text{V}_{\text{GS}}=10\text{V}, \text{R}_g=3\Omega$ (Note 1,2)	-	3	-	ns
Turn-On Rise Time	tr		-	40	-	
Turn-Off Delay Time	$\text{td}_{(\text{off})}$		-	38	-	
Turn-Off Fall Time	tf		-	39	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_s	---	-	-	1.5	A
Diode Forward Voltage	V_{SD}	$\text{I}_s=1.0\text{A}, \text{V}_{\text{GS}}=0\text{V}$	-	0.77	1.2	V

NOTES :

1. Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited

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TYPICAL CHARACTERISTIC CURVES

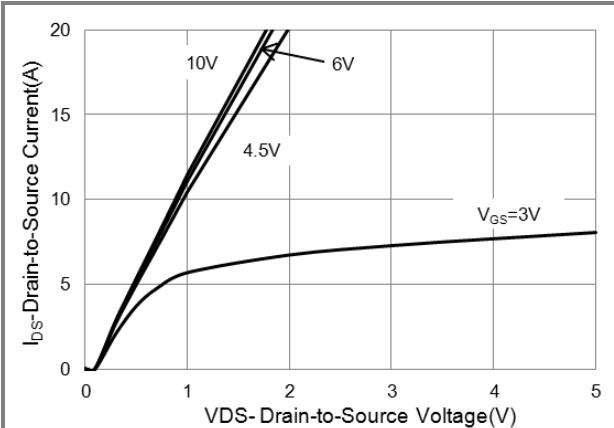


Fig.1 On-Region Characteristics

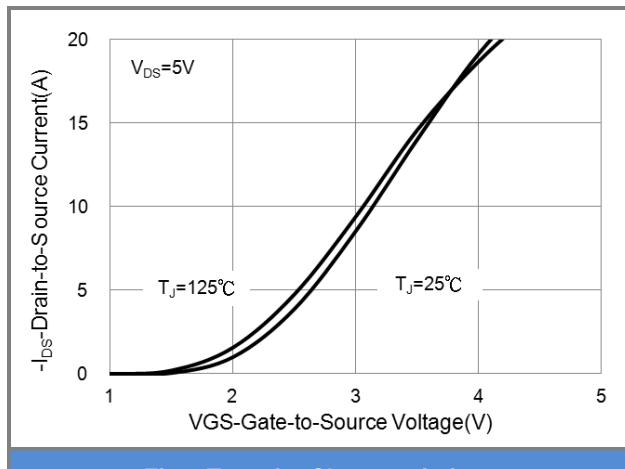


Fig.2 Transfer Characteristics

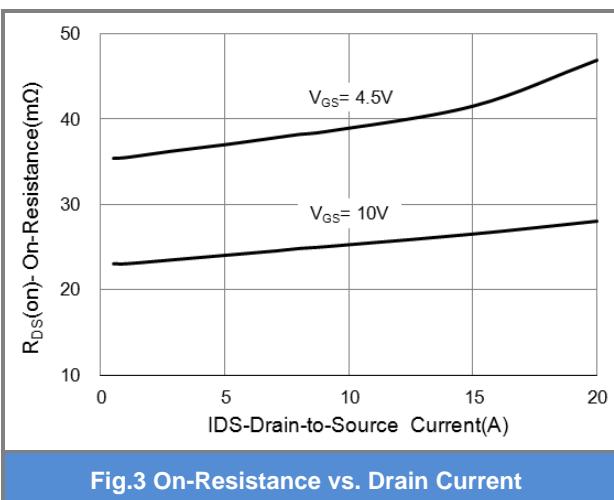


Fig.3 On-Resistance vs. Drain Current

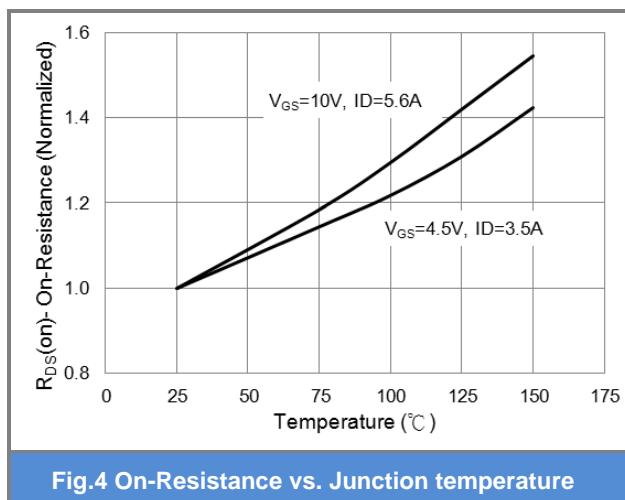


Fig.4 On-Resistance vs. Junction temperature

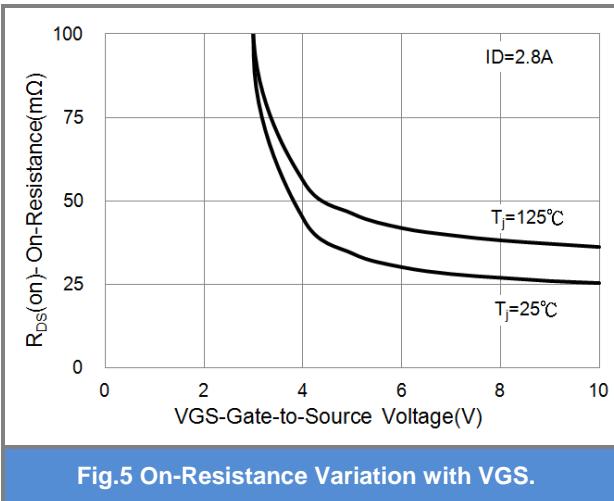


Fig.5 On-Resistance Variation with VGS.

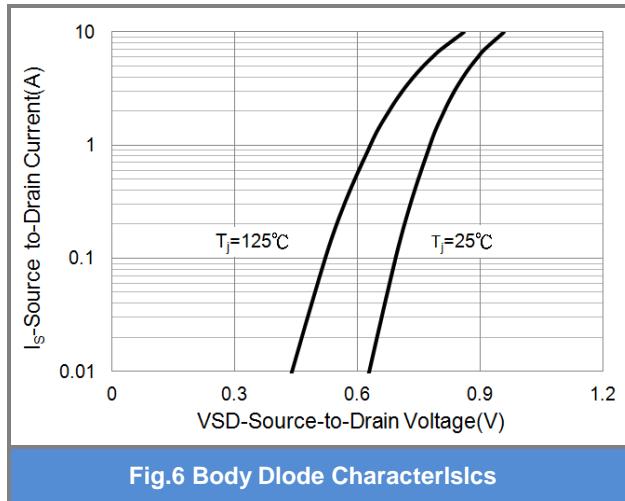
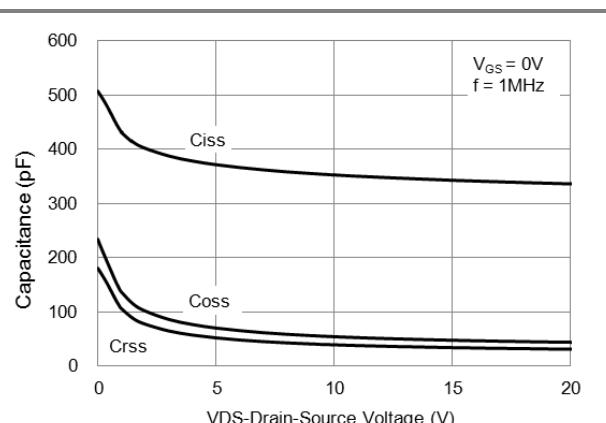
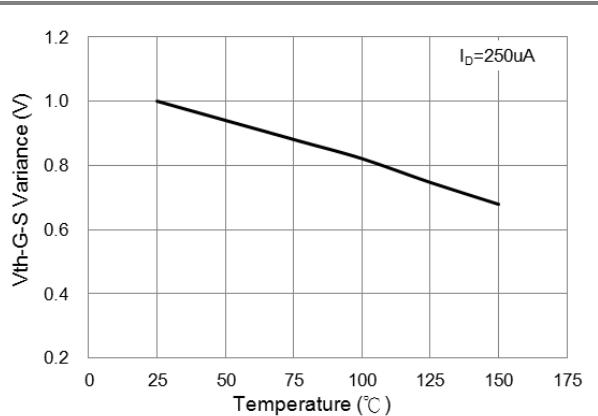
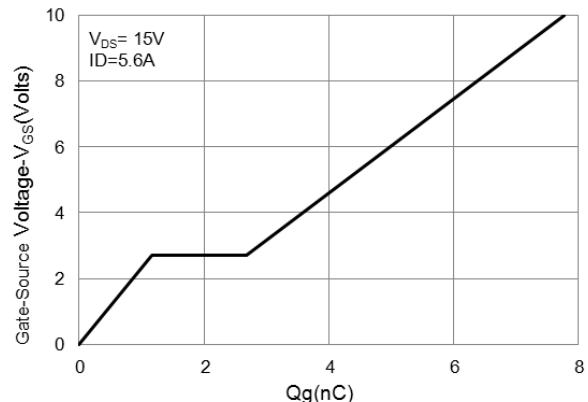


Fig.6 Body Diode Characteristics

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TYPICAL CHARACTERISTIC CURVES

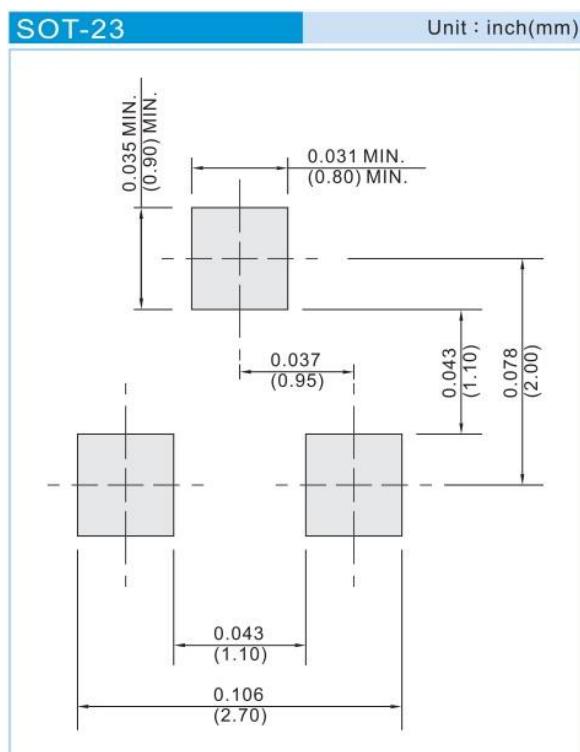


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Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJA3404-AU	SOT-23	3K pcs / 7" reel	A04
PJA3404-AU	SOT-23	12K pcs / 13" reel	A04

Mounting Pad Layout



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